



SRI MANAKULA VINAYAGAR
ENGINEERING COLLEGE
(AN AUTONOMOUS INSTITUTION)



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

*7th UG - Board of Studies Meeting in the Department of
Electronics and Communication Engineering*

for the Programme

B.Tech. – Electronics and Communication Engineering

Venue

**Seminar Hall,
Department of Electronics and Communication Engineering
Sri Manakula Vinayagar Engineering College
Madagadipet, Puducherry – 605 107**

Date & Time

February 29, 2024, at 10.30 am.

BOARD OF STUDIES MEETING

The Seventh Board of Studies meeting for UG, PG, and Research programmes was held on 29th February 2024 at 10:30 am in Seminar Hall, Department of Electronics and Communication, Sri Manakula Vinayagar Engineering College, Puducherry.

MEMBERS OF THE BOARD OF STUDIES

1. Head of the Department concerned (Chairperson).

Dr. P. Raja, Professor and Head

2. Faculty members of the Department.

Name and Designation	Specialization
Dr. R. Ramya, Professor	Communication Systems
Dr.A.Jayachitra, Professor	Control and Instrumentation
Dr. R.Kurinjimalar, Professor	Mobile Satellite Communication
Dr. J.Pradeep, Associate Professor	Image Processing
Dr.S.B.Lenin, Associate Professor	Wireless Communication, VLSI Design
Dr.P.Arunagiri, Associate Professor	Wireless Communication
Dr.PremKumar.S, Associate Professor	Wireless Communication
Dr.N.Saranya, Assistant Professor	Antennas and Microwave Engineering
Dr.S.Dhivya, Assistant Professor	Digital Image Processing
Dr.R.Vijayarani, Assistant Professor	Wireless Communication
Dr.Vankayala Chethan, Prakash, Assistant Professor	Wireless Communication
Mrs.S.Jayanthi, Assistant Professor	VLSI Design
Mrs.V.Logisvary, Assistant Professor	Applied Electronics
Mr.J.Soundararajan, Assistant Professor	Digital Communication and Network Engineering.
Mr.M. Harikrishnan, Assistant Professor	Image Processing
Mr.S.Pushparaj, Assistant Professor	VLSI Design
Navaneethakrishnan. V.M, Assistant Professor	Embedded Systems
Mrs.P.Srividdhya, Assistant Professor	Image Processing
Mr.P.Mahendra Peruman, Assistant Professor	VLSI Design
Mrs.R.Gayathri, Assistant Professor	Signal Processing
Mr.S.Ganesan, Assistant Professor	Electrical Drives and Control
Mr.R.Kalaiyaran, Assistant Professor	Wireless Communication
Mr.S.Mohanram, Assistant Professor	Embedded Systems
Mr.K.Jambulingam, Assistant Professor	Wireless Communication
Ms.R.Priya, Assistant Professor	Digital Image Processing
Ms.K.Archana, Assistant Professor	Applied Electronics
Ms.B.Menaga, Assistant Professor	Applied Electronics
Mr.P. Suseendar, Assistant Professor	Embedded Systems
Mrs.T.S.Krishnapriya, Assistant Professor	Embedded Systems
Mr.S.Yuvaraj, Assistant Professor	VLSI Design

Mrs.S.Anbarasi, Assistant Professor	Wireless Communication
Mr.Sundar Mohan, Assistant Professor	Image Processing
Mrs.Kavitha.K, Assistant Professor	Communication Systems
Mrs.Tamilselvi D, Assistant Professor	Communication Technology
Invited Members	
Dr.T.Gayathri, Professor	Graph Theory
Dr.T.Jayavarthanan, Professor	Vibrational Spectrography
Dr.S.Savithri, Professor	Environmental Chemistry
Dr.P.Rajeswari, Associate Professor	Indian Writing in English

3. Two subject experts from outside the parent University are nominated by the Academic Council

Dr.J.Charles Pravin, Professor, Department of ECE Kalasalingam Academy of Research and Education Krishnankoil- 626126 9566368171 charles@klu.ac.in	Dr.V.J.K.Kishore Sonti, Professor, School of Electrical and Electronics, Sathyabama Institute of Science and Technology, Chennai. 9043536557 drkishore.sonti@gmail.com
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4. One expert is nominated by the Vice-Chancellor from a panel of six recommended by the Autonomous College Principal as a University Nominee

Dr. M. Jagannath
Professor, School of Electronics Engg.
Vellore Institute of Technology
Vellore. 988438626
agan.faith@gmail.com

5. One representative from the Industry/Corporate sector/allied areas nominated by the principal.

Er.R. Murali Srinivasan,
Principal Engineer, Western Digital Corporation
Prestige Park, Prestige Excelsior,
Building No. 143/1, Amani Belandur Kane Village
Varthur Hobli, Bangalore - 560103.
9731324679
muralisrivathsav@gmail.com

6. One member of the College alumni to be nominated by the principal.

S. Krishnakumar,
Test Engineer (Cloud Architect),
Wipro Technologies,
Survey No - 602/03, SEZ Ave,
ELCOT SEZ, Shollinganallur,
Chennai, TN, India-600119.
9791317100
krishnakumars2508@gmail.com

7. Experts from outside the Autonomous College, whenever special courses of studies are to be formulated, to be nominated by the principal.

Prof. R. Nakkeeran
Professor, Department of Electronics Engineering
School of Engineering & Technology
Pondicherry University

AGENDA OF THE MEETING

BoS /2024 /UG/ECE 7.1	
	To review and confirm the minutes of the Sixth BoS meeting held on 21 st July 2023
BoS /2024 /UG/ECE 7.2	
	To appraise the uniqueness of the Regulations 2023 <ul style="list-style-type: none">● Credit distribution.● Course structure● Pattern of question papers● Course assessment process
BoS /2024 /UG/ECE 7.3	
	To discuss and approve the Regulations Curriculum and Syllabi of the third and fourth semesters for the B.Tech – Electronics and Communication Engineering students admitted from the academic year 2023-24. <ul style="list-style-type: none">● Professional Core Courses● Professional Elective Courses
BoS /2024 /UG/ECE 7.4	
	To appraise and approve the professional and open electives chosen by the students in semesters – IV, VI, and VIII under Regulations 2020
BoS /2024 /UG/ECE 7.5	
	To appraise and approve the Employability Enhancement Courses and Skill Development Courses chosen by the students in semesters II, IV, and VI under Regulations 2020
BoS /2024 /UG/ECE 7.6	
	To appraise the measures for the improvement of the standards of teaching and research <ul style="list-style-type: none">● Graduation details● Publications – Papers and Patents● Student Participation in various events● Internship● Placement Details● MoUs Signed
BoS /2024 /UG/ECE 7.7	
	To discuss and recommend the panel of examiners for question paper setting and evaluation process.
BoS /2024 /UG/ECE 7.8	
	Any other items with the permission of the chair

MINUTES OF THE MEETING

Dr. P. Raja, Chairman of the BoS, opened the meeting with a warm welcome to all the members newly constituted for the BoS as per UGC Regulations 2023. He also expressed gratitude to them for their participation in the invitation to the 7th meeting of BoS on the program of B.Tech – Electronics and Communication Engineering.

The Board chairperson briefed about the Institutional credentials, Awards, Recognition, Centre of Excellences, etc., to the new BoS members. Also briefed about the Department of Electronics and Communication Engineering

The Board Chairman briefed the activities of the BoS Meeting held so far by the new members.

BoS Meeting Number	Date	Curriculum Approval Details
1	17-07-2020	Sem-I and Sem-II under Regulation 2020: UG programme Sem-I and Sem-II under Regulation 2020: PG programmes
2	10-04-2021	Sem-III and Sem-IV under Regulation 2020: UG programme Sem-III and Sem-IV under Regulation 2020: PG programmes
3	28-08-2021	Sem-V and Sem-VI under Regulation 2020: UG programme
4	26-02-2022	Sem-VII under Regulation 2020: UG programme
5	17-09-2022	Sem-VIII under Regulation 2020: UG programme
6	21-07-2023	Sem-I and Sem-II under Regulation 2023: UG programme Sem-I and Sem-II under Regulation 2020: PG programmes

Subsequently, the Chairman proceeded with the meeting and discussed the agenda items.

BoS /2024 /UG/ECE 7.1

To review and confirm the minutes of the Sixth BoS meeting held on 21st July 2023

The Sixth BoS Meeting for B.Tech.- Electronics and Communication Engineering under Regulations 2023 was held on 21-07-2023. During this meeting, the members provided valuable suggestions regarding the new curriculum and syllabi presented under the Regulations 2023.

The Board Chairman presented the action taken report from the sixth BoS meeting and submitted the minutes of the sixth BoS meeting for confirmation.

S. No	Sem	Title of Course	Unit	Suggestions	Action Taken
1	I	Circuits and Networks	-	This course is suggested to be moved to semester I because its content is almost a prerequisite for all the core courses.	The course is moved to Sem - I

S. No	Sem	Title of Course	Unit	Suggestions	Action Taken
2	II	Electron Devices	-	It is suggested that the course be shifted from semester I to semester II because students may find it more comfortable to take it after completing the Circuits and Networks course.	The course is moved to Sem - II
3	II	Electron Devices	III	The basic concepts of NMOS and PMOS transistors are essential for understanding CMOS devices.	Included these topics
4	II	Electron Devices Laboratory	-	The concept of EDA tools should be introduced in the Electron Devices Laboratory to gain more insights into the device characteristics.	Included the EDA tools

Minutes are Reviewed and Confirmed

BoS /2024 /UG/ECE 7.2

To appraise the uniqueness of the Regulations 2023

- Credit distribution.
- Course structure
- Pattern of question papers
- Course assessment process

- The Board Chairman explained the Autonomous Regulations 2023 which includes the credit distribution, course structure, continuous assessment, and end-semester question paper pattern as well as the assessment process to the members of the board. Even though, it was approved in the 6th BoS meeting the above information was shared with the new members to know the regulations before the course details and its assessment.

Credit Distribution

S. No	Category	Credits per Semester								Total credits
		I	II	III	IV	V	VI	VII	VIII	
1	Humanities and Social Sciences including Management courses	3	5	1	1	2			3	15
2	Basic Science courses	7	4	5	4					20
3	Engineering Science courses	8	8	4	4	4				28
4	Professional core courses	4	4	13	11	8	15	11		66
5	Professional Elective courses				3	3	3	3	6	18
6	Open Electives					3	3	3		9
7	Project work and internship					1	1	3	8	13
8	Ability Enhancement Courses*									
9	Mandatory Courses*									
Total Credits		22	21	23	23	21	22	20	17	169

Highlights of Regulations 2023

- Theory cum Practical Courses
 - Theory cum Practical Courses from Sem I to VI except Sem V
- Honours Degree
 - The student who has secured more than 8 CGPA in III-semester can register for an Honours Degree
 - The student has to earn an additional 18-20 credits by registering for an honors course
 - 5 additional courses offered from Sem IV to V for getting an Honors Degree out of which one may be an NPTEL course based on student choice.
- Micro and Mini Projects in the Semester V and VI
 - Micro and Mini Projects are added with Existing Project Phase-I and Project phase-II.
- UHV-II and Research Methodologies are compulsory courses for all Engineering.
- SDG-related courses are introduced as non-credit mandatory courses.

Question paper pattern.

Theory Courses – CAT 1 and 2

2 Marks Question	5 Marks Question	10 Marks Question	Total Marks
5	4	2 (Out of 3 Questions)	50

Model and ESE

2 Marks Question	5 Marks Question	10 Marks Question	Total Marks
10	5	3 (Out of 5 Questions)	75

Course Assessment

Theory Courses

Assessment	Continuous Assessment Marks (CAM)					End Semester Examination (ESE) Marks	Total Marks
	CAT – 1	CAT -2	Model	Assignment	Attendance		
Marks	5	5	5	5	5	75	100

Theory cum Practical Courses

All theory cum practical courses shall be assessed as follows. The weightage for the theory part is 60% and for the practical part is 40%.

Continuous Assessment Marks (CAM) – Maximum of 50 Marks										ESE (Theory)	Total Marks
Continuous Assessment (Theory)					Continuous Assessment (Practical)						
CAT 1	CAT 2	Model	Attend	Total	Conduction of Practical	Report	Viva	Total	ESE (Practical Internal Evaluation)		
5	5	5	5	20*	15	10	5	30*		75	-
*To be weighted for 10 marks				10	*To be weighted for 10 marks			10	30	*To be weighted for 50 marks	100

Final End semester practical exam to be conducted with internal and external examiners assigned by the Head of the Institution and HoD.

Continuous Assessment Test (CAT)

S. No	Test	Portions	Marks	Duration	Weightage for internal marks
1	CAT – 1	2 units	50	1 Hr 30 Min	5
2	CAT – 2	2 units	50	1 Hr 30 Min	5
3	Model	All 5 units	75	3 Hr	5
Continuous Assessment Test Marks					15

Attendance Marks

Attendance carries 5 marks, and the distribution of marks is as follows:

- 5 marks for 95% and above
- 4 marks for 90% and above but below 95%
- 3 marks for 85% and above but below 90%
- 2 marks for 80% and above but below 85%
- 1 mark for 75% and above but below 80%

Assignment Marks

- Assignments carry 5 marks.
- The assignment should be in the form of Application-oriented / Problem-solving / Design / Analytical content beyond the syllabus.

Approved and Recommended to the Academic Council

BoS /2024 /UG/ECE 7.3

To discuss and approve the Regulations Curriculum and Syllabi of the third and fourth semesters for the B.Tech – Electronics and Communication Engineering students admitted from the academic year 2023-24.

- Professional Core Courses
- Professional Elective Courses

The board chairman presented the curriculum and syllabi of the third and fourth semesters under Regulation 2023 to the board members.

Semester – III

Sl. No.	Course Code	Course Title	Category	Periods			Credits	Max. Marks		
				L	T	P		CAM	ESM	Total
Theory										
1	U23MATC03	Probability and Statistics	BS	3	1	0	4	25	75	100
2	U23ADTC01	Programming in Python	ES	3	0	0	3	25	75	100
3	U23ECT302	Electronic Circuits	PC	3	0	0	3	25	75	100
4	U23ECT304	Sensors and Its Applications	PC	3	0	0	3	25	75	100
5	U23ECT305	Engineering Electromagnetics	PC	3	0	0	3	25	75	100
Theory cum Practical										
6	U23ECB301	Signals and Systems	PC	2	0	2	3	50	50	100

Practical										
7	U23ENPC01	General Proficiency - I	HS	0	0	2	1	50	50	100
8	U23MAPC01	Engineering Mathematics Laboratory	BS	0	0	2	1	50	50	100
9	U23ADPC01	Programming in Python Laboratory	ES	0	0	2	1	50	50	100
10	U23ECP303	Electronic Circuits Laboratory	PC	0	0	2	1	50	50	100
Ability Enhancement Course										
11	U23ECC3XX	Certification Course – III	AEC	0	0	4	-	100	-	100
12	U23ECS301	Ability Enhancement Course–I: PCB Design	AEC	0	0	2	-	100	-	100
Mandatory Course										
13	U23ECM303	Climate Change	MC	2	0	0	-	100	-	100
Total							23	675	625	1300

The board members provided the following suggestions:

- The application of Electron Devices are given in Unit-I in the course of Electronic Circuits, however the same was studied in Semester – II. The unit – I of in Electronic Circuits course will be released by it is BJT and MoSFET biasing with its stability.
- The members suggested that the course “Measurement and Instrumentation” offered in Semester – III may be offered as one of Professional Elective - I.
- It is suggested that the “Engineering Electromagnetics” theory cum practical course is to be a professional core course because it is a prerequisite for Communication related courses.
- The course “Signals and Systems” is recommended as Theory cum Practical course because the students get confident about the signals and systems while studying theory as well as practical as single course.

The courses were modified according to the members' suggestions and provided in Annexure – I

Semester – IV

Sl. No.	Course Code	Course Title	Category	Periods			Credits	Max. Marks		
				L	T	P		CAM	ESM	Total
Theory										
1	U23MATC04	Numerical Methods and Optimization	BS	3	1	0	4	25	75	100
2	U23CSTC03	Data Structures	ES	3	0	0	3	25	75	100
3	U23ECT406	Operational Amplifiers and Applications	PC	3	0	0	3	25	75	100
4	U23ECT407	Digital Circuits	PC	3	0	0	3	25	75	100
5	U23ECE4XX	Professional Elective – I	PE	3	0	0	3	25	75	100
Theory cum Practical										
6	U23ECB402	Analog Communication	PC	2	0	2	3	50	50	100
Practical										
7	U23ENPC02	General Proficiency -II	HS	0	0	2	1	50	50	100
8	U23CSPC02	Data Structures Laboratory	ES	0	0	2	1	50	50	100
9	U23ECP404	Integrated Circuits Laboratory	PC	0	0	2	1	50	50	100
10	U23ECP405	Digital Circuits Laboratory	PC	0	0	2	1	50	50	100
Ability Enhancement Course										
11	U23ECC4XX	Certification Course – IV	AEC	0	0	4	-	100	-	100

12	U23ECS302	Ability Enhancement Course-II: Repair and Maintenance of Electronics Equipment	AEC	0	0	2	-	100	-	100
Mandatory Course										
13	U23ECM404	Right to Information and Good Governance	MC	2	0	-	-	100	-	100
Total							23	675	625	1300

The board members provided the following suggestions:

- Analog Communication is suggested to be introduced as a Theory cum Practical course in the 4th semester.

All the suggestions are considered and provided in Annexure - II

Professional Elective – I

Professional Elective –I (Offered in Semester IV)		
Sl. No.	Course Code	Course Title
1	U23ECE401	Transmission Lines and Waveguides
2	U23ECE402	Computer Architecture
3	U23ECE403	Computer Networks
4	U23ECE404	Biomedical Electronics
5	U23ECE405	Measurement and Instrumentation

- Instead of Aircraft Communication in the Professional Elective-I, the suggestion is to replace it with Transmission Lines and Waveguides.
- The Suggestion is to rename the Professional Elective-I course Computer Architecture and Interfacing to Computer Architecture is proposed.
- Similarly, renaming the Professional Elective-I course Data Networks to Computer Networks.
- The member suggested that the Professional Elective course System on Chip Design be moved to a higher semester. Instead, a Professional Elective-I course Biomedical Electronics will be introduced in its place.

All the suggestions are considered and provided in Annexure - III

Approved with Minor corrections and Recommended to the Academic Council

BoS /2024 /UG/ECE 7.4

To appraise and approve the professional and open electives chosen by the students in semesters – IV, VI, and VIII under Regulations 2020

The Board Chairperson submitted the details of the students who opted for Professional and Open electives from Semesters IV, VI, and VIII under Regulations 2020.

Semester – IV

Category	Course Code	Name of the Course	No. of Students
Professional Elective - III	U20ECE401	Computer Networks	62
	U20ECE402	Sensors for Industrial Applications	61
	U20ECE403	Computer Architecture	60
	U20ECE404	PLC and SCADA Systems and its Applications	61
Open Elective - I	U20CSO401	Web Development	62
	U20CCO401	Basic DBMS	60
	U20ITO402	R Programming	60
	U20ADO402	Introduction to Data Science	62

Semester –VI

Category	Course Code	Name of the Course	No. of Students
Professional Elective - III	U20ECE611	Low Power VLSI Design	54
	U20ECE613	Nano-electronics and Devices	52
	U20ECE612	Aircraft Communication and Navigation Systems	120
Open Elective - III	U20CSO604	Graphics Designing	53
	U20ITO604	Mobile App Development	60
	U20CCO604	Web Programming	53
	U20ADO603	Principle of Artificial Intelligence and Machine Learning	60

Semester –VIII

Category	Course Code	Name of the Course	No. of Students
Professional Elective - V	U20ECE823	Virtual and Augmented Reality	106
	U20ECE822	Machine Learning for Wireless Communication	51
	U20ECE825	Real-Time Systems	60
Open Elective - VI	U20ECE826	VLSI for Communication Systems	60
	U20ECE827	5G Wireless Communication Systems	60
	U20ECE828	Biomedical Electronics	48
	U20ECE830	Hardware-Software Co-Design	49

As per the guidelines, the following details are submitted to the members of BoS for kind perusal.

Approved and Recommended

BoS /2024 /UG/ECE 7.5**To appraise and approve the Employability Enhancement Courses and Skill Development Courses chosen by the students in semesters II, IV, and VI under Regulations 2020**

The Board Chairman introduced the Employability Enhancement Courses and Skill Development Courses chosen by students from semesters II, IV, and VI under Regulations 2020.

Members Appreciated the Employability Enhancement Courses and Skill Development Courses offered by the students from semesters II, IV, and VI.

Year / Sem	Course Code	Course Title	No. of students
I / II	U23ECC274	Python Programming	224
II / IV	U20ECC459	IoT using Python	122
II / IV	U20ECC461	Machine Learning	122
III / VI	U20ECC660	Java Programming	102
III / VI	U20ECC662	Machine Learning and Deep Learning	49
III / VI	U20ECC659	IoT using Python	75

Approved and Recommended

BoS /2024 /UG/ECE 7.6**To appraise the Measures for the improvement of the standards of teaching and research**

- Graduation details
- Events organized.
- Publications – Papers and Patents
- Student Participation in various events
- Internship
- Placement Details
- MoUs Signed

- The Board Chairman provided an overview of the activities undertaken in the department to enhance the standards of teaching and research during the academic year 2022-2023.

Graduation Details

Year	No. of students admitted	No. of students graduated
2018- 2022	172	170
2019 - 2023	210	200

Events organized.

No. of Expert Lecture	Industrial Visit	Skilled based Workshop
18	8	4

Publications – Papers and Patents

- Number of Publications (International Journal) : 31
- Number of Publications (International Conference) : 31
- Patent : 10

Student Participation in various events

- Paper presentations : 49
- Technical Events : 75
- Robo Events : 33
- Other Events : 10

Internship : 16

Placement Details : 154

MoUs Signed : 3

- This included a comprehensive summary of graduation statistics, faculty and student publications, and patents, student engagement in various events, as well as details regarding student internships and placements.
- The members expressed appreciation for the department's efforts and achievements during this period.

Noted and Appreciated

BoS /2024 /UG/ECE 7.7

To discuss and recommend the panel of examiners for question paper setting and evaluation process.

- The Board Chairman presented the details of the panel of examiners responsible for question paper setting and evaluation processes to the board members. Following deliberation, the members approved the panel of examiners.

Approved and Recommended

BoS /2024 /UG/ECE 7.8

Any other items with the permission of the chair

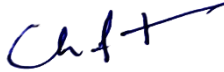
The Board Chairman invited suggestions from the members to enhance the teaching and learning process. The members suggested the following points.

- The BOS members recommended establishing an innovation council and introducing courses related to intellectual property rights (IPR) and entrepreneurship.
- Additionally, they proposed incorporating topics such as IPR laws and constitutional laws into professional elective or open elective courses.
- The members proposed enhancing the incubation process by introducing startup ideas focused on healthcare.
- The members recommended strengthening teaching standards by focusing on core concepts and peripheral topics.

Noted and approved.



Dr. M. Jagannath
Professor, School of Electronics Engg.
Vellore Institute of Technology
Vellore.



Dr. J. Charles Pravin,
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Kalasalingam Academy of Research and
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Dr. V. J. K. Kishore Sonti,
Professor, School of Electrical and Electronics,
Sathyabama Institute of Science and
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Prof. R. Nakkeeran
Professor, Department of Electronics
Engineering
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Er. R. Murali Srinivasan,
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S. Krishnakumar,
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Dr. Gerardine Immaculate Mary
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Vellore, Tamil Nadu, India



Dr. R. RAMYA
Professor/ ECE
(Member)



Dr.A.Jayachitra,
Professor
(Member)



Dr. R. KURINJIMALAR
Professor / ECE
(Member)



Dr. J. PRADEEP
Associate Professor / ECE
(Member)



Dr.S.B.Lenin,
Associate Professor
(Member)



Dr.P.Arunagiri,
Associate Professor
(Member)



Dr.PremKumar.S,
Associate Professor
(Member)



Dr.N.Saranya,
Assistant Professor
(Member)



Dr.S.Dhivya,
Assistant Professor
(Member)



Dr.R.Vijayarani,
Assistant Professor
(Member)



Dr.Vankayala Chethan Prakash,
Assistant Professor
(Member)



Mrs.S.Jayanthi,
Assistant Professor
(Member)



Mrs.V.Logisvary,
Assistant Professor



Mr.J.Soundararajan,
Assistant Professor

(Member)



Mr.M. Harikrishnan,
Assistant Professor
(Member)



Navaneethakrishnan.V.M,
Assistant Professor
(Member)



Mr.P.Mahendra Peruman,
Assistant Professor
(Member)



Mr.S.Ganesan,
Assistant Professor
(Member)



Mr.S.Mohanram,
Assistant Professor
(Member)



Ms.R.Priya,
Assistant Professor
(Member)



Ms.B.Menaga,
Assistant Professor
(Member)

(Member)



Mr.S.Pushparaj,
Assistant Professor
(Member)



Mrs.P.Srividhya,
Assistant Professor
(Member)



Mrs.R.Gayathri,
Assistant Professor
(Member)



Mr.R.Kalaiyarasan,
Assistant Professor
(Member)



Mr.K.Jambulingam,
Assistant Professor
(Member)



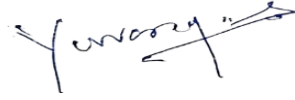
Ms.K.Archana,
Assistant Professor
(Member)



Mr.P. Suseendar,
Assistant Professor
(Member)



Mrs.T.S.Krishnapriya,
Assistant Professor
(Member)



Mr.S.Yuvaraj,
Assistant Professor
(Member)



Mrs.S.Anbarasi,
Assistant Professor
(Member)



Mr.Sundar Mohan,
Assistant Professor
(Member)



Mrs.Kavitha.K,
Assistant Professor
(Member)



Mrs.Tamilselvi D,
Assistant Professor
(Member)



Dr.T.Gayathri,
Professor
(Member)



Dr.T.Jayavarthan,
Professor
(Member)



Dr.S.Savithri,
Professor
(Member)



Dr.P.Rajeswari,
Associate Professor
(Member)