

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)

Department of Electrical and Electronics Engineering

Minutes of 4th Meeting of BoS (UG)

Venue

Seminar Hall,

Department of EEE,

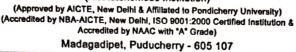
Sri Manakula Vinayagar Engineering College

Date & Time

22nd February, 2022 at 10:00 A.M



SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE (An Autonomous Institution)





DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of 4th Meeting of Board of Studies (UG)

The Fourth meeting of Board of Studies in Electrical and Electronics Engineering Department was held on 22nd February 2022 at 10:00 A.M in the Seminar Hall, Department of EEE, Sri Manakula Vinayagar Engineering College, with Head of Department in the Chair.

The following members were present for the BoS meeting

SI. No.	Name of the Member	Designation
Head of	the Department (Chairman)	
1	Dr. S. Anbumalar, M.E., Ph.D., Professor and Head Specialization: Control System Years of Experience: 29 years Sri Manakula Vinayagar Engineering College saravanan.anbumalar@gmail.com 9443179533	Chairman
The ent	ire faculty of each specialization	08.4
2	Dr. P. Jamuna, M.E., Ph.D., Professor Specialization: Power Electronics and Drives Years of Experience: 16 Sri ManakulaVinayagar Engineering College jamuna1981@gmail.com 9789544379	Member
3	Dr. D. Raja, M.Tech., Ph.D., Professor Specialization: Electrical Drives and Control Years of Experience: 15 Sri Manakula Vinayagar Engineering College rajaapeee@gmail.com 9944337970	Member
4	Dr. K. Gowrishankkar, M. Tech., Ph.D., Professor Specialization: Instrumentation and control Years of Experience: 16 Sri Manakula Vinayagar Engineering College gowri200@yahoo.com 9095555412	Member
5	Dr. S. Ganesh Kumaran, M.E., Ph.D., Associate Professor Specialization: Electrical Machines Years of Experience:10 Sri Manakula Vinayagar Engineering College ganeshphd4u@gmail.com 9677624378	Member

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	SURPLY STATES AND ADDRESS AND	Member
_	Dr. T. Gayathri Professor, Dept. of Mathematics, SMVEC	oracles and the second
6	Professor, Dept. of Methods	Member
-	Dr. K. Kathikeyan Associate Professor, Dept. of Chemistry, SMVEC	Charge and the second s
7	Associate Professor, Dept.	Member
^	Mrs. G. Namita Associate Professor, Dept. of English, SMVEC	
8	Associate Professor, Dept. of 279	Member
	Dr. P. Jayavardhane	Monipel
9		and by the Academic Cou
	Associate Professor Dept. of Physics, SMVEC, Madagadipet-605107 Diect experts from outside the Parent University nominal of the Physics of	ted by the Academic Cou
wo sul	pject experts from outside the	
	Dr. I Kanadarai, W.L., · ······	
	Professor & Head (In charge) Specialization: Control System	
		Subject Expert
10	Years of Experience.22 PSG College of Technology (Autonomous)	
	Coimbatore – 641 004.	and the Kindley But I
	Jkr.eee@psgtech.ac.in	
	04436 54496	
	Dr. P. Lakshmi, M.E., Ph.D.,	THE REPORT OF THE PARTY OF THE
	m f	THE PARTY OF THE P
	Specialization: Electrical Engineering	Subject Expert
		Subject Export
11	College of Engineering Guindy, Arma Shirts Stay	
	Chennai 600 025.	
	p_lakshmi@annauniv.edu	
	9444266117 pert nominated by the Vice-Chancellor from a panel of si	x recommended by the
One ex	pert nominated by the vice-chances	PALLEGATION FROM THE PARTY OF T
college	principal. Dr. A. Kavitha, M.Tech., Ph.D	. is obstantially
	Professor	
	Specialization: Electrical Engineering	in the set of the property
	- Functiones 22	Subject Expert
12	College of Engineering Guindy, Anna University,	serge constant of the service of
	Chennai-600025	to plant was and
	akavitha@annauniv.edu,	1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1. 1
	9444388778	lating to placement.
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One re	presentative from industry/corporate sector/ailled area realer. S. Selva Kumar, B.Tech.	plating to placement. Member
One re	presentative from industry/corporate sector/ailled area re Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited	
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Rengaluru	Member
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Rengaluru	Member
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001	Member
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001 Destgraduate meritorious alumnus nominated by the Challege	Member
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001 estgraduate meritorious alumnus nominated by the Challeroval of the principal of the college Er. K. Ramraj, M.Tech Technical Director,	Member
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001 ostgraduate meritorious alumnus nominated by the Challeroval of the principal of the college Er. K. Ramraj, M.Tech Technical Director, Specialization: Power Electronics	Member rman, Board of Studies, v
13 One po	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001 estgraduate meritorious alumnus nominated by the Chair proval of the principal of the college Er. K. Ramraj, M.Tech Technical Director, Specialization: Power Electronics Years of Experience:8	Member
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Agenda of the Meeting	
Agenda 1/ BoS/ 4 /2022 /EEE /UG	Confirmation of minutes of 3 rd meeting of BoS and the Curriculum Structure of B.Tech Electrical and Electronics Engineering of R-2019 and R-2020 Regulations – Modifications if any
Agenda 2 / BoS/ 4 /2022 /EEE /UG	To discuss modifications in the syllabi of IV year, VII and VII semesters, under Autonomous Regulations R-2019 for the B.Tech-Electrical and Electronics Engineering students admitted in the Academic Year 2019-20.
Agenda 3 / BoS/ 4 /2022 /EEE /UG	To discuss modifications in the syllabi of II and III years (IV to V semester), under Autonomous Regulations R-2020 for the B.Tech - Electrical and Electronics Engineering students admitted from the Academic Year 2020-2021.
Agenda 4 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the Academic Calendar for odd/ever Semester of Academic year 2021-22. The classes commenced from 21.01.2022 in online mode and in offline mode for all the years from 02.02.2022 onwards.
Agenda 5 / BoS/ 4 /2022 /EEE /UG	To approve the professional and open Elective courses offered to the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.
Agenda 6 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the on-line SWAYAM/MOOCS course offered for the III year/ VI semester students under R-2011 regulations during the period January 2022 to May 2022.
Agenda 7 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the Certification courses offered for the year and III year students under R-2019 and R-2020 regulation during the period January 2022 to May 2022.
Agenda 8 / BoS/ 4 /2022 /EEE /UG	To discuss and recommend the panel of examiners to the Academic Council
Agenda 9 / BoS/ 4 /2022 /EEE /UG	To discuss various Research activities in the department Implementation of AICTE-MODROB during the period 2021-2023. Patents Publication and submission Submission of research proposals
	Journal Paper publications and submission

Department of EEE - Fourth Meeting of BoS

Any other additional points to be discussed with the permission of Chair.

Agenda 10 / BoS/ 4 /2022 /EEE /UG

Minutes of the Meeting

Dr. S. Anbumalar, Chairman, BoS opened the meeting by welcoming the external Dr. S. Anbumalar, Chairman, Bos opening thereafter deliberated on agenda items that members, the internal members and the meeting thereafter deliberated on agenda items that had been approved by the Chairman.

Agenda 1/ BoS /4 /2022 /EEE /UG

Confirmation of minutes of 3rd meeting of BoS and the Curriculum Structure of B.Tech Confirmation of minutes of 3 Heeting of R-2019 and R-2020 Regulations - Modifications if

chairman, BoS, apprised the minutes of 3rd BoS, its implementation and then it is confirmed with the approval in 4th BoS meeting.

Agenda 2/ BoS /4 /2022 /EEE /UG

To discuss modifications in the syllabi of IV year, VII and VIII semesters, under Autonomous Regulations R-2019 for the B.Tech - Electrical and Electronics Engineering students admitted in the Academic Year 2019-20.

The modifications to be carried out in the syllabi of IV year, VII and VIII semesters (R2019 Regulations) were discussed and the following suggestions are given by BoS members.

S. No.	Regulations	Semester	Course Name with Code	Unit	Changes incorporated
1	R2019	VII	Industrial Automation and Control U19EET71	٧	loT based experiments ar included in the lab course Hence, Unit V has change as Introduction to IoT
2	R2019	VII	Industrial Automation and Control Lab U19EEP72		Two IoT based experiment are included by replacing the SCADA experiments.
3	R2019	VII	Communication	. 11	The following topics wer additionally present in UNII II and they can be removed OOK Systems, OPSY QAM, MSK, GMSK
	12015	Professional Elective	Engineering U19EEE71	III	The following topics wer additionally present in UNIT III and they can be removed Source coding: Shaum Fao, Huffman coding noiseless coding The following topics were additionally and the coding codina coding co
4	R2019	VII Professional Elective	SMPS and UPS U19EEE75	1	theorem The topic Cascaded Book Converters is not required UNIT- I and it can be

The above corrections are incorporated and the Syllabi (Given in Annexure- I) $a^{r\theta}$ approved by the BoS members.

Agenda 3/ BoS /4 /2022 /EEE /UG

To discuss modifications in the syllabi of II and III years (IV to VI semester), under Autonomous Regulations R-2020 for the R Total Autonomous Regulations R-2020 for the B.Tech - Electrical and Electronics Engineering students admitted from the Academic Year 2020 2021 students admitted from the Academic Year 2020-2021. The modifications to be carried out in the syllabi of II & III years, IV to VI semesters (R2020 Regulations) were discussed and the following asset as III years, IV to VI semesters (R2020 Regulations)

Regulations) were discussed and the following suggestions are given by BoS members.

S. No.	Regulations	Semester	Course Name with Code	Unit	Changes incorporated
		-		Instru Engin few to	content in Measurement and mentation for Electrical eering Course is vast and hence, pics need to be removed as per aggestions given below. Systematic and random errors,
				1	propagation of errors, Limiting errors of instruments has to be removed.
					Principle of operation, construction, Torque equation, types, testing and Calibration using direct and
				II	phantom loading has to be removed. Testing, application of measuring CT and VT has
=					to be removed by retaining Instrument Transformers. Decibel meters - Q meter - tan-
1	R2020	IV	Measurement and Instrumentation for Electrical Engineering U20EET411	Ш	delta meter - Modulation index meter - Sampling theory and its applications in current, voltage, power, energy measurements has to be removed.
			3-13-2		The following topics has to be removed Owens and Heavy side Campbell bridges
				IV	 De Sauty bridges localization of cable faults by Murray and Varley loop test - Methods of reducing
- * "		*		1 3 ⁷¹ - 1	bridge errors - Wagner Earthing Device. Suggested to include Schering Bridge.
					The following topics has to be removed stroboscope, gyroscope –
3° - 1				V	Force: Strain gauge – Torque: magnetostricitive, Position: synchro- Transmitter and receiver. Pressure: Manometers, Bourdon.
2	R2020	IV	Microprocessor and Microcontroller U20EET412	I, II, III, IV, V	 Students faced difficulties in understanding both 8-bit and 16-bit processors under R-2019 regulations. Hence, the Units are rearranged only using 8-bit processor.
3	R2020	IV	Measurements and Instrumentation Lab U20EEP408	- 2	 Experiment 2(b): Measurement of inductance and Q-factor using Owen Bridge can be replaced by Maxwell Bridge Experiment 9: Measurement of strain, Load and Level using strain gauges has to be removed

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					Experiment 10: Measurement of torque and pressure using strain gauges has to be removed
4	R2020	IV	Microcontroller and its Applications Lab U20EEP409		 Experiments based on 16 bit processor has to be removed and suggested to include the experiment "Relay interfacing with PIC microcontroller".
5	R2020	V	Power Electronics U20EET513	1	Included the topic TRIAC
6	R2020	VI	Renewable Energy Sources	Cour	content in Renewable Energy se is vast and hence, few topics to be removed as per the estions given below. Suggested to remove the terms radiation, Extra- terrestrial, Spectral distribution, Solar constant, solar radiation on earth, measurements
			U20EET617	II	Suggested to remove the terms power extracted - wind distribution and speed prediction - wind map of India, Fundamentals — types of machines and their characteristics, Case study on Wind power generation using micro wind turbine for residential purpose.
7	R2020	VI	Power System Analysis	1	Suggested to remove Power Scenario in India. Suggested to remove the term
		,	U20EET618	Y	Contingency selection and ranking for the Power system.
8	R2020	IV Profession	Energy Storage Technology	1	for Portable Energy, Demand and Scale Requirements. Suggested to remove Shaped
		al Elective	U20EEE405	11, 111	Metal Batteries Removed Case Study on Hybridization of different Energy Storage Devices

The above corrections are incorporated and the Syllabi (Given in Annexure- II) are approved by the BoS members.

Agenda 4/ BoS /4 /2022 /EEE /UG

To discuss and approve the Academic Calendar for odd/even Semester of Academic year 2021-22. The classes commenced from 21.01.2022 in online mode and in offline mode for all the years from 02.02.2022 onwards.

The Academic Calendars are prepared for this semester and it includes the schedule for CAT, Model Exam, QCM, Project review and Internal Marks distributions were discussed and approved (given in Annexure-III)

- The classes are scheduled in the online mode for II, III years from 21.01.2022 to 01.02.2022.
- As per Government order, the classes for all years had commenced from 02.02.2022 onwards in offline mode.

Agenda 5/ BoS /4 /2022 /EEE /UG

To approve the Professional and Open Elective Courses offered to the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.

 The Professional Elective and Open Elective courses opted by III year / VI semester students under R-2019 regulations during the period January 2022 to May 2022 is listed below are approved by the BoS members.

S. No.	Course Name	Course Code
	Professional Elective - III	
1	Smart Grid	U19EEE61
2	Special Electrical Machines	U19EEE63
	Open Elective - III	
1	Mobile App Development	U19ITO64
2	Web Programming	U19CCO64

Agenda 6/ BoS /4 /2022 /EEE /UG

To discuss and approve the on-line SWAYAM/MOOCS courses offered for the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.

- The list of online SWAYAM / MOOCS courses (given in Annexure- IV) offered for III
 year / VI semester students under R-2019 regulations during the period January
 2022 to May 2022 was presented and approved by the BoS members.
- The list of online SWAYAM / MOOCS courses (given in Annexure- IV) offered for the Faculty of EEE department during the period January 2022 to May 2022 was presented and approved by the BoS members

Agenda 7/ BoS /4 /2022 /EEE /UG

To discuss and approve the Certification courses offered for the I, II and III year students under R-2019 and R-2020 regulations during the period January 2022 to May 2022.

 The semester-wise list of Certification courses offered for II and III year students under R-2019 and R-2020 regulations during the period January 2022 to May 2022 was presented and approved by the BoS members.

Semester	Certification Course	
	R-2019 (Batch: 2019 – 2023)	
VI	Artificial Intelligence and Edge computing / CCNA	
	R-2020 (Batch: 2020 – 2024)	-
111	Solar Smart Energy system with IOT	
	R-2020 (Batch: 2021 – 2025)	
1 0	AutoCAD for Electrical	

Agenda 8/ BoS /4 /2022 /EEE /UG

To discuss and recommend the panel of examiners to the Academic Council.

 The list of Question Paper Setters and Evaluators (given in Annexure-V) was presented and recommended by the BoS members to the academic council.

Agenda 9/ BoS /4 /2022 /EEE /UG

To discuss various Research activities in the department

- Implementation of AICTE-MODROB during the period 2021-2023.
- · Patents Publication and submission
- · Journal Paper publications and submission

Department of EEE - Fourth Meeting of BoS

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The efforts taken to improve the Research activities in the department were presented and the BoS noted the Agenda.

- Implementation of AICTE approved MODROB for the Power Electronics and Drives
 Lab with a sanctioned amount of 11 Lakhs is in progress.
- The Department has published 10 design patents and Planned to improve in the forthcoming years.
- Each Faculty in the department is advised to publish one SCI and one Scopus Journal for this semester.

The Fourth meeting of BoS approval was concluded at 12.30 PM by Dr. S. Anbumalar, Chairman, Board of Studies, Department of Electrical and Electronics Engineering, Sri Manakula Vinayagar Engineering College.

SI.No	Name of the Member with Designation and official Address	MEMBERS AS PER UGC NORMS	Signature
1	Dr.S.Anbumalar Professor and Head Department of EEE SMVEC,Madagadipet-605107	Chairman	1982
2	Dr.A.Kavitha Professor, Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (University Nominee)	Laithe
3	Dr. P. Lakshmi Professor, Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (Academic Council Nominee)	P. Jahl.
4	Dr. J. Kanakaraj Professor & Head Department of EEE PSG College of Technology (Autonomous) Coimbatore – 641 004.	Subject Expert (Academic Council Nominee)	t. Kanastfrj
5	Er.S. Selva Kumar Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001	Representative from Industry	8. S.J.L.
6	Er.K.Ramraj Technical Director LED FORSE India Poornankuppam Puducherry – 605 007.	Postgraduate Alumnus (nominated by the Principal)	F. Run Ri
7	Dr. P. Jamuna Professor Department of EEE,SMVEC	Internal Member	Same
8	Dr.D.Raja Professor Department of EEE,SMVEC, Madagadipet-605107	Internal Member	profes

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9	Dr. K. Gowrishankar Professor Department of EEE,SMVEC, Madagadipet-605107		Internal Member	Gh. <u>u</u>
10	Dr.S.Ganesh Kumaran Associate Professor Department of EEE, SMVEC, Madagadipet-605107		Internal Member	S. Brust
11	Dr.T.Gayathri Professor and Head Dept of Mathematics, SMVEC, Madagadipet-605107	* ;	Internal Member	T. 92
12	Dr.K.Kathikeyan Associate Professor Dept. of Chemistry, SMVEC, Madagadipet-605107	di biji di Piji di Marin	Internal Member	and the same
13	Mrs.G.Namita Associate Professor Dept. of English, SMVEC Madagadipet-605107,		Internal Member	N-T
14	Dr. P. Jayavardhane Assistant Professor Dept. of Physics, SMVEC, Madagadipet-605107		Internal Member (Science & Humanity)	1.8.J-

<u> Annexure – I</u> R2019 Syllabi

U19EET71 INDUSTRIAL AUTOMATION AND CONTROL

Hrs 45

Course Objectives

- To apprehend the basic architecture of Industrial automation system.
- To study about the components used in PLC.
- To practice the ladder logic programming of PLC.
- To learn about the building blocks of SCADA.
- To brief about the communication protocols and IoT.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the type of Automation system and its architecture in detail.(K3)
- CO2 Discuss the history of PLC, main parts and its functions. (K3)
- CO3 Illustrate the operation of Relays, contactors, Motor Starters, Switched, Sensors, Output Control Devices, etc., (K3)
- CO4 Acquire knowledge about the operation of SCADA and its sub-systems. (K3)

CO5 - Gain knowledge on fundamentals of IoT. (K3)

UNIT I INTRODUCTION TO AUTOMATION

Automation overview - requirement of automation systems - architecture of industrial automation system -Levels of Automation-basic elements of an automated system - industrial bus systems: modbus and profibus.

UNIT II PROGRAMMABLE LOGIC CONTROLLERS

Introduction to PLC, Principles of Operation - Size and Application. Hardware Components: I/O Section, Discrete /Analog I/O Modules, Special I/O Modules, CPU, Memory Design, Memory Types, Programming Terminal Devices, Recording and Retrieving Data - Processor Memory Organization, Relay-Type Instructions, Instruction Addressing, Branch Instructions, Internal Relay Instructions.

UNIT III LADDER LOGIC PROGRAMMING

PLC Wiring Diagrams and Ladder Logic Programs: Electromagnetic Control Relays, Contactors, Motor Starters, Manual/Mechanical Operated Switches, Sensors, Output Control Devices, Seal-in Circuits, Latching Relays, Converting Relay Schematics into PLC Ladder Programs, Programming Timers: Mechanical Timing Relays, Timer Instructions, On-Delay /Off-Delay Timer Instruction, Retentive Timer, Cascading Timers.

UNIT IV SCADA FUNDAMENTALS

Introduction, Open system: Need and advantages, Building blocks of SCADA systems, RTU-Evolution, Components, Communication, Logic, Termination and Testing and HMI subsystem - Power supplies, Advanced RTU functionalities, IEDs, Data concentrators and merging units.

Master Station: Software /Hardware components, Server systems in the master station, Small, medium, and

UNIT V INTRODUCTION TO IOT

loT fundamentals, IoT Architecture and protocols, Various Platforms, IoT components and Communication Technologies, Challenges in IoT, Case study.

Text Books

- 1. Frank D. Petruzella, "Programmable Logic Controllers", McGraw Hill, 4th Edition , 2011
- 2. Mini S. Thomas, "Power System SCADA and Smart Grids", CRC Press,3rd Edition April 2015.
- S. Mukhopadhyay, S. Sen and A. K. Deb, "Industrial Instrumentation, Control and Automation", Jaico Publishing House, 1st Edition, 2013.
- 4. Jeeva Jose, "Internet of Things", Khanna Publishing House, 1st Edition, 2018.

Reference Books

- 1. Gary Dunning, "Introduction to Programmable Logic Controllers", Cengage Learning, 3rd India Edition.
- Frank lamb, "Industrial Automation: Hands On", McGraw-Hill Education, 1st Edition, 2013.
- T. Huges, "Programmable Logic Controllers", ISA press, 1994.
- William T. Shaw, "Cybersecurity for SCADA systems", Penn Well Books, 2006.
- Arshdeep Bahga, Vijay Madisetti, "Internet of Things: A Hands-on Approach", 1st Edition, 2014 Web References

- 1. https://electrical-engineering-portal.com/download-center/books-and-guides/automation-control/plc-ladder-
- https://www.beckhoff.com/english.asp?start/?pk_campaign=AdWords-AdWordsSearch-IndustrialAutomationEN&pk_kwd=industrial%20automation
- https://www.plantautomation-technology.com/articles/an-overview-of-distributed-control-systems-dcs https://www.controleng.com/articles/scada-remains-relevant-for-industrial-automation/
- https://sw.aveva.com/monitor-and-control/scada Page | 10



COs		Program Outcomes (POs)												Program Specific Outcomes (PSOs)		
	PO1	PO2	PO3	PO4	PO5	PO6	P07	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	
11	3	3	3	3	•	-	-		•	-	-	1	3	3	3	
2	3	3	3	3	-	-	-	· .		-	-	1	3	3	3	
3	3	3	3	3	-	-	-	-	-	-	-	1	3	3	3.	
4	3	3	3	3	-	-	٠.	-	-	-	-	1	3	3	3	
5	3	3	3	3	-	-	-	-	-	-	-	1	3	3	3	

Correlation Level: 1 - Low, 2 - Medium, 3 - High

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INDUSTRIAL AUTOMATION AND CONTROL LAB

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U19EEP72

Course Objectives

- To gain practical knowledge regarding the automation components.
- To perform delay operations using the PLC.
- To gain practical knowledge on interfacing of different sensors, counter, timer, RTD using PLC.
- To equip the students to provide the solution for real time industrial applications.
- To equip the students to develop a fault monitoring system using SCADA.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the ladder logic programs and components used for process control.(K2)
- CO2 Design PLC-relay logic for the real time applications (K3)
- CO3 Implement Industrial processing system. (K3)
- CO4 Design a SCADA monitoring system for real time applications.(K3)
- CO5 Diagnose the fault in Power generation and distribution networks, etc. (K3)

List of Experiments

- 1. Study of basic programming of PLC
- 2. Arithmetic operation, Timer, Counter operation using PLC
- 3. PLC based control of Level Process, Temperature Process, Speed.
- 4. Annunciator system using PLC
- PLC based control of batch process system
- Bottle filling system using PLC
- 7. a. Interfacing of lamp and button with PLC for ON/OFF operation.
 - b. Perform Delayed Operation of Lamp By Using Push Button.
- 8. Combination of Counter and Timer for Lamp ON/OFF operation.
- 9. DOL Starter and Star Delta Starter operation by using PLC.
- 10. Develop/ Execute ladder program for the Control of automatic bottle filling system.
- 11. Develop/ Execute ladder program for Traffic Light Control
- 12. Develop/ Execute ladder program for Reversal of DC Motor Direction
- 13. Develop/ Execute ladder program for Stair case lighting
- 14. IoT based Street light monitoring and control
- 15. IoT based Industrial pollution monitoring system.

Reference Books

- 1. S. Mukhopadhyay, S. Sen and A. K. Deb, "Industrial Instrumentation, Control and Automation", Jaico Publishing House, 1st Edition, 2013.
- 2. Gary Dunning, "Introduction to Programmable Logic Controllers", Cengage Learning, 3rd India Edition, 2007.
- 3. Frank lamb, "Industrial Automation: Hands On", McGraw-Hill Education, 1st Edition, 2013.
- T. Huges, "Programmable Logic Controllers", ISA press, 1994.
- 5. R. Krishnan, "Electric Motor Drives, Modelling, Analysis and Control", Pearson Education India, 1st Edition, 2015.
- Viswanandham, "Performance Modeling of Automated Manufacturing Systems", PHI, 1st Edition, 2009.
- Jose A. Romagnoli, Ahmet Palazoglu, "Introduction to Process control", CRC Taylor and Francis group, 3rd Edition, 2020.

Web References

- https://electrical-engineering-portal.com/download-center/books-and-guides/automation-control/plcladder-sequential-programming
- https://www.beckhoff.com/english.asp?start/?pk_campaign=AdWords-AdWordsSearch-2. IndustrialAutomationEN&pk_kwd=industrial%20automation
- https://www.advantech.com/solutions/ifactory 3.
- https://www.plantautomation-technology.com/articles/an-overview-of-distributed-control-systems-dcs
- https://www.controleng.com/articles/scada-remains-relevant-for-industrial-automation/ 5.
- https://sw.aveva.com/monitor-and-control/scada

COs	4	Program Outcomes (POs)													ecific (SOs)
	PO1	PO2	PO3	PO4	PO5	PO6	P07	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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Front

U19EEE71

COMMUNICATION ENGINEERING

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Course Objectives

- To introduce different methods of analog communication systems and their significance.
- To introduce Digital Communication methods for high bit rate transmission.
- To introduce MAC used in communication systems for enhancing the number of users.
- To explain the various media for digital communication.
- To deliberate the use of Power Lines for communication.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Comprehend the basic Characteristics of the signals and analog modulation techniques. (K2)
- CO2 Comprehend the needs of modulation and various Digital modulation techniques. (K3)
- CO3 Describe multiple access techniques and its applications. (K2)
- CO4 Explain the advanced communication systems. (K2)
- CO5 Explore the role of Communication Engineering in the realization of Smart grids (K2)

UNIT I ANALOG COMMUNICATION

(9 Hrs)

Modulation -need for modulation - AM - Frequency spectrum - vector representation - power relations generation of AM - DSB, DSB/SC, SSB, VSB AM Transmitter and Receiver; FM and PM - frequency spectrum - power relations: NBFM and WBFM, Generation of FM and PM, Armstrong method and Reactance modulator:

UNIT II DIGITAL COMMUNICATION

Pulse modulations: PAM, PWM, PPM - Sampling theorems - quantization, PCM, DM, DPCM - Digital modulations: ASK, FSK, PSK, BSK, applications of Data communication.

UNIT III MULTIPLE ACCESS TECHNIQUES

(9 Hrs)

SS and MA techniques: FDMA, TDMA, CDMA, SDMA application in wire and wireless communication : Advantages (merits)

UNIT IV SATELLITE AND FIBRE OPTIC COMMUNICATION

(9 Hrs)

Orbital aspects-Geostationary satellites-Satellite Uplink - Satellite Downlink - Satellite Transponder - Modulation techniques for satellite links - Satellite Earth station

Principle of light propagation in fibre - Index profiles - Modes of propagation - Losses in fibre -Dispersion -Light sources and detectors- Fibre optic communication link

UNIT V POWER LINE COMMUNICATION

(9 Hrs)

Power Supply networks - Narrowband and Broadband PLC - Structure of PLC access network - PLC network elements - connection to core network - structure campus communication network and performance issues -Architecture of smart grid technology.

Text Books

- 1. Taub & Schiling, "Principles of communication systems", Tata McGraw hill, 2007.
- J.Das, "Principles of digital communication", New Age International, 1986.
- 3. Halid Harasnica, Ralf Lehneri, "Broad band Power line Communications Design", John Wiley and Sons, 2005

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- 1. Simon Haykin, "Communication Systems", Tata McGraw Hill, 4th Edition.
- Kennedy and Davis, "Electronic Communication Systems", Tata McGraw hill, 4th Edition, 1993.
- Sklar, "Digital Communication Fundamentals and Applications", Pearson Education, 2001.
- Baryle, Memuschmidt, "Digital Communication", Kluwer Publication, 2004.
- 5. B.P.Lathi, "Modern Digital and Analog Communication Systems", Oxford University Press, 1998,
- 6. K. Muralibabu, "Communication Engineering", Lakshmi Publications, 2013.

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- 2. nptel.ac.in/courses/IIT-MADRAS/Principles of.../pdfs/1_8.pdf
- 3. yoonc01.tistory.com/attachment/fk10000000032.ppt
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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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SMPS AND UPS

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Course Objectives

- To provide conceptual knowledge of various types of DC DC converters.
- To impart the knowledge on various types of switched mode power converters and its voltage contra
- To understand the importance of Zero voltage and Zero current switching used in resonant converters
- To analyze the PWM techniques and harmonic reduction techniques in DC AC converters.
- To explain the various types of filters and techniques to improve the power quality.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Design the DC-DC converters for different applications. (K2)
- CO2 Analyze, design and select the converters used for switched mode power supplies in Computers, Laptop, and TV. (K4)
- CO3 Describe the importance of resonant Converters in reducing power loss and improving the life time of the power semiconductor device. (K2)
- CO4 Conclude the different voltage and harmonics reduction techniques used for DC-AC converters. (K4)
- CO5 Interpret knowledge on the techniques used to improve the power quality and design of filters for UPS.(K2)

UNIT I DC - DC CONVERTERS

(9 Hrs)

Principles of DC-DC Converters - Analysis and state space modeling of Buck, Boost, Buck- Boost and Cuk converters. Choice of switching frequency - Device Selection - EMI issues

UNIT II SWITCHED MODE POWER CONVERTERS

SMPS Types: Self-Oscillating Flyback, Forward, Push pull, Luo, Half bridge and fullbridge converters- control circuits and PWM techniques - SMPS with multiple outputs - Choice of switching frequency - Device Selection State space modeling.

UNIT III RESONANT CONVERTERS

(9 Hrs)

Introduction- classification - Load Resonant converters - ZVS, ZCS, Clamped voltage topologies- DC link inverters with Zero Voltage Switching- Series and parallel Resonant inverters- Voltage control. Multi energy storage element resonant converters - two, three and four element RPS - Application of Regulated Power Supply.

UNIT IV DC - AC CONVERTERS

(9 Hrs)

Single phase and three phase inverters - control techniques, harmonic elimination techniques - Multilevel inverters -Concepts - Types: Diode clamped, Flying capacitor, Cascaded types; Switched Inductor and Capacitor multilevel Inverter - Applications.

UNIT V POWER CONDITIONERS, UPS AND FILTERS

(9 Hrs)

Introduction- Power line disturbances- Power conditioners -UPS: offline UPS, Online UPS - Filters: Voltage filters, Series-parallel resonant filters, filter without series capacitors, filter for PWM VSI, current filter, DC filters - Design of high frequency inductor and transformer - Selection of capacitor and Batteries

- 1. Simon Ang, Alejandro Oliva, "Power-Switching Converters", CRC Press, 3rd Edition, 2010.
- Kjeld Thorborg, "Power Electronics In theory and Practice", Overseas Press India Private Ltd, 1st Edition,
- 3. M. H. Rashid, "Power Electronics handbook", Elsevier Publication, 4th Edition, 2017.

- Philip T Krein, "Elements of Power Electronics", Oxford University Press, 2nd Edition, 2014.
 Erickson, W. Robert, "Fundamentals of Power Electronics", Springer, 2nd Edition, 2010.
- 3. Joseph Vithayathil, "Power Electronics, Principles and Applications", McGraw Hill Series, 6th Reprint, 2013.
- 4. Ned Ned Mohan, Tore M. Undeland, William P. Robbins, "Power Electronics: -Converters, Applications, and Design", John Wiley and sons Publication, 3rd Edition, 2010.
- 5. Fang Lin Luo, "Advanced DC/AC converters: Applications in renewable Energy", CRC press, 1st Edition, 2013.

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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Annexure – II

R2020 Syllabi

U20EET411

MEASUREMENTS AND INSTRUMENTATION FOR **ELECTRICAL ENGINEERING**

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Course Objectives

- To give the students an insight into the constructional details and working principles of various measuring instruments.
- To provide the use of different types of analog and digital meters for measuring electrical and physical quantities.
- To demonstrate various Bridges for the measurement of resistance, inductance and capacitance.
- To provide the procedure to calibrate an energy meter.
- To understand and apply different types of sensors for the measurement of physical quantities such as speed, torque, pressure, displacement, temperature, etc.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Acquire knowledge on the characteristics of measuring instruments and their classification.(K2)
- CO2 Conversant in construction, working of A.C / D.C meters and their proficient use. (K3)
- CO3 Acquire knowledge in various methods of digital meters and its measurement. (K3)
- CO4 Acquire knowledge on construction and working principle of various types of display devices and bridge comparison methods for R, L and C measurement. (K3)
- CO5 Demonstrate the various types of transducers used for physical measurements. (K3)

UNIT I INTRODUCTION TO MEASUREMENT

(9 Hrs)

Functional elements of Generalized measurement system - Types of measurement - Classification of instruments - Static and Dynamic characteristics of instruments - Mean, Standard deviation - error - Accuracy, Precision, Sensitivity, Linearity, Resolution, Hysteresis, Threshold, Input impedance - loading effects -Probability of errors - Errors in Measurements.

UNIT II ELECTRICAL INSTRUMENTS

Essential requirements of an instrument - Ammeter and voltmeter - Moving coil - Moving Iron - Extension of voltmeter and ammeter range - Electro dynamo meter type Wattmeter - Induction type Energy meter Measurement of active and reactive powers in balanced and unbalanced systems - Instrument Transformers -Construction, phasor diagrams - Magnetic measurements - Determination of B-H curve and measurements of iron loss.

UNIT III DIGITAL INSTRUMENTS

Digital Volt Meter and its design - Voltage ratio measurement techniques - Digital ohmmeter, capacitance meter - impedance meters (Polar and Cartesian types) - Signal analyzers: spectrum and logic analyzers - Digital Frequency Meter - Measurement of Frequency - Study of Phasor Measurement Units (PMU).

UNIT IV BRIDGES AND DISPLAY DEVICES

(9 Hrs)

Bridges: Measurement of resistances - D.C potentiometer - Wheat stone, Kelvin and Kelvin's Double bridge -A.C bridges for measurement of L and C - Maxwell, Anderson, Hay, Wein and Schering bridges - Measurement of earth resistance.

Display Devices: CRT display, analog and digital CRO, LED, and LCD.

UNIT V TRANSDUCERS

(9 Hrs)

Transducers - Definition and classification - Linear Displacement: Resistive Potentiometers, strain gauge, LVDT, Capacitive Piezoelectric - Rotational Displacement: magnetic, Position: synchro Transmitter and receiver - speed: Magnetic and photo electric pickup transducer - Temperature: Thermistors, thermocouple - Flow Electromagnetic, Ultrasonic - Level: DP cell, Ultrasonic - Density: Hydrometer - Voltage, current and power Hall Effect transducer

Text Books

- 1. A.K. Sawhney, "A Course in Electrical & Electronic Measurements and Instrumentation", Dhanpat Rai and Co., New Delhi, 19th Edition, 2015.
- J. B. Gupta, "A Course in Electronic and Electrical Measurements", S. K. Kataria & Sons, Delhi, 12th Edition,
- 3. E. O. Doebelin and D. N. Manik, "Measurement Systems Applications and Design", Tata McGraw Hill Education Pvt. Ltd., Special Indian Edition, 2007.

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- David Bell, "Electronic Instrumentation and Measurements", Oxford University Press, 1st Edition, 2013.
 A. J. Bouwens, "Digital Instrumentation", Tata McGraw Hill Publications, 16th Reprint Edition, 2008.
 H.S. Kalsi, "Electronic Instrumentation", Tata McGraw Hill Education, 4th Edition, 2019.
 C.S. Rangan, G.R. Sharma and V. S. V. Mani, "Instrumentation Devices and Systems", Tata McGraw Hill Proceedings of the Computation of the Book Co., New Delhi, 1st Edition, 2004.

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- https://nptel.ac.in/courses/108/105/108105153/
- 3. http://www.nptelvideos.in/2012/11/industrial-instrumentation.html
- 4. http://vlabs.iitkgp.emet.in/asnm/
- https://www.youtube.com/watch?v=xLjk5DrScEU
- 6. http://www.wisegeek.com/what-are-transducers.htm-

COs/POs/PSOs Mapping

COs	kes'				•	ram Oı		•	•				Outco	ram Spe omes (P	SOs)
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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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U20EET412

MICROPROCESSOR AND MICROCONTROLLER



Course Objectives

- To get familiar with basic architecture and programming techniques of microprocessor 8085.
- To learn interfacing of memory and data transfer techniques using microcontroller.
- To understand the interfacing of input/output devices required for real time applications.
- To introduce the basic concepts of embedded system design using microcontroller.
- To equip the student with ability to design PWM control for various application such as AC-DC, DC-DC

Course Outcomes

After completion of the course, the students will be able to

- CO1 Illustrate the architecture of microprocessor and to develop skills in writing assembly language
- CO2 Have a clear understanding of microcontroller architecture with functional details of each pin.(K3) CO3 - Write and debug Assembly and C programs for 8 bit Microcontroller.(K3)

CO4 - Interface input/output peripheral devices and to implement the advanced communication protocol like 1°C

CO5 - Design and develop microcontroller based real-time applications. (K4)

UNIT I ARCHITECTURE AND PROGRAMMING OF 8085 MICROPROCESSOR

8085 Microprocessor: Architecture, Addressing modes, Instruction set - Assembly language programs Machine cycles and Timing diagrams. Application: Interfacing of stepper motor control with 8085

UNIT II INTRODUCTION TO PIC16F MICROCONTROLLER

Microprocessor and Microcontroller difference - RISC and CISC programmer's model - Criteria for selecting microcontroller - Overview of PIC family - PIC16F877A architecture, status register, Special function registers memory organization, On-Chip peripherals - PIC16F877A pin configuration - Fuse bits of PIC

UNIT III PIC16F PROGRAMMING

Data types and assembler directives - Addressing modes - Instruction set - Bit addressability - MACROs - Intel HEX file - Programming Tools: MPLAB IDE - I/O Port Programming, Timer programming, PWM programming, External Interrupt programming, ADC programming, EEPROM programming

UNIT IV SERIAL COMMUNICATION PROTOCOLS AND ITS PROGRAMMING

Introduction to UART protocol and its programming - I2C protocol and its Programming - SPI protocol and its Programming - Serial Port programming.

UNIT V PERIPHERAL INTERFACING AND ITS PROGRAMMING

LCD and Keyboard Interfacing - Relay interfacing - Stepper and DC Motor control - RTC Interfacing - LM35 Temperature sensor interfacing - MAX7219 display controller interfacing - Ultrasonic sensors interfacing -Introduction to PIC24F and dsPIC33EV

Text Books

- 1. Muhammad Ali Mazidi, Sarmad Naimi and Sepehr Naimi, "PIC Microcontroller and Embedded Systems: Using Assembly and C for PIC18*, Micro Digital Education, Illustrated Edition, 2017.
- Ramesh S Gaonkar, "Microprocessor Architecture: Programming and Applications with the 8085", Prentice Hall of India, New Delhi, 5th Edition, 2011.

Reference Books

- Sunil Mathur, Jeebananda Panda, "Microprocessor and microcontroller", PHI Learning Private Limited, New Delhi, 1st Edition, 2016.
- 2. dsPIC33EV data sheet

Web References

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- 2. https://www.youtube.com/watch?v=S1QCZW92fU4
- https://www.microchip.com/promo/explorer-8-development-board 3.
- https://www.mikroe.com/easymx-pro-stm32
- https://www.microchip.com/DevelopmentTools/ProductDetails/DM240001-2#utm_source=MicroSolutions&utm_medium=Link&utm_term=FY17Q3&utm_content=MCU16&utm_ campaign=Article

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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U20EEP408

MEASUREMENTS AND INSTRUMENTATION LAB

Course Objectives

- To give the students an insight into the constructional details of various measuring instruments for better
- understanding or their working principles.

 To demonstrate various Bridges for the measurement of resistance, inductance and capacitance using To understand the concept of magnetism and to determine the B-H curve for magnetic material specimen

To test and apply different types of sensors for the measurement of physical quantities such as speed torque, pressure, displacement, temperature, etc. by conducting an appropriate experiment.

Course Outcomes

After completion of the course, the students will be able to

CO1 - Realize the advantages and the necessity of measurement systems in all Engineering and Scientific

CO2 - Measure Resistance, Inductance and capacitance using AC and DC bridges. (K3)

CO3 - Determine the magnetization characteristics and hysteresis loss of Iron specimen using BH curve. (K3) CO4 - Calibrate single phase and three phase energy meters used in domestic and commercial applications.

CO5 - Determine the characteristics of RTD, thermostat, strain gauge and LVDT transducers and to apply for

List of Experiments

1. (a) Measurement of resistance using Wheatstone bridge

(b) Measurement of insulation resistance.

2. (a) Measurement of capacitance and loss angle of capacitor using Schering Bridge. (b) Measurement of inductance and Q-factor using Maxwell Bridge.

3. Extension of voltmeter and ammeter.

4. Calibration of single phase and three phase Energy meter using loading method.

5. Determination of B-H Curve for the magnetic material specimen.

6. Calibrate Current Transformer and Potential Transformer to determine ratio error and phase errors.

7. Characteristic of Temperature transducers (LDR, thermistor and thermocouple).

8. Measurement of Displacement using transducers.

9. Measurement of Voltage, current and power using Hall Effect transducer.

10. Characteristics of Optical Transducers (LDR, Phototransistor, Photovoltaic and photoconductive cells)

11. Measurement of speed using Magnetic and photo electric pickup transducers.

- 12. Measurement of Position using synchro Transmitter and receiver
- 13. Spectrum analyser and its use for analysing frequency spectra of periodic and non-periodic signals.

Reference Books

1. A. K. Sawhney, "A course in Electrical and Electronics Measurement and Instrumentation", Dhanpat Rai and Sons, 19th Edition, 2015.

William D. Coopers and Albert D. Helfrick, "Modern Electronic instrumentation and Measurements Techniques", Pearson Education India, 1st Edition, 2002.

3. E. W. Golding and F. C. Widdis, "Electrical Measurements and Measuring Instruments", Medtech Publication, 6th Edition, 2019.

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- http://vlabs.iitkgp.ernet.in/asnm/ 4.
- http://www.wisegeek.com/what-are-transducers.htm-5.

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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U20EEP409

MICROCONTROLLER AND ITS APPLICATIONS LAB

30

Course Objectives

- To become familiar with architecture and instruction set for 8085.
- To provide hands-on training of interfacing external sensors and actuators with microcontroller
- To impart knowledge for on-chip peripheral programs
- To impart knowledge to generate pulses for electrical applications.
- To impart knowledge to do minor projects using microcontroller for solving real world engineering

Course Outcomes

After completion of the course, the students will be able to

- CO1 Develop assembly language program for microprocessor 8085. (K3)
- CO2 Analyze various platforms for programming by knowing the complete hardware configurations. (K4) CO3 - Analyze abstract problems and apply a combination of hardware and software to address the problem
- CO4 Design a control algorithm for various applications using microcontrollers. (K3)
- CO5 Design and generate pulses for real time electrical applications. (K3)

List of Experiments

Microprocessor Experiments using 8085:

- 1. 8 bit Addition, Subtraction, Multiplication and Division
- 2. Assembly Language Programming: Subroutines, parameter passing to subroutines

Microcontroller Experiments using PIC:

- 3. PIC Assembly language- Programming using the PIC Instruction Set.
- 4. a) PIC Timer to generate accurate delay
 - b) PIC Timer to generate waveforms
- 5. Seven Segment Display interfacing with PIC
- 6. a) 16x2 LCD interfacing with PIC
 - b) 4x4 matrix keyboard interfacing with PIC
- 7. PIC UART programming
- 8. PIC on-chip ADC for interfacing analog sensors

Application of Microcontroller using PIC:

- 9. Experimentation of DC Motor Interfacing and Speed/Direction Control with PIC
- Stepper motor interfacing with PIC
- 11. Relay interfacing with PIC
- 12. DS1307 RTC Interfacing with PIC
- 13. MAX7219 LED matrix driver Interfacing with PIC using SPI protocol
- 14. Interface to peripherals and use of the I2C bus

Reference Books

- 1. Muhammad Ali Mazidi, Sarmad Naimi and Sepehr Naimi, "The AVR Microcontroller and Embedded Systems Using Assembly and C", Micro Digital Education, Illustrated Edition, 2017.
- 2. Ramesh S Gaonkar, "Microprocessor Architecture: Programming and Applications with the 8085" Prentice Hall of India, New Delhi, 5th Edition, 2011.
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- dsPIC33EVdata sheet.

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- 4. https://www.mikroe.com/easymx-pro-stm32
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COS/POS/PSOs Mapping

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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POWER ELECTRONICS

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Course Objectives

- To explain about the operations, switching characteristics of power semiconductor devices
- To study the operations and performance parameters of controlled Rectifiers.
- To analyze the operation and performance of dc to dc converters.
- To impart knowledge on different control techniques for inverters.
 - To familiarize the principle of operation of AC voltage controllers and cyclo converters

Course Outcomes

After completion of the course, the students will be able to

- CO1 Discriminate the switching characteristics of power devices and to use for power conversion. (K2)
- CO2 Inspect the performance of control rectifiers in continuous and discontinuous modes. (K2)
- CO3 Acquire knowledge on operation and analysis of DC to DC converters (K2)
- CO4 Outline the operating principles of various types of inverters. (K2)
- CO5 Gain knowledge on the operation of AC to AC converters and its applications (K2)

UNIT I POWER SEMI-CONDUCTOR DEVICES

Study of switching characteristics of MOSFET, IGBT, SCR and TRIAC, Turn on and Turn off methods of SCR Protection circuits - Triggering circuits.

UNIT II PHASE-CONTROLLED CONVERTERS

(9 Hrs)

Operation and analysis of single and three phase controlled rectifiers - half and fully controlled Converters with R, RL and RLE loads - Effect of source inductance on controlled rectifiers - Power factor and harmonic improvement methods - series converter, twelve pulse converter, Dual converter- circulating and noncirculating current mode.

UNIT III DC TO DC CONVERTERS

Principles of step down and step up chopper - Class A, B, C, D and E chopper, voltage commutated, current commutated chopper, multi-phase chopper, principle of operation of buck, boost and buck boost regulators switching schemes.

UNIT IV INVERTERS

Single phase and three phase voltage source inverters - Voltage and harmonic control techniques - Capacitor commutated current source inverter and auto sequential current source inverter.

UNIT V AC CHOPPER AND CYCLO CONVERETERS

Single phase and Three-phase AC voltage controllers - Control strategy - Single phase step-up/step-down midpoint type and bridge type cyclo-converters - Three phase cyclo-converters. Applications - regulated power supply, UPS, solid-state motor starter.

Text Books

- 1. P. S. Bimbhra, "Power Electronics", Khanna Publishers, New Delhi, 6th Edition, 2018.
- 2. M.H. Rashid, "Power Electronics: Circuits, Devices and Applications", Pearson Education, New Delhi, 4th Edition, 2017.

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- 1. Ned Mohan, M. Underland, William P. Robbins, "Power Electronics Converters, applications and design", JohnWiley & sons, Singapore, 2001.
- 2. M. D. Singh, K. B. Khanchandani, "Power Electronics", Tata McGraw Hill, New Delhi, 2007.
- 3. Cyril W. Lander, "Power Electronics", McGraw Hill Book Company, Singapore, 1993.
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- 5. https://nptel.ac.in/courses/108/102/108102145/

Department of EEE – Fourth Meeting of 6 nS

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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Department of EEE - Fourth Meeting of BoS

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U20EET617

RENEWABLE ENERGY SOURCES

Course Objectives

- To impart knowledge on renewable energy sources and technologies.
- To gain adequate knowledge on variety of issues in harnessing renewable energy sources.
- To outline about the alternate renewable energy sources for both domestics and industrial applications To provide knowledge about grid connectivity in renewable energy systems.
- To provide in-depth knowledge in the key concepts of energy policies.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the national and international energy scenario of renewable energy Sources. (K2) CO2 - Design the aerodynamics of wind turbines and calculate their energy production. (K3)
- CO3 Analyze electrical power generation from biomass, geothermal, tidal, wave etc. (K2)
- CO4 Analyze technical and sustainability issues involved in the integration of renewable energy systems. (K2) CO5 - Compare the cost economics of using renewable energy sources with non-renewable energy sources (K2

Overview - Limitations of conventional energy resource - Importance of renewable sources - Types - Preserview Indian and international energy scenario. Solar Energy: solar thermal power and its energy conversion - solar collectors - types and applications. Photovoltaic (PV) technology - photovoltaic effect - efficiency of solar cells Design Concept of solar PV system - standards and applications.

UNIT II WIND AND HYDRO POWER ENERGY

Wind Energy: wind data - properties - speed and power relation - wind turbines and electric generators (9 Hrs horizontal and vertical wind mills - wind energy farms - off-shore plants- Selection factors. Hydro Energy: small mini and micro hydro power plants and their resource assessment - plant layout with major components -selection

UNIT III ALTERNATE ENERGY SOURCES

Biomass: Photosynthesis and origin of biomass energy - terms and definitions - pyrolysis, thermo-chemica biomass conversion to energy, Geothermal: resources, hot spring, steam system, site selection, challenges Ocean and Tidal energy: principle of OTEC - wave energy conversion machines - fundamentals of tidal power conversion systems and limitations - Introduction to fuel cells.

UNIT IV GRID INTEGRATION

Wind power interconnection requirement - low-voltage ride through (LVRT), ramp-rate limitations, supply of ancillary services for frequency and voltage control - load and reserve requirement - issues in interconnection steady - state and dynamic performance of power system - interfacing solar system with grid - protective relaying islanding, Power quality issues.

UNIT V RENEWABLE ENERGY POLICY

Renewable energy policies: Five Year Plan programmes - Feed-in tariffs - portfolio standards - policy targets, tax incentives - bio-fuels mandates - International policies for climate change and energy security - Economic analysis and comparisons - Life cycle analysis - financial analysis - cost of conserved energy and externalities Cost assessment of supply technologies versus energy - Efficiency - Renewable Energy Certification - Carbon contents.

Text Books

- 1. G. N. Tiwari and M. K. Ghosal, "Renewable Energy Resources: Basic Principle and Application", Alpha Science International Ltd, New Edition, 2005.
- 2. B. H. Khan, "Non-Conventional Energy Resources", Tata McGraw Hill, 2rd Edition, 2009.
- 3. R. Loulou, P. R. Shukla and A. Kanudia, "Energy and Environment Policies for a sustainable Future", Allied Publishers Ltd, New Delhi, 1997.

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- 1. Solanki Chetan Singh, "Solar Photovoltaic Fundamentals, Technologies and Applications", PHI, New Delh 3rd Edition, 2015.
- 2. Mukund R Patel, "Wind and Solar Power Systems", CRC Press, New York, 2nd Edition, 2011.
- 3. D. P. Kothari, K. C Singal, Rakesh Ranjan, "Renewable Energy Sources and Emerging Technologies", Phil Learning Pvt. Ltd, New Delhi, 2nd Edition, 2013.
- 4. John Twidell and Tony Weir, "Renewable Energy Resources", Routledge publication, 3rd Edition, 2015.
- 5. Godfrey Boyle, "Renewable Energy: Power for a Sustainable Future", Oxford University Press, 3rd Edition 2014.

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- https://nptel.ac.in/courses/108/108/108108078/

COS/POS/PSOs Mapping

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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POWER SYSTEM ANALYSIS

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Course Objectives

- To create computational models for power system using per unit analysis.
- To perform load flow analysis using Gauss Seidal and Newton-Raphson methods.
- To analyze the sequence network using symmetrical components.
- To import the knowledge about symmetrical and unsymmetrical faults in power system.
- To demonstrate different methods and factors influencing on power system stability

Course Outcomes

After completion of the course, the students will be able to

- CO1 Compute the reactance diagram and network matrices. (K4)
- CO2 Apply the iterative techniques to solve the power flow analysis used in power system planning. (K3)
- CO3 Analyze the Sequence networks using positive, negative and zero sequence network. (K4)
- CO4 Carry out short circuit studies to design the circuit breaker ratings in power system (K4)
- CO5 Analyze stability problems in power system during pre-fault and post-fault conditions (K4)

UNIT I MODELING OF POWER SYSTEM COMPONENTS

(12 Hrs)

Need for system planning and operational studies - Power system components - Representation - Single line diagram - Per unit quantities - P.U. impedance / reactance diagram - Formulation of network matrices for the power systems - Bus impedance and bus admittance matrices - Reduction techniques on network matrices for network changes - Z bus Building algorithm.

UNIT II LOAD FLOW STUDIES

Classification of buses - formulation of load flow problem - Load flow solution by Gauss - Seidal, Newton Raphson and Fast Decoupled Load Flow (FDLF) Analysis - Comparison - Computation of slack bus power transmission loss and line flow - Voltage Control Methods - Tap-changing and phase - shifting transformers.

UNIT III SYMMETRICAL COMPONENTS AND SEQUENCE NETWORKS

(12 Hrs)

Symmetrical components - Simple problems to calculate symmetrical voltages and currents - Sequence networks- positive, negative and zero sequence networks - Sequence networks of Series impedance, loads and Rotating machines - Advantages and Limitations.

UNIT IV FAULT ANALYSIS

(12 Hrs)

Need for fault analysis - Types of faults - Symmetrical fault analysis through bus impedance matrix - Analysis of unsymmetrical faults- LG, LL and LLG - Analysis of simultaneous unbalanced short circuit and open conductor faults in power systems - short circuit capacity - circuit breaker selection - Representation of various types of faults in sequence networks.

UNIT V STABILITY STUDIES

Definition - Importance of stability analysis- classifications - Steady state and transient stability - Angle and voltage stability - Single Machine Infinite Bus (SMIB) system - swing equation - Swing Curve - Numerical integration methods - Equal area criterion - Critical clearing angle and time - Factors affecting stability Methods of improving transient stability.

Text Books

- 1. P. Kundur, "Power System Stability and Control", Tata McGraw Hill Education Pvt. Ltd., New Delhi, 10th Reprint, 2013.
- D. P. Kothari and I. J. Nagrath, "Power System Engineering", Tata McGraw-Hill Education, 3rd Edition
- 3. Hadi Saadat, "Power System Analysis", Tata McGraw Hill Education Pvt. Ltd., New Delhi, 21st Reprint 2010.

Reference Books

- 1. M. A. Pai, "Computer Techniques in Power System Analysis", Tata McGraw-Hill Publishing Company Ltd New Delhi, 2nd Edition, 2012.
- 2. J. Duncan Glover, Mulukutla S. Sarma, Thomas J. Overbye, "Power System Analysis and Design Cengage Learning, 5th Edition, 2016.
- 3. John J. Grainger, Jr. William D. Stevenson, "Power System Analysis", McGraw Hill Education (India) Private Limited, New Delhi, 2nd Edition, 2015.

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cos/POs/PSOs Mapping

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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ENERGY STORAGE TECHNOLOGY

Course Objectives

- To understand the purpose of energy storage systems.
- To learn the different energy storage techniques.
- To learn about the different types of batteries available for energy storage.
- To impart knowledge regarding on the advanced energy storage systems.
- To learn about the different vehicular energy storage schemes.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Familiarize the need for energy storing.(K2)
- CO2 Analyze the various energy storage techniques in the form of electrical, magnetic and chemical systems
- CO3 Analyze the different batteries and its characteristics used for storing the energy in electric vehicles, nano-tubes etc.(K4)
- CO4 Impart the concepts of Superconducting Magnet Energy Storage Systems and super-capacitors in digital cameras, PC cards, electric vehicles, medical applications etc.(K3)
- CO5 Analyze the various energy storage techniques used in Electric vehicles and its hybridization concepts, power grid stabilization, rail-system power models etc.(K4)

UNIT I ENERGY STORAGE NEEDS

Energy Storage Need of energy storage - Different modes of Energy Storage - Potential energy - Pumped hydro storage - Kinetic Energy and Compressed gas system - Flywheel storage, compressed air energy storage - Environmental and sustainability issues.

UNIT II ENERGY STORAGE TYPES

(9 Hrs)

Electrical and Magnetic energy storage, Capacitors, electromagnets - Chemical Energy storage - Thermochemical, photo-chemical, bio-chemical, electro-chemical, fossil fuels and synthetic fuels - Hydrogen for energy storage, Solar Ponds for energy storage. Electrochemical Energy Storage Systems, Case study on perovskite solar cell.

UNIT III BATTERIES

Batteries - Primary, Secondary, Lithium, Solid-state and molten solvent batteries - Lead acid batteries - Nickel Cadmium Batteries - Advanced Batteries - Role of carbon nano-tubes in electrodes - Flow battery operation -Flexible fiber battery- air batteries

UNIT IV SUPERCONDUCTING MAGNET ENERGY STORAGE SYSTEMS

Superconducting Magnet Energy Storage(SMES) systems - Capacitor and Batteries: Comparison and application - Super capacitor - Electrochemical Double Layer Capacitor (EDLC), principle of working, structure, performance and application, role of activated carbon and carbon nano-tube - Super Capacitors - power calculation - operation and design.

UNIT V VEHICULAR ENERGY STORAGE SYSTEMS

(9 Hrs)

Energy storage technologies in hybrid vehicles - flywheel, hydraulic, fuel cell and hybrid fuel cell energy storage system - ultra capacitors - comparison - battery charging control

Text Books

- 1. Jiujun Zhang, Lei Zhang, Hansan Liu, Andy Sun, Ru-Shi Liu, "Electrochemical Technologies for Energy Storage and Conversion-2 Volume set", John Wiley and Sons, 1st Edition, 2011.
- 2. Detlef Stolten, "Hydrogen and Fuel Cells: Fundamentals, Technologies and Applications", Wiley, 1st Edition. 2010.
- 3. Robert Huggins, "Energy Storage: Fundamentals, Materials and Applications", Springer, 2nd Edition, 2016.
- Andrei G. Ter-Gazarian, "Energy Storage for Power Systems", Institution of Engineering and Technology. 3rd Edition, 2020.

Reference Books

- 1. Francois Beguin and Elzbieta Frackowiak, "Super capacitors: Materials, Systems and Applications", Wiley-VCH, 1st Edition, 2013.
- 2. Doughty Liaw, Narayan and Srinivasan, "Batteries for Renewable Energy Storage", The Electrochemical Society, New Jersy, 2010.
- 3. Ali Emadi, Mehrdad Ehsani, John M. Miller, "Vehicular Electric Power Systems: Land, Sea, Air and Space Vehicles", CRC Press, 1st Edition, 2003.
- Chris Mi, M. Abul Masrur, David Wenzhong Gao, "Hybrid Electric Vehicles: Principles and Applications with Practical Perspectives", Wiley, 1st Edition, 2011.

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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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Annexure - III

Academic calendar (IV Year)

Use of Cell Phones

Maintenance of Discipline

Discipline is an important factor that shapes one's personality. It is considered as a golden key capable of opening many doors. This institution expect each and every student to follow the rules and regulations in total. Maintaining

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SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

An Autonomous Institution
(Accredited by NBA ARTE, New Delht, NAAC with 'A' Grade)

Madagadipet, Puducherry - 605 107



Academic Calendar

January 2022 to May 2022

Programme : B.Tech

Department :

Year / Sem : IVYr / VIII Sem

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sent Marks for Practical : 50 Marks

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Students should not be absent for the online classes/regular class Attendance for the online classes/regular classes are monitor regularly and it is recorded. Continuous assessment mark will be be on the performance of the students in the continuous assessment is assignment and attendance percentage.

Assignments: 5 marks

Out of 25 continuous assessement marks, 5 marks will be awarded for the assignment. The assignment questions will be different for each and every student. The students have to submit 3 assignments in each subject. Best of 2 out of 3 assignments will be consider. ments will be consider.

Undertaking Minor / Major Projects

on is advised to take atleast one numor project. Involving in the project ping to understand the basics of the subject. Some of the numor / major all do be benefing the society. Moreover, the Management awards cash the best projects in each department

n in the Curricular / Co-curricular / Extra curricular Activities

rest arccincouraged to participate in the curricular / co-curricular / extra cutties. Involvement in these activities will improve their knowledge edgest. If a student or a ream get cash prize / award in the technical used by the recognised institutions, then the management of this will also wantion as amount equivalent to the winning award / cash prize (appreciation.

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Date	Day	Schedule	Working day Holkies
1	PA		58
5	Set	The state of the s	59
3	Sun		Hobbin
4	AADA		60
3	Tve	Feedback from the students & Analysis	61
8	Wed	COM meeting 100	82
7	Thu	The same of the sa	4
	Fn	A STATE OF THE PARTY OF THE PAR	64
	Set		65
10	Sun		Holder
11	Mon	Model for IV Year (3 units)	60
12	No.		87
13	V#4	The state of the s	6.9
H	IN	Tamil New Year	Nabday
1500	Fit	Good Friday	Hober
16	Set		69
	Sun		Heliday
18	Mon	The state of the s	70
19	Tue		71
20	Med		72
21_	_IN	A CONTRACTOR OF THE PROPERTY O	73
22	FH	Model Result Analysis & Report Submission	74
23	Set	Submission of Attendance & Assessment -Term III	73
N	Sn		Holiday
29	Mon	Tentative End Semester Examination starts	-
.8	Tue	The state of the s	
27	Meg		
29	TN	The state of the s	
29	Fri		
30	Set		
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		Total number of working days : 1: Total number of holiday : 7	;

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West West The Freshol from the students & Analysis Sale CAT4 for IV Year (1 x units) West CAT4 for IV Year (1 x units) West To The Sale Sale CAT4 Result Analysis & Report Submission of Atlandance & Assessment - Final Project Review Final	32 33 34 35 Hohday 36 37 38 39 40 41 Holday
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Thu Fi Sal CAT-II Result Analysis & Report Submission Mo Abbrisson of Asendance & Assessment - Term II Thu Final Project Review Final Sal Sal	45
No. 54 CAT-II Result Analysis & Report Submission in the Submission of Abendance & Assessment - Term II The Final Project Review Tru	46
74 Sull Sull Sull Sull Sull Sull Sull Sul	47
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	55
Wor .	58
Wed	57
Thu	
Total number of working days : 27 Total number of holiday : 04	
Total number of holiday : 04 ந்தி வெள்மணி ஒவ்வோகு வாய்பாகும் உள்ள ஆயத்தைப் ப ந்தி வெள்மணி இயத்தோம் உள்ள வாச்சிக்கைய் பாச்சிதோன்.	

Important points for the kind attention of the Parents

Dear Parent

The VIII semester classes commences on 24th January 2022. The alone mentioned semester is a very short term, inclining working days meant for model exam. The students have to complete a lot of work within a short period. Rease the parents are kindly requested not to permit their wards to avail frequent leave during this semester period for the following reasons.

Marks in the continuous assessment lest decide the major part of the continuous assessment marks. So, availing leave for the continuous assessment test must be avoided at any coul as this would seriously affect the continuous assessment marks.

Practicals are very important not only to score more marks but also it will help to understand the theory part of the subject, hence advice your ward not to avail leaves during practical classes.

Please spare your valuable time to talk to your son/daughter every day and try to understand what he/she is doing in respect of his/her studies. Kindly extend all your support to your our/daughter which will help them to come out successfully. For any assistance from our side your any always feel feet to contact the respective Cucrdinator / HOD any time during the working bours.

Dete	Day	Schedule	Working day Holiday
1	Sat	New Year	Holiday
24	Sun		Holiday
1	Mon		
4	Tue		
5	Wed		
•	Thu		
1	Fn		
	Sat		
2	Sun	一种企业	Holiday
10	Mon		
Ħ	Tue		
Q	Wed		
13	Thu	Bogi Festival	Holiday
M	Fri	pongal	Holiday
15 2	Set	Thenwalturar Day	Holiday
16	Sun	Uzhavar Thiruna	Holiday
17	Mon		- HONOS
	Tue	the state of the s	
20	Wed		
7	The	The state of the s	
2	Fn		
23	Sel		
24	Sun		2000
8	Mon	Commencement of VW semester classes	Holiday
	Tue	W VI semester classes	1
7	Wed	Republic day	2
2	The	The state of the s	Holiday
3	Fn	The state of the s	3
M	Sal	Andrew Street, Square Control of the Street, S	4
31	35	District Control of the Control of t	5
_	3		Holiday
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		Total members	
	-	Total number of working days: 06 Total number of holiday:03 magning assistant assumpts asing agraphs	* Artificial Services
	THE R	the parties of holiday :03	

1 2 3 4 5	Tue Wed Thu Fri Sat		7 6 9
3 4 5	Wed Thu Fri Sat		9
3 4 5 6	Fri Sat		10
4 5 6	Sat		
6			-11
ALCOHOLD TO	Sug		
Witness Commission of the Publish Commission			Holday
7 1	Mon		12
8	Tue	Feedback from the students & Analysis	13
9	Wed	QCM meeting - 1 Phase II First review	14
10	Thu		15
11	Fri		16
12	Sat	A STATE OF THE STA	17
13	Sun		Hohday
14	Mon	CAT-I for IV Year (1 % units)	18
15	Tue		19
16	Wed		20
17	Thu		21
18	Fri		22
19	Sat		23
20	Sun	of the second of the second of the second of the	Holiday
21	Mon	CAT-I Result Analysis & Report Submission	24
22	Tue		25
23	Wed	Submission of Attendance & Assessment - Term 1	26
24	Thu		27
25	Fri		28
26	Sal		29
27	Sun	The State of the S	Hobday
	Mon		30

Academic calendar (III & II Year)

Use of Cell Phones

It has been decided out to permit rell phones trade the callege rangus. It may student is formal using the cell phone inside the college campus, it would be veraflecated and will not be returned back on any circumstance. Hence the students are fustracted not to attend the college with the mobile phones.

Dress Code

The students are requested to strend the college neatly dressed. While the male students should attend the college with the shirts neatly tasked in and with the shoes, the female students are permitted to come with churidar and disputa properly pinned. Students were ing full band shirts should use it as such without fidding it to half etc. Curvail wears the peans, I -durit see, both for boys and girls are withy probabilised inside the compan. Each department has prescribed uniforms for the idea. The students are requested to such a direct to the dress codes as well as the rules and regulations of the college.

Maintenance of Discipline

Discipline is on important factor that shapes one's personably. It is considered on a golden key capable of opening many down. This institution expects each and every student to follow the rules and regulations in total. Maintaining discipline in the compan wall premote a conductive contenuent for studes.

- 0	eline class	- Mary	ng hour	Regular	tass
Thour	09.30 a.m. to	10.20am	1 bour	@.Coam to	09.50 a m
(Presid	10.20 a.m to	10 X0a.m	II hour	O Mam to	10.40 a.m
Ilhou	10. Wam to	11.20am	Fernal	10.40am to	10.55 a.m.
[Preside	11.20am to	11. Oa m	III hour	10.55 am to	11.45am
III bour	11.30a.m to	12.20 p.m	IV how	11.45 am to	12.35 p.m.
L. post	01.30 p.m to	02.20pm	/ bour	01.15 p.m to	ac aspm
Frenk	C Mpm to	02.30 p.m	\Thour	02.05 pm to	@ 55 pm
\'how	C 30pm to	01.20 pm	\TI bour	2.55 p.m. to	Q3.45 p.m
Best	CA 20pm to	08.30 p.m	/Ill post	03.40 p.m. 10	04.35 pm
VI hose	03.50 pm to	(N.20pm		•	

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

An Autonomous Institution

(Accredited by NBA-ACTE, New Delhi, NAAC with "A" Grade)

Madagadipet, Puducherry - 605 107



Academic Calendar

January 2022 to May 2022

Name

Programme : B.Tech.

Department :

Year / Sens : Il Yr / III Senn & III Yr / Vl Senn

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باریشیر محیات بات بازی



Sa'i Manukula Vinayugar Engineering College has been conferred with Auknomens Status by the University Grants Commission on 26° September 2019 and the same was approved by Poud-cherry University on 19° Pure 2020. SNNTEC Autocomous Regulations P.2019, is followed for the audicuts admired in the Academic Year 2019-20 Quesseur Third Year). SNNTEC Autocomous Regulations R.2020, is followed for the students admirted from the Academic Year 2020-21 onwards (present first year

BROKE SCHOOL BULLECATION ON OUR RECYTATION FROM BY A CLASSICIALITY

- Industry 4.0 ready curriculum

- Multichsciplinary curriculum Grienied towards entreprenurship develop
- Choice to learn HLTS / Foreign Languages
- Results will be declared within a month after completion of examinations Supplementary Examination in 5th and 5th semester for the students having 2

Ethanorech/Mandatury course
The Instante has Established I? Center of Excellence to provide 91 International
Certification courses from BBM, Google, Cisco, e Plan, Microsoft, Autodesk,
Texas instruments Festic, Bentley, Schneider Electric, Amazen web services,
Stemens, Tally DLLL EMC, Hastin Lecknew, PEC, LI san Excellence in
Technology & Dalactic solutions All students should enroll in certification
course from semester-1 to semester-VI.

Students may undergo it aiming or intereship during summer / sinter vacation at Industry/ Research or ganization. Students are also permitted to undergo intereships during their eighth semister after the completion of theory clauses.

Date	Day	Schedule	Morking day Holiday
1	Wed		
2	Thu		
3	Ea		
4	Sat		
5	Sun	Sales the good of the sales and the sales of	Holiday
-6-	Mon_		
7	Tue		
8	Wed		
9	Thu		
10	_Fri		
11	Sat		Holdry
13	Sun	Carried Control of Con	HOPEZ
14	Mon		
15	Wed		
16	Thu		_
18	Sat		
19	Sun		KALL
20	Mon	Control of the Contro	700
21	Tue		
22	Wed		
23	Thu		
24	Fri		
25	Sat		-
26	Sun		Holde
27	Mon		-
26	Tue		
29	Wed		-
30	Thu		
	-	The state of the s	
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And the last		Total number of working days : •	
		Total number of holiday ; - na zámana pode, tangu sayuguh p@da gapan;	

May 2022 1 Sun 2 Mon 3 Tue 4 Wed 5 Thu 6 Fri 7 Sat 8 Sun 9 Mon 10 Tue 11 Wed 12 Thu 13 Fri 14 Sat 15 Sun 16 Mon 17 Tue 18 Wed 19 Thu 20 Fri 21 Sat 22 Sun 24 Tue 25 Wed 26 Thu 27 Fri 28 Sun 29 Mon 31 Tue Holiday

Supplementary examination is an additional examination conducted within a month of time after deducting the results of end senseste examination. In order to complete the program within 4 years, only the student with maximum of two arreats will be permitted to appear for supplementary examination. The supplementary examination will be conducted in fifth and eighth sensester only. For supplementary examination, the continuous assessment marks of the last attempt will be considered.

Tutor Ward System

istribution of Continuous Assessment Marks (CAM) and End Semester Examination Marks (ESM) Scheme for Continuous Assessment Test(CAT)

			Co	otinues Ass	cysnic	nt c	omp	men	s		
S. No	Course Type	Test Marks	Average of pre-post test viva for each cach experiment	Average of nucles for experiment report for each experiment	Shodd Exam Report	Assignment	Review- 1	Review-2	Review-3	Attendance	Total
1.	Theory	15	•	•		3				5	25
2	Practical	•	10	15	15	•	·	•	•	10	50
7	Project work	$\overline{\cdot}$	-			┮	Ю	10	30	-	40

Weightage of Assessment for Theory Course							
2 No	Test	Portion for Test	Test Marks	Duration of Test	Weightage for Internal		
1	CATI	1% Units	50	1 % hours			
2	CAT 2	1% Units	50	1 % hours	10		
3	Model	5 Units	75	3 bours	05		
		Continuous	Assessment fo	Theory Course	15		

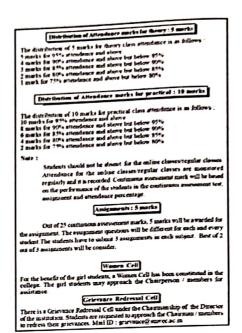
Question paper for CAT and ESE will be but Table (a) and (b) Table (a) Question Paper pattern for C

Test Type 2 Marks 5 Marks 10 Marks Total Marks
CAT 1 to 2 (questions) 4(questions) 2 (questions) 50

	(10 Marks)	(20 Marks)	(20 Marks)	
Model	End seures	75		
Table (b)	Question paper par	tern for End w	mester Examina	lon(ESE)
2 Marks	5 Mark		10 Marks	Total Marks
10(20 Marks)	5 (25 Mar)		3 (30 Marks)	75

Date	Day	Schedule	Working day
1	Fri		Holiday 60
2	Sat		81
388	Sun		Modday
4	Mon		62
5	Tue		63
6	Wed	Assignment - III	EL .
7	Thu	Feedback from the students & Analysis	65
8	Fri	QCM meeting - 3	-
9	Sal		68
10	Sun		-
11	Mon	Model for IL III Year (5 units)	Noticing 58
12	Tue		
13	Wed		- 69
#14	Thu	Tamil New Year	70
15	Fri	Good Friday	Haliday
16	Sat		Heider
17	Sun		71
18	Mon	The second secon	Haldey
19	Tue		72
20	Wed		73
21	Thu	Last working day for IL III Year	78
22	Fri	Model Result Analysis & Report Submission	75_
23	Sat	Submission of Attendance & Assessment -Term III	76
24	Sun	A V Transport - I delie (II	77
25	Mon	Tentative End Semester practical examination starts	Honday
26	Tue	Party or Evaluation state	78
27	Wed		79
28	Thu		80
29	Fri		81
30	Sat	Tentable End Semester practical examination ends	82
-		Marbea evanneron ents	83
		Total number of working days : 24 Total number of holiday : 6	

Dete	Day	Schedule	Weeking day Hestology
, 1	Tue		33
2	Wed		1 33 M
3	The	A STATE OF THE PARTY OF THE PAR	35
4	Fri		36
5	SMI		37
6	Sun		Probate
7	Mon	many and and the first of the second of the	38
	Tue		29
9	Wed	Assignment - II	40
10	Thu	Feedback from the students & Anahala	41
- 11	Fn	OCM meeting · 2	42
12	Set	and the second s	43
13	Sun	NAME AND ADDRESS OF THE OWNER, WHEN PARTY OF T	Holder
14	Man	CATA for 8, Ill Year (1 % ursts)	44
15	Tue	Town or of m less (1 4 area)	45
16	Wed		46
17	Thu		47
18	Fn		48
10	Sat	The second section of the section of the second section of the section of t	40
20	Sun		Hohday
21	Mon	CAT-II Result Analysis & Report Submission	50
21 22	Tue		51_
23	Wed	Submission of Attendance & Assessment-Term II	52
24	The		53
25	Fn		54
26	Sat		55
27	Sun	AND THE RESIDENCE OF THE PERSONNESS.	Holday
28	Mon		56
20	Tue		57
30	Wed		58
31	Thu		59
		Total number of working days : 27 Total number of holiday : 84	_



	Impurtance of Continuous Assessment Marks(CAM)
CANADA ALSO	sons assessment marks once extend are carried over to the subsequent Hencir the students are requested to work hard to get the maximum accessment packs. If the continuous assessment marks are lower, a will hances of getting the first class, distinction, gold medals and ranks.
	Importance of CAT-I/CAT-II/ Model Examination
Continuon Model Exa examination	ensembers marks are awarded for the performance in the CAT-I, CAT-II & m. Hence all the students are requested to prepare well for each test to earn the maximum continuous assessment marks.
	Undertaking Minor / Major Projects
project wall	of it advised to take effects one numer project. Involving in the project sing to understand the basics of the subject. Some of the numer / major also be brunching the society. Moreover, the Management awards cash select judgets in each department.
F - 41 4	tion to the Curricular / Co-curricular / Estra curricular Activities
All the stude curricular a level in the event organ institution is as a token of	ents are encour agod to participate in the curricular / co-curricular / extractivities involvement in these activities will improve their likewidely authority in a statum get cash prize / swiff in the technical particular than the companion of the ecognitied institutions, then the immangement of this prize will also sendition an amount equivalent to the winning award / cash prize of appreciation.
for each "	tudent, leave account rected has been provided. The students are object, the leave fecord to their parents and strictly adarre to the

exceptant cases, the statement of
Transport For int
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6) buses have been arranged for the sudents to reach the college from Puducherry.
have been arranged by the singular Cuddalors, Nellikuspans.
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Date	Day	Schedule	Horking da Horiday
1	Tue		9
2	Wed		
3	Thu		
4	Fri		12
5	Sat		13
6	Sun		Poster
7	Mon		14
8	Tue		18
9	Wed	Assignment - I	17
10	Thu	Feedback from the students & Analysis	18
11	Fn	OCM meeting - 1	10
12	Sat		Halicon
13	Sun	College and the second	20
14	Mon	CAT-I for IL III Year (1 % units)	21
15	Tue		22
16	Wed		23
17	Thu		23
18	Fn		
19	Sal		25
20	Sun		Holdey
21	Mon	CAT-I Result Analysis & Report Submission	26
22	Tue		27
23	Wed	Submission of Alterdance & Assessment - Yors: 1	28
24	Thu		29
25	Fri		30
26	Sat		31
27	Sun		Holdey
28	Mon		32
		Total number of working days : 24 Total number of holiday : 04	

Date	Day	Schedule	Working day
21.00	Sat	New Year	Holiday
2	Sun	The second secon	Holder
3	Mon	The second secon	House
4	Tue	The state of the s	-
5	Wed	A second	-
6	Thu	The state of the s	- Constitution of Street, Stre
7	Fri	The second secon	and the same of th
8	Sat	A service of the control of the cont	
9	Sun		Holiday
10	Mon		House
-11	Tue	The state of the s	
12	Wed		
13	Thu	Bogi Festival	Holiday
14	Fri	pongal	Holiday
15	Set	Thiruvalluvar Day	Holiday
16	Sun	Uzhever Thiruna	Holder
17	Mon	The state of the s	and the same of
18	Tue	The state of the s	- Liphon American
18	Wed		
20	Thu		
21	Fri	Commencement of B & VI comeater classes	1
22	_Set_		2
23	Sun		Holiday
24	Mon	The state of the s	3
	Tue		1
26	Wed	Republic day	Holiday
27	Thu		5
28	Fn		6
	Sat		7
30	Sun	A CONTRACTOR OF THE PROPERTY OF THE PARTY OF	Holiday
31	Mon		8
		Total number of working days : 08 Total number of holiday :03	

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Important points for the kind attention of the Parents

Dear Parent

The III and VI semester classes commences on 21° January 2022. The above mentioned semester is a very short term, including norking days meant for model exam. The students have to complete a lot of war k within a short period. Hetere the purrents are kindly requested not to permit their wards to reall frequent leave during this venuester period for the following reasons:

III Semester (II Year) & VI Semester (III Year): All the III & VI semester papers are considered as analytical papers. Hence, regular attendance and more concentration are required to clear these semester papers.

Marks in the continuous assessment test decide the major part of the continuous assessment marks. So, availing heave for the continuous assessment test must be avoided at any cost as this would seriously affect the continuous assessment marks.

Practicals are very important not only to score more marks but also it wil help to understand the theory part of the subject, hence advice your ward not a avail leaves during practical classes.

Pienes spore your valuable time to talk to your somidaughter every day and try to understand what he/she I doing in respect of his/her studies. Kindly extend all your support to your son doughter which will help them to come our successfully. For any assistance from our side you may always feel fee to contact the respective Coordinator / HOD any time during the working hours.

Academic calendar (I Year)

Use of Cell Phones

It has been decided not to permit cell phones haside the college campus. Il indent is found uning the cell phone inside the college campus, it would be ented and will not be returned back on any circumstances. Hence the stadents

Dress Code

The students are required so ntrend the college nearly dressed. While the male students should strend the college with the shorts treatly tocked in and with the shorts, the funnal students are permitted to come with charidar and obtains reporting paid by takens wearing full hand shirts should were it as such a disparing reporting the first Crumia weren's like jearn. T-shirts etc., both fix boys and grids are strictly probabilised made the campus. Each department has prescribed uniforms for the labs. The students are requested to strictly adhere to the dress codes as well as the rules and remaining of the colleges.

Maintenance of Discipline

Discipline is an important factor that shapes one's personality II is considered as a guiden key capable of questing many down. This institution expects such and every winders to follow the rules and regulations in total. Maintaining discipline in the compass will premove a conductive environment for studies.

- O		- West		Regular	
1 pora	09.30am te	10.25 am	1 hour	845am to	09.35 a.m
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SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)
ed is REARTE, See 1999 and Aeredied by SASC 400 'A' Grass
Madegadipet, Puducherry - 605 107



Academic Calendar

November 2021 to April 2022

Name

Programme : B. Tech.

Department :

Year / Som : 1 year / 1 semester

Amell 2022

About Autonomous

Sri Manakula Vianyagar Engineering College has been conferred with Autonomous Status by the University Grants Commission on 26th September 2019 and the same was approved by Pondicherry University on 19th June 2020, SMYEC Autonomous Regulations R2019, is followed for the students admitted in the Academic Year 2020-21. SMYEC Autonomous Regulations R2020, is followed for the students admitted from the Academic Year 2021-22.

HIGHLICHTS OF SMYEC AUTONOMOUS RECULATIONS 2020

- Temil. Hindi and French as options in Modern Indian Language
- ladustry 4.0 ready curriculum
- Skill enhancement courses in every semester
- Multidisciplinary curriculum with open elective o
- Courses on entrops encurial development IPLIS / IAS / entrance exams coaching

The Institute has Established 17 Centers of Excellence to provide 91 International Certification courses from IBM, Google, Cisco, E Plan, Mist north, Autodesk. Teans institutions, Festo, Bentley, Schneider Electric, Amazou web service, Sienusea, 1819, DELL EMC², Harita Technery, PTC, LN an Excellence in Technology & Didactic solutions, All the students should enroll in the certification course in every semester

Students may under so is mining or internship during summer / winter vacation at Industry Research or ganitation students are also permitted to under gor internships during their sixth semoster after the theory classes are over

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13	Wed		
	Thu	Tamil New Year / Dr. Ambedkar Birthday	Holiday
15	Fri	Good Friday	Holiday
16	Sat		
31744	Sun		Heliday
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19	Tue		-
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Reme fire

- More number of uniferrs will receive the degree within the stipulated time
- The inchmation prefer to recruit audients having all arrows. If the supplementary examinations are conducted, then more than the prefer of the conducted of the secondary.

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After the publication of the result, photocopy of the answer books shall be provided to the student on request with ripulated for fixed by the follows from the latest parties.

Punctuality in Attendance

The students are requested to keep op principality in attending the college. The late comes will be losing their attendance and the internal marks. Hence all the students are requested to whether the college on time. A student shall be permitted to appear for the End Semester Examination or the end of the semester and the college of the students.

Repeating the Course

A student who secures overall attendance which is less than 60% has to repeat the course with the secures of the institution.

Mentor Mentes System

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Paris		,	(out of) one from	questions, each unit)	(compulsory question from unit lif) 9 mark question (out of 3 questions, one from each Unit)			38			

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1	Tue	CAT - II Result Analysis and Report submission	52_
2	Wed		53
1	Thu		54
-	Fri		55
5	Sat	Secol caching date (2) Senior (2,) Pacement Academic Activities	56
6	Sun		Holiday
7	Mon		57
8	Tue		58
9	Wed		59
10	Thu		60
11	Fri	Field trip / Industrial Visit	61
12	Sat	Special coaching class / CP / Saminar / CL / Pacamont / Academic Architect	82
13.00	Sun		Heliday
14	Mon		63
15	Tue		64
16	Wed		65
17	Thu		66
18	Fri		67
19	Sat	Assignment - II	68
20	Sun		Holiday
21	Mon	Feedback from the students & Analysis	69
22	Tue	QCM meeting 3 / Report Submission	70
23	Wed	AND THE PERSON NAMED OF TH	71
24	Thu		72
25	Fri	- Warner Control of the Control of t	73
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27	Sun		Heliday
28	Mon	Submission of attendance and Assessment Term - 3	75
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17	Wed	And the state of t	tion to the spinish below
18	Thu	The state of the s	
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23	Tue		-
24	Wed		
25	Thu	I year Induction Day	
26	Fri	AICTE Induction programs starts	
27	Sat		
28	Sun	CONTRACTOR CONTRACTOR NOT A TOTAL CONTRACTOR OF A CONTRACTOR CONTRACTOR OF A C	Holiday
29	Mon	The same of the sa	3
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sincere, hard working and got the Gold modals of the Pouc top ten ranks in all the branches. The details of the Uni ad Top Ten Ranks won by the students are given be \$ 23.467.5930 Blick EEE 2.4.6,7 **9** 3.43.67.9.10 Black ECE 23.436789.10 £ 24.6,7,8.10 Blick CXE 9 2.3.4.10 ×2,3,5,6,8 Black F 234,5,6,78.9.10 \$ 23,45,6,7,49.10 Blich Mck (4.5.7, 9.10 3,7,8,10 2.3,4,6,7,10 Black Cost 2.3.10 \$ 2,6789.10.11 MCA 14,7,9.10 \$ 23.A5.7830 184 \$ 1,4,6.7,8 Mich CE 1 2 3.4.5.7.8.9 幣力 2.3.4.5 Mich ECE 2, 3, 6, 7, 8, 9 923 Mike HD R 23 MEd:NW \$ 2,3.4,5,7,8.9 W 2.5.4 Mildel St 8

Gold Medals and Top Ten Ranks

It is compulsory for all the students to complete Certificate Courses, Skill ment Courses and Mandatory Courses along with their Academic Courses, ourses will enhance the students to upgrade their required skills to cope up

Please space your valuable time to talk to your son/daughter every day it try to understand what he/she is doing in respect of his/her studies. Kindly end all your support to your son/daughter which will help them to coure out coscially. For any assistance from our side you may always feel fire to contact respective DEAN at 0413-2642000 (extu) 2308.

Our canques has been enabled by high speed uninterrupted Wi-Fi connectivity. The Computer Centre is open till 8,00 p.m. on all the working days except on the dates of University examinations.

Library Working Hours

college. The gut students may approach the Charper on a member's or assistant Grievance Redressal Cell There is a Grievance Redressal Cell under the Charmanship of the Durector of to instinuion. Students are requested to approach the Charman's members to redre their grievances. Mail ID: grievance@smycc.ac.in

Annexure - IV



SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)
(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution & Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution & Accredited by NAC with 'A' Grade) Madagadipet, Puducherry - 605 107





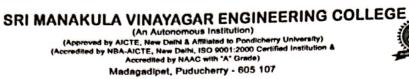
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF STUDENTS AND FACULTIES ENROLLED FOR NPTEL COURSE DURING THE ACADEMIC YEAR 2021 - 2022

PERIOD	DEPARTMENT	FACULTIES	STUDENTS
JAN-APR 2022	EEE	16	111
	12	7	

Page | 46









DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

List of faculty enrolled for NPTEL during the academic year 2021-2022

S. No	Name	Designation	Name of the course registered
1	Dr. S.Anbumalar	Professor & Head	
2	Dr.P.Jamuna	Professor	
3	Dr.D.Raja	Professor	
4	Dr.K.Gowrishankar	Professor	
5	Dr.G.GaneshKumaran	Associate Professor	
6	Dr.M.Jayachandran	Assistant Professor	
7	Mr.A.Janagiraman	Assistant Professor	NBA Accreditation & Teaching And Learning in
8	Mr.K.Thangaraj	Assistant Professor	Engineering
9	Mr.J.Muruganandam	Assistant Professor	[12 Weeks]
10	Mr.C.Adrien Perianayagam	Assistant Professor	
11	Mr.R.Ragupathy	Assistant Professor	
12	Ms.N.Swarnalakshmi	Assistant Professor	
13	Mr.R.Nakkeeran	Assistant Professor	
14	Mr.D.Sivaraj	Assistant Professor	. ,
15	Ms.R.Aarthi	Assistant Professor	
16	Mr.I.Shivashankar	Assistant Professor	Effective Engineering Teaching in Practice [4 Weeks]

List of students enrolled for NPTEL during the academic year 2021-2022

S. No.	Name	Sem	Sec	Name of the c	ourse
1	ABDUL RAZAAK. A	V	Α	Microprocessor and Microcontroller	
2	ABIRAAMI. V	V	Α	Microprocessor and Microcontroller	
3	AKASH. S	V	В	Microprocessor and Microcontroller	
4	AKSHAYA S	V	В	Ethical Hacking	
5	ALASHFAK M	V	В	Microprocessor and Microcontroller	
6	ANAND M V	V	В	Microprocessor and Microcontroller	
7	ANJANA. V	V	Α	Microprocessor and Microcontroller	
8	ANNAMALAI. E	V	Α	Microprocessor and Microcontroller	Analog Circuits
9	ARAVIND. G	V	Α	Microprocessor and Microcontroller	
10	ARAVINDA. C	V	Α	Microprocessor and Microcontroller	7-1
11	ARAVINDHAN. A	V	В	Microprocessor and Microcontroller	
12	ARIPRASATH. N	V	Α	Microprocessor and Microcontroller	
13	ARTHI. A	V	Α	Microprocessor and Microcontroller	
14	ARULEESWARAN.P	V	В	Microprocessor and Microcontroller	
15	ARUNACHALAM.T	V	Α	Microprocessor and Microcontroller	
16	ARVIND.D	V	В	Microprocessor and Microcontroller	

Page | 47

17	ASMA DEC.				
18	ASMA BEGAM. M	V	В	Microprocessor and Microcontroller	
19	BHUVANESH I	V	Α	Microprocessor and Microcontroller	
20	BOMIDI PUJITHA	V	В	Microprocessor and Microcontroller	
21	DEEPA PRAGASAN. V	V	Α	Microprocessor and Microcontroller	
22	DEEPIKA. V	V	Α	Microprocessor and Microcontroller	
23	DEIVAPRASATH.A	V	В	Microprocessor and Microcontroller	
24	DELLI BABU. S	V	В	Microprocessor and Microcontroller	
	DEVANATHAN. J	V	Α	Microprocessor and Microcontroller	
25	DHILIPKUMAR. S	V	Α	Microprocessor and Microcontroller	
26	DHINESHE. S	V	Α	Microprocessor and Microcontroller	7
27	DHIVYADARSHNI. V	V	Α	Microprocessor and Microcontroller	
28	DILEEP PRASATH K	V	В	Microprocessor and Microcontroller	THE REAL PROPERTY.
29	DINESH BABU.A	V	Α	Microprocessor and Microcontroller	1200
30	DINESH KUMAR. M	V	В	Microprocessor and Microcontroller	
31	GOGULARAJ.V	V	Α	Microprocessor and Microcontroller	1/8 17 18
32	GOKULRAJ N	V .	В	Microprocessor and Microcontroller	
33	GOKUL S	V	A	Microprocessor and Microcontroller	1
34	GUGAN. M	V	A	Fundamental of Power Electronics	
35	GUNAVATHI S	V	В	Microprocessor and Microcontroller	
36	HARIHARAN S	V	B	Microprocessor and Microcontroller	375
37	HARI PRASAD K	V ÷	A	Microprocessor and Microcontroller	
38	HARISH, A	V	A	Microprocessor and Microcontroller	
39	HEMALATHA A	- V	A	Microprocessor and Microcontroller	7.7
40	HEMALATHA, V	V	В	Microprocessor and Microcontroller	Ethical hacking
41	HEMAMAALAN, C	V	В	Microprocessor and Microcontroller	- moon macking
42	ISRAK HUSSAIN. S	V 1	В	Microprocessor and Microcontroller	1000
43	IYYAPPAN. M	V	A	Microprocessor and Microcontroller	To the last
		V	B	Microprocessor and Microcontroller	
44	JAGAN P	V	A	Microprocessor and Microcontroller	in the
45	JAGAN.P	V	A	Microprocessor and Microcontroller	
46	JAI GANESH. J	V		Microprocessor and Microcontroller	
47	JAWAHAR. G	1.4	В	'	
48	JEEVAJOTHI. K	V V	В	Microprocessor and Microcontroller	1 1 1 1 1 1
49	JENSY ALBIYA. A.J.	V	В	Microprocessor and Microcontroller	100000
50	KARTHIK. R	V	В	Microprocessor and Microcontroller	
51	KHAJA MOIDEEN S	V	В	Microprocessor and Microcontroller	
52	KIRUTHIGA. C	V 4	Α	Microprocessor and Microcontroller	To produce
53	KISHOR. G	V	Α	Microprocessor and Microcontroller	1,1,41
54	KISHORE.D	V	В	Microprocessor and Microcontroller	1000
55	KUMARAN. S	V	Α	Microprocessor and Microcontroller	1
56	LOGANATHAN S	V	В	Microprocessor and Microcontroller	3 19.00
57	LOGESHWARAN. V	V	Α	Microprocessor and Microcontroller	
58	LOKESWARI. G	V	В	Microprocessor and Microcontroller	1 3 3
	MESHACH. E	V	В	Ethical Hacking	
59	MOHAMED FAWAZ.Y	1 V	Α	Microprocessor and Microcontroller	
60	NANDHINI.C	2 V	Α	Microprocessor and Microcontroller	1
61	NARENDIRAN.A	V	В	Microprocessor and Microcontroller	
62		- V	A	Microprocessor and Microcontroller	
63	NIVETHITHA. R	V	B	Microprocessor and Microcontroller	
64	NIVETHITHAASRI, P.R.	V			
65	OUMAR GOURU. O		A	Microprocessor and Microcontroller	
66	PALEPU SHIVA	V	В	Microprocessor and Microcontroller	

67	PRAVIN. M	V	Α	Microprocessor and Microcontroller	
68	PRIYADHARSHAN. S	٧	В	Microprocessor and Microcontroller	
69	PRIYENGA.E	V	Α	Microprocessor and Microcontroller	
70	RAJARAJAN. D	V	Α	Microprocessor and Microcontroller	
71	RAJMUGILAN. R	V	В	Microprocessor and Microcontroller	
72	RASIN A	V	В	Microprocessor and Microcontroller	
73	RATHINASABAPATHY A	V	A	Microprocessor and Microcontroller	
74	SAKTHI ESWARAN.S	V	В	Microprocessor and Microcontroller	
75	SANCHUNA .S	V	В	Microprocessor and Microcontroller	Ethical hacking
76	SANJAY. M	V	В	Microprocessor and Microcontroller	Luncarnacking
77	SANJAY, M	V	Α	Microprocessor and Microcontroller	
78	SARAVANAKRISHNAN. V	V	В	Microprocessor and Microcontroller	
79	SATHYANARAYANAN.V	V	A	Microprocessor and Microcontroller	
80	SHARAN. S	٧	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
81	SINDHUJA. K	٧	Α	Microprocessor and Microcontroller	Microcorrubiler
82	SIVABALAN S	V	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
83	SIVARAMAN P	V	Α	Microprocessor and Microcontroller	Wild Goot to Gillet
84	SOWMYA S	V	В	Fuzzy Sets, Logic and Systems & Applications	Microprocessor and Microcontroller
85	SUNIL KUMAR. M	, V	В,	Microprocessor and Microcontroller	
86	SURENDAR.G	V	Α	Microprocessor and Microcontroller	
87	SURYAPRAKASH. P	7 V	В	Microprocessor and Microcontroller	
88	THAMARAISELVAN. S	· V	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
89	THAMIZHSELVAN. G	- V	В	Microprocessor and Microcontroller	
90	THAMIZHSELVAN V	٧	Α	Microprocessor and Microcontroller	First say y
91	THANYASRI.S.K	V	Α	Microprocessor and Microcontroller	1 1
92	THIRUMANIRAJ. P	V	Α	Microprocessor and Microcontroller	
93	THIRUMARAN. D	· V	Α	Microprocessor and Microcontroller	
94	VASANTHAKUMAR. R	V	В	Microprocessor and Microcontroller	
95	VETRIVEL.V	V	Α	Microprocessor and Microcontroller	- :
96	VIDHYALAKSHMI E	V	- B	Microprocessor and Microcontroller	1 1 2
97	VIGNESH. K	. V	В	Microprocessor and Microcontroller	
98	VIGNESHWARAN.V	V	Α	Microprocessor and Microcontroller	- 44
99	YOGESH. A	V	В	Microprocessor and Microcontroller	1 ., .
100	YOGESH. R	V	Α	Microprocessor and Microcontroller	
101	YOGESHWAR S	V	Α	Microprocessor and Microcontroller	7
102	ATHMAJAN.S	٧	В	Microprocessor and Microcontroller	
103	DHANUSH.N	V	В	Microprocessor and Microcontroller	Power System Engineering
104	GANESAMURTHY. S	V	Α	Microprocessor and Microcontroller	
105	JAVITH AHAMED.J	V	В	Microprocessor and Microcontroller	
106	MUTHUKUMARAN.R	٧	Α	Microprocessor and Microcontroller	· MIL ·
107	NANTHAKUMAR.B	V	Α	Microprocessor and Microcontroller	
108	NAYAKAN.S.T	V	A	Microprocessor and Microcontroller	7.1
109	SAMEER.B	V	A	Microprocessor and Microcontroller	
110	SETHURAM.S	V	В	Microprocessor and Microcontroller	· ·
	32111017 11110	V		Microprocessor and Microcontroller	

Annexure - V

List of Examiners



SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)
(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)
(Accredited by NBA-AICTE, New Delhi & Accredited by NAAC with "A" Grade)
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