



**SRI MANAKULA VINAYAGAR**  
**ENGINEERING COLLEGE**  
(An Autonomous Institution)  
Puducherry

*2<sup>nd</sup> - Board of Studies Meeting in the department of*  
**Electronics and Communication Engineering**

*for the Programme*  
**M.Tech – Electronics and Communication Engineering**  
**M.Tech – VLSI and Embedded Systems**  
**P.hD – Electronics and Communication Engineering**

*Venue*  
Seminar Hall, Department of ECE  
Sri Manakula Vinayagar Engineering College  
Madagadipet, Puducherry – 605 107

*Date & Time*  
10-04-2021 & 11.30 am

**BOARD OF STUDIES MEETING**

The Second Board of Studies meeting for PG and Research programmes was held on 10<sup>th</sup> April 2021 at 11:30 am in the Seminar Hall, Department of ECE, Sri Manakula Vinayagar Engineering College with the Head of the Department in the Chair.

The following members were present for the BoS meeting in the venue

Sl. No	Name of the Member	Designation
1	<b>Dr. P. Raja</b> Professor and Head, Department of ECE	Chairman
2	<b>Dr. V. R. Vijayakumar</b> Associate Professor & Head, Department of ECE, Anna University, Regional Campus, Coimbatore	Expert Member (Academic Council Nominee)
3	<b>Mr. C. Gnanavel</b> Manager, Production and Technology, Lenovo India Ltd., Puducherry	Industry Member
4	<b>Dr.V.Bharathi</b> , Professor / ECE <b>Specialization:</b> Wireless Communication	Member
5	<b>Dr.R.Ramya</b> , Professor/ ECE <b>Specialization:</b> ECE	Member
6	<b>Dr. J.Pradeep</b> , Associate Professor / ECE <b>Specialization:</b> Image Processing	Member
7	<b>Dr. R.Kurunjimalar</b> , Associate Professor / ECE <b>Specialization:</b> Mobile Satellite Communication	Member
8	<b>Dr. D. Jagadiswary</b> , Associate Professor / ECE <b>Specialization:</b> Biometric Security	Member
9	<b>Prof. R. Ilaiyaraja</b> , Assistant Professor / ECE <b>Specialization:</b> VLSI Design	Member
10	<b>Prof.Egalite Francis</b> , Assistant Professor <b>Specialization:</b> Mathematics	Member
11	<b>Prof. K. Oudayakumar</b> , Associate Professor <b>Specialization:</b> Physics	Member
12	<b>Dr. S. Deepa</b> , Professor <b>Specialization:</b> Chemistry	Member
13	<b>Dr.D.Jaichithra</b> , Associate Professor <b>Specialization:</b> English	Member
14	<b>Mr. Dharanidharan. G</b> Associated Functional Consultant, Birlasoft Limited, Old Mahabalipuram Road, Chennai - 600096	Alumni Member

The following members were present in online plat form

Sl. No	Name of the Member	Designation
1	<b>Dr.Gerardine Immaculate Mary</b> Professor, Department of Embedded Systems, Vellore Institute of Technology (VIT), Vellore, Tamil Nadu, India	Expert Member (University Nominee)
2	<b>Dr. N. Venkateswaran</b> Professor, Department of ECE, SSN - College of Engineering, Kalavakkam, Tamil Nadu, India	Expert Member (Academic Council Nominee)

## AGENDA OF THE MEETING

### Item No. : BoS / PG / ECE 2.1

To discuss and review the minutes of the first BoS meeting held on 18-07-2020

### Item No. : BoS / PG / ECE 2.2

To consider and ratify the Common Courses and Audit courses introduced in PG programmes from the Academic Year 2020-21

### Item No. : BoS / PG / ECE 2.3

To consider and approve the students admitted in the Academic Year 2020-21

### Item No. : BoS / PG / ECE 2.4

To consider and approve the admission process introduced in research program (Ph.D- Electronics and Communication Engineering)

### Item No. : BoS / PG / ECE 2.5

To appraise about the recognized research guides in the department of Electronics and Communication Engineering.

### Item No. : BoS / PG / ECE 2.6

To discuss and appraise the Examiners for M.Tech programs

### Item No. : BoS / PG / ECE 2.7

Consideration of review on the revised Program Educational Objectives (PEOs) and Program Specific Outcomes (PSOs) of the M. Tech programs

### Item No. : BoS / PG / ECE 2.8

Any other item with the permission of chair

## MINUTES OF THE MEETING

Dr. P. Raja, Chairman, BoS opened the meeting with warm welcome and thanked all the Members for accepting the second BoS meeting Invitation for the M.Tech and Research programmes. The Chairman proceeded the meeting subsequently and discussed the agenda items.

### BoS / PG / ECE 2.1

**To discuss and review the minutes of the first BoS meeting held on 18-07-2020**

The first BoS Meeting for M.Tech.- Electronics and Communication Engineering and M. Tech – VLSI and Embedded Systems under Regulations 2020 held on 18-07-2020 and confirmed the following points

### Confirmation of M.Tech Regulations 2020

<b>M.Tech – Electronics and Communication Engineering</b> (Student admitted in AY 2020-21)	<b>M.Tech – VLSI and Embedded Systems</b> (Student admitted in AY 2020-21)
<ul style="list-style-type: none"><li>• Number of credits: 72</li><li>• Course structure of the programme</li><li>• Curriculum for I to IV Semesters</li></ul>	<ul style="list-style-type: none"><li>• Number of credits: 72</li><li>• Course structure of the programme</li><li>• Curriculum for I to IV Semesters</li></ul>

<ul style="list-style-type: none"> <li>Approved the syllabi for the semesters I to IV</li> </ul>	<ul style="list-style-type: none"> <li>Approved the syllabi for the semesters I to IV</li> </ul>
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- Evaluation system followed for courses offered in M.Tech programmes
- Internal mark evaluation based on student activity in every course was appreciated

#### Suggestions and action taken on M.Tech – Electronics and Communication Engineering

Suggestion given by the Member	Action Taken
Suggested to include “simulation of different modulation” using different tools based on the instructional of the course of “Advanced Digital Communication”	Included the topics in the course of “Advanced Digital Communication”
Suggested to introduce the course “Image and Video processing” instead of “Advanced Digital Signal Processing”	Course was introduced in semester - II
Suggested to remove the course “Advanced Electromagnets” and instead of introduce “Millimeter Wave Communication Networks” in the curriculum	Course “Millimeter Wave Communication Networks” was introduced in semester - II
Suggested to publish papers in reputed journals	Made mandatory to publish the paper in reputed Journals and included in regulation
Introduce the course of “Advanced Technologies in Wireless Networks” as elective course	Introduced the course of “Advanced Technologies in Wireless Networks” in professional elective – V group

#### Suggestions and action taken on M.Tech – VLSI and Embedded Systems

Suggestion given by the Member	Action Taken
Common and Audit courses are needed to be introduced	Discussed with other department and incorporated
Suggested to increase number of elective papers in Semester-III	Increased the elective papers from 2 to 3
Suggested to publish papers in reputed journals	Made mandatory to publish the paper in reputed Journals and included in regulation
Suggested to include the course PLC-SCADA in elective	Included the course in “Industrial Automation using PLC and SCADA” as elective
Suggested to invite Industrial Expert for Project Review-III	Process initiated and to be implemented from the academic year 2021-22

**Minutes are Reviewed and Confirmed**

**BoS / PG /  
ECE 2.2**

**To consider and ratify the Common Courses and Audit courses introduced in PG programmes from the Academic Year 2020-21**

The following common courses are introduced in semester I and II in all M.Tech programmes as per guidelines of AICTE model curriculum.

Semester	Course Code	Course Title	Objective of the Course
1	P20CCT101	Research Methodology and IPR	<ul style="list-style-type: none"> <li>To impart knowledge and skills required for research and IPR</li> <li>Problem formulation, analysis and solutions</li> <li>Technical paper writing / presentation without violating professional ethics</li> <li>Patent drafting and filing patents</li> </ul>
1	P20CCP101	Technical Report Writing and Seminar	<ul style="list-style-type: none"> <li>Selection of topic based on interest</li> <li>Formulate the Objective</li> <li>To develop their scientific and technical reading and writing skills by which they need to understand and construct research articles.</li> <li>To obtain information from a variety of sources (i.e., Journals, dictionaries, reference books) and then place it in logically developed ideas.</li> <li>Preparation of report</li> </ul>
2	P20CCP202	Seminar on ICT a hands on approach	<ul style="list-style-type: none"> <li>To develop their technical reading and presentation skills that they need to understand and present using ICT Tools.</li> <li>To obtain information from a variety of sources (i.e., Journals, dictionaries, reference books) and practice to present.</li> </ul>

### Audit Courses

The All India Council for Technical Education (AICTE) introduced Audit courses in M.Tech programmes covering subjects of developing desired attitude among the learners is on the line of initiatives such as Unnat Bharat Abhiyan, Yoga, Value education, Disaster management, Sanskrit, Pedagogy, Constitution of India, Personality development through Indian culture etc. The students are asked to complete minimum two audit courses in duration of the program.

Sl. No	Course Code	Course Title
1	P20ACTX01	English for Research Paper Writing
2	P20ACTX02	Disaster Management
3	P20ACTX03	Sanskrit for Technical Knowledge
4	P20ACTX04	Value Education
5	P20ACTX05	Constitution of India
6	P20ACTX06	Pedagogy Studies
7	P20ACTX07	Stress Management by Yoga
8	P20ACTX08	Personality Development Through Life Enlightenment Skills
9	P20ACTX09	Unnat Bharat Abhiyan

A common courses mentioned above had been introduced in Semester I & II and audit courses from the academic year 2020-21 under Regulations 2020.

**Considered and ratified changes made in M.Tech curriculum and syllabi for Common and Audit Courses**

**BoS / PG / ECE 2.3**      **To consider and approve the students admitted in the Academic Year 2020-21**

The details of the students admitted in M.Tech – Electronics & Communication Engineering and M. Tech – VLSI and Embedded Systems in the academic year 2020-21

Name of the Programs	Number of students admitted
M.Tech – Electronics and Communication Engineering	2
M.Tech – VLSI and Embedded Systems	4
<b>Total Number of Students</b>	<b>6</b>

Overall admission for the academic year 2020-21 is 13 %.

Student admitted details are given in Annexure given in **Annexure - I**

**Considered and Approved**

**BoS / PG / ECE 2.4**      **To consider and approve the admission process introduced in research program (Ph.D- Electronics and Communication Engineering)**

Pondicherry University recommended to admit the research scholar by conducting national entrance examination as per the UGC Guidelines and Admission to be done based on the Merit list and reservation Policy

**Overview of Research Program**

<b>Regulation</b>	<b>2020</b>
<b>Eligibility</b>	M.E. / M.Tech. / M.S. (By Research) in the relevant branch of Engineering or Technology
<b>Categories of Ph.D Scholars</b>	Full time Research Scholar
	Part time Research Scholar
<b>Mode of Selection</b>	<ul style="list-style-type: none"> <li>Entrance Examinations shall be conducted as written test for eligible candidates</li> <li>Conduct the interview for the selection of the Ph.D. candidates</li> </ul>
<b>Duration of the Research</b>	Part-Time candidates, both Internal and External, shall have to complete a minimum of three years from the date of registration
	All candidates shall publish minimum of two research paper in UGC approved journals or standard journal
<b>Doctoral committee</b>	Doctoral committee should conduct Twice per year and four DC meeting required for submission of synopsis
<b>Course of Study</b>	<ul style="list-style-type: none"> <li>Two theory ( each 4 credits ) subject of course work that is prescribed by the Doctoral Committee for the scholar to undergo as a part of the programme research</li> <li>Research Methodology course is mandatory</li> <li>The scholars shall secure a CGPA of 7.0 on 10-point scale in the course work</li> </ul>

<b>Extension of Research</b>	<ul style="list-style-type: none"> <li>• <b>Maximum duration: six years</b>, Extension of time three months prior to the completion of six years with the recommendation of the Supervisor</li> <li>• Maximum grace period of one year, beyond the normal maximum period of 6 years shall be granted by the Director to enable the scholar to submit the Synopsis and Thesis</li> </ul>
<b>Doctoral committee</b>	Doctoral committee should conduct Twice per year and four DC meeting required for submission of synopsis

**Noted and Approved**

<b>BoS / PG / ECE 2.5</b>	<b>To appraise about the recognized research guides in the department of Electronics and Communication Engineering</b>
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Research Committee is constituted as per UGC guidelines and committee is scrutiny the "Research Supervisor" application based on the eligibility criteria. The following faculty members are recognized as "Research Guide/ Supervisor" in the department of Electronics and Communication Engineering.

<b>S. No</b>	<b>Name of the Research Supervisor</b>	<b>Designation</b>
1	Dr. P. Raja	Professor and Head
2	Dr. L. M. Varalakshmi	Professor
3	Dr. V. Bharathi	Professor
4	Dr. R. Ramya	Professor
5	Dr. A. Vijayalakshmi	Professor
6	Dr.A.Jayachitra	Professor
7	Dr.R.Kurinjimalar	Associate Professor
8	Dr.J.Pradeep	Associate Professor

**Noted and Apprised**

<b>BoS / PG / ECE 2.6</b>	<b>To discuss and appraise the Examiners for M.Tech programs</b>
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The list of Examiners are discussed and given in **Annexure-II**

**Noted and Apprised**

<b>BoS / PG / ECE 2.7</b>	Consideration of review on the revised Program Educational Objectives (PEOs) and Program Specific Outcomes (PSOs) of the M. Tech programs
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Feedback was collected from the stakeholders for the revision of Program Educational Objectives (PEOs), and Program Specific Outcomes (PSOs) of the PG programs.

Revised PEOs and PSOs are given in Annexure given in <b>Annexure - III</b>
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**Considered and Approved**

**BoS / PG / ECE 2.8**    **Any other item with the permission of chair**

- Introduction of new M.Tech program in Cyber Security Systems and Networks by changing the name of M.Tech – Electronics and Communication Engineering with the intake of 24.
- The M.Tech program in Cyber Security Systems and Networks is going to be introduced from the academic year 2021-22. In this regard, the Chairman of BoS submitted the overview of curriculum for kind consideration and suggestions

**Considered and recommended to introduce the new M.Tech – Cyber Security Systems and Networks**

Dr. P. Raja, Chairman – BoS and Head of Department, Electronics and Communication Engineering, concluded the meeting at 12:30 pm with vote of thanks.



**Dr. P. RAJA**  
Board Chairman - ECE



**Dr. GERARDINE IMMACULATE MARY**  
Professor, Department of Embedded Systems,  
Vellore Institute of Technology (VIT), Vellore  
(Expert Member - University Nominee)



**Dr. N. VENKATESWARAN**  
Professor, Department of ECE,  
SSN College of Engineering, Kalavakkam  
(Expert Member – AC Nominee)



**Dr. V. R. VIJAYAKUMAR**  
Associate Professor & Head, Department of  
ECE, Anna University, Regional Campus,  
Coimbatore  
(Expert Member – AC Nominee)



**Mr. C. GNANAVEL**  
Manager, Production and Technology,  
Lenovo India Ltd., Puducherry  
(Industry Member)



**Mr. DHARANIDHARAN. G**  
Associated Functional Consultant,  
Birlasoft Limited, Chennai  
(Alumni Member)





**Dr. V. BHARATHI**  
Professor / ECE  
(Member)



**Dr. J. PRADEEP,**  
Associate Professor / ECE  
(Member)



**Dr. D. Jagadiswary**  
Associate Professor / ECE  
(Member)



**Prof. EGALITE FRANCIS**  
Assistant Professor / Mathematics  
(Member)



**Dr. S. DEEPA**  
Professor / Chemistry  
(Member)



**Dr. R. RAMYA**  
Professor/ ECE  
(Member)



**Dr. R. KURUNJIMALAR**  
Associate Professor / ECE  
(Member)



**Prof. R. ILAIYARAJA,**  
Assistant Professor / ECE  
(Member)



**Prof. K. OUDAYAKUMAR,**  
Associate Professor / Physics  
(Member)



**Dr. D. JAICHITHRA**  
Associate Professor  
(Member)



**SRI MANAKULA VINAYAGAR**  
**ENGINEERING COLLEGE**

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Puducherry

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Student Admitted in M. Tech – Electronics and Communication Engineering**

<b>S. No</b>	<b>Enroll No</b>	<b>Name of the Student</b>
1	20098226	S.Priyadharshini
2	20098559	M.Vidhya Bharathi

**Student Admitted in M. Tech – VLSI and Embedded Systems**

<b>S. No</b>	<b>Enroll No</b>	<b>Name of the Student</b>
1	20098456	Bharathi. G
2	20098436	Dhanush. R
3	20098142	Gayathri. S
4	20098221	Sivashankari . S



## **SRI MANAKULA VINAYAGAR**

### **ENGINEERING COLLEGE**

(An Autonomous Institution)

Puducherry

### **DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

#### **Consolidated Examiners List**

<b>S. No</b>	<b>Name of the Examiner</b>	<b>Highest Qualification</b>	<b>Specialization</b>	<b>Experience (in Years)</b>	<b>Communication Address</b>	<b>Email ID with Mobile Number</b>
1.	Dr.T.Shankar	Ph.D	Wireless Communication, Networks	19	Professor, Department of Communication Engineering, School of Electronics Engineering (SENSE), VIT University, Vellore- 632 014	tshankar@vit.ac.in 9486282025 7810972776
2.	Dr.K.Gunavathi	Ph.D	VLSI Design, Communication Systems	32	Professor, Department of Electronics and Communication Engineering, PSG college of Technology Peelamedu, Coimbatore -641004	kgunavathi2000@yahoo.com 9486399299
3.	Dr.G.Umamaheswari	Ph.D	Communication Engineering, Wireless Network Security	28	Professor, Department of Electronics and Communication Engineering, PSG college of Technology Peelamedu, Coimbatore -641004	uma@ece.psgtech.ac.in 9944449295
4.	Dr.S.J.Thiruvengadam	Ph.D	Signal processing Wireless communication	25	Professor, Department of ECE, Thiagarajar College of Engineering, Thiruparankundram, Madurai Tamil Nadu 625015	sjtece@tce.edu 9865079402

5.	Dr.V.Janakiraman	Ph.D	VLSI, Signal processing	20	Professor & Vice Principal Department of Electronics and Communication Engineering Dhanalakshmi Srinivasan College of Engineering and Technology, East Coast Road, Mallapuram, Chennai, Tamil Nadu 603104	vjramece@gmail.com 9444255029, 7358374100
6.	Dr.V.Kamatchi Sundari	Ph.D	Image Processing	20	Professor Department of ECE Prince Shri Venkateshwara Padmavathy Engineering College, Mambakkam - Medavakkam Main Rd, Ponmar, Chennai, Tamil Nadu 600127	vkamatchisundari@gmail.com 9952041393
7.	Dr. P. Vijayakumar	Ph.D	Wireless Communication, Network Security	13	Associate Professor, Department of Electronics Engineering, Vellore Institute of Technology, Kelambakkam - Vandalur Rd, Rajan Nagar, Chennai, Tamil Nadu 600127	vijayrgcet@gmail.com 9894727271
8.	Dr.A.Rajesh	Ph.D	Wireless Communication	8	Associate Professor Department of Communication Engineering School of Electronics Engineering VIT University, Vellore Campus, Tiruvalam Rd, Katpadi, Vellore, Tamil Nadu 632014	rajesha@vit.ac.in 9994220967
9.	Dr J.Raja	Ph.D	Digital Communication	30	Professor & Head Department of Electronics and Communication Engineering Sri Sairam Engineering College, Sai Leo Nagar, West Tambaram, Chennai - 600 044	rajaj.ece@sairam.edu.in 9444477858
10.	Dr.Rajarajan	Ph.D	Antenna Design, Networks	23	Professor & Head Department of Electronics and Communication Engineering, Sri Sairam Institute of Technology,Sai Leo Nagar, West Tambaram, Chennai - 600 044	rajarajan.ece@sairamit.edu.in 7708728553
11.	Dr.V.P.Harigovindan	Ph.D	Wireless Communication, Networks	11	National Institute of Technology Puducherry, Thiruvettakudy Karaikal – 609609	hari.guruvayoor@gmail.com 9495314044

12.	Dr.S.Muthumanickam	Ph.D	Antennas and Wave Propagation	18	RMK college of Engineering and technology ,Puduvoyal Kavaraipettai - 601206	mthusushant@gmail.com 99627119921
13.	Dr.B.Ramani	Ph.D	Speech technology, Signal Processing	15	SRM Valliammai Engineering College, National Highway 45, Potheri, SRM Nagar, Kattankulathur, Tamil Nadu 603203	ramani8981@gmail.com 9962521711
14.	Dr.B.Partibane	Ph.D	Wireless Communication	15	Dept of ECE, SSN College of Engineering, Old Mahabalipuram Rd, OMR, Kalavakkam, Tamil Nadu 603110 Chennai	partibaneb@ssn.edu.in 6383612183
15.	Ms. K.Arthi	Ph.D(Pursuing)	VLSI Design	6	Dept of ECE, SRM Valliammai Engineering College, National Highway 45, Potheri, SRM Nagar, Kattankulathur, 603203	athik.ece@valliammai.co.in 9751352711
16.	Ms. V.JeanShilpa	M.E	Applied Electronics	12	B.S Abdur Rahman Crescent Institute of Science and technology, Vandalur, Chennai	jeanshilpa@gmail.com 9655537103
17.	Dr.V. P Harigovindan	Ph.D	Wireless communication, Wireless Networks	15	Associate Professor, Department of Electronics and Communication Engineering National Institute of Technology, Karaikal-609605	hari@nitpy.ac.in 9495314044
18.	Dr.K. Thenmozhi	Ph.D	Power Electronics	24	Dean, School of Electrical & Electronics Engineering Shanmugha Arts, Science, Technology & Research Academy SASTRA Deemed University, Tirumalaisamudram, Thanjavur – 613 401	thenmozhi@ece.sastra.edu 9994106100
19.	Dr.P.T.Vanathi	Ph.D	VLSI Design, Signal Processing, Wireless Sensor Networks	20	Associate Professor PSG College of Technology Avinashi Rd, Peelamedu, Coimbatore, Tamil Nadu 641004	vanathi@psgtech.ac.in 9486438516
20.	Dr. R. Ramesh	Ph.D	Digital signal processing, Field theory	22	Principal, Saveetha Engineering college, Thandalam, Chennai-602105	ramesh@saveetha.ac.in 9791072604
21.	Dr.R.Saravanakumar	Ph.D	VLSI DESIGN	20	Assistant Professor –Level III Department of Electronics and Communication Engineering	saravanskumar@bitsathy.ac.in 9894016318

					Bannari Amman Institute of Technology, Sathyamangalam, Erode .TN	
22.	Dr.Srigitha.S.Nath	Ph.D	Applied Electronics	22	Associate Professor, Head Of the department, Department of Electronics and Communication Engineering SaveethaEngineering college, Thandalam,Chennai-602105	hod.ece@saveetha.ac.in 9840367678
23.	Dr. T. Aravind	Ph.D	MEMS, VLSI Design	12	Department of Electronics and Communication Engineering SaveethaEngineering college, Thandalam,Chennai-602105	aravind@saveetha.ac.in 948803369
24.	Dr. S. Praveen Kumar	Ph.D	MEMS, BIO-MEMS, Nano Technology	18	Department of Electronics and Communication Engineering SaveethaEngineering college, Thandalam, Chennai-602105	praveenkumar@saveetha.ac.in 9894921193
25.	Dr.M.Vanitha	Ph.D	Embedded system	18	Associate Professor, Department of Electronics and Communication Engineering SaveethaEngineering college, Thandalam,Chennai-602105	vanitha@saveetha.ac.in 7871784886
26.	Dr.S.Jayalalitha	Ph.D	Linear Integrated Chip	22	Dean, School of Electrical & Electronics Engineering Shanmugha Arts, Science, Technology & Research Academy, SASTRA Deemed University, Tirumalaisamudram, Thanjavur – 613 401 Tamilnadu	sj_instru@eie.sastra.edu 9942408037
27.	Dr.N.UshaBhanu	Ph.D	Signal & Image processing	23	Dept of ECE, SRM Valliammai Engineering College, National Highway 45, Potheri, SRM Nagar, Kattankulathur, 603203	Ushabhanu123@gmail.com 9382207816
28.	Ms.R.Anitha	M.E	Applied Electronics	12	Dept of Electronics and Communication Engineering, B. S. Abdur Rahman Crescent Institute Of Science and Technology, Vandalur, Tamil Nadu 600048	r.anitha@crescent.education 8681930515
29.	Dr.K.Nirmaladevy	Ph.D	Low Power VLSI	12	KCG College of Technology, Karapakkam, Chennai - 600097	<a href="mailto:Nirmaladevy@kcg.com">Nirmaladevy@kcg.com</a> 9677781647

30.	Dr.G.Renganayaki	Ph.D	VLSI Design	19	KCG College of Technology, Karapakkam,Chennai - 600097.	renganayakigurusamy@gmail.com, 9245111212
31.	Mr.T.Thomaslenoid	Ph.D(Pursuing)	ECE	11	KCG College of Technology, Karapakkam, Chennai - 600097.	thomasleonid@gmail.com 9600341804
32.	Mr. M.MohamedYaseen	Ph.D(Pursuing)	ECE	16	KCG College of Technology, Karapakkam, Chennai - 600097.	yas16479@gmail.com 8124219200
33.	Dr.JoseAnand	Ph.D	VLSI	18	KCG College of Technology, Karapakkam, 600097.	joseanandme@yahoo.co.in, 9894280100
34.	Dr.T.Sripriya	Ph.D	ECE	16	Saveetha Engineering College, Saveetha Nagar, Thandalam, Vellore - Chennai Rd, Chennai, Tamil Nadu 602105	sripriya@saveetha.ac.in, 9600037163
36.	Mrs.N.Subhashini	Ph.D(Pursuing)	Wireless Communication	19	SRM Valliammai Engineering college,National Highway 45, Potheri, SRM Nagar, Kattankulathur, Tamil Nadu 603203	subhashini.nk@gmail.com, 9994742868
37.	Dr. J. Mohan	Ph.D	Bio-medical Signal and Image Processing	18	SRM Valliammai Engineering college,National Highway 45, Potheri, SRM Nagar, Kattankulathur, Tamil Nadu 603204	mohanjece@valliammai.co.in, 9840791532
38.	Dr.S.Kavitha	Ph.D	Image Processing	18	Nandha Engineering college Perundurai, Main Road, VaikkaalmeduErode, Tamil Nadu 638052	gskkavitha@gmail.com, 9942215651
39.	Dr. M. Karthikeyan	Ph.D	MIMO detection	6	Assistant Professor (Sr), Vellore institute of technology VIT, Vellore Campus, Tiruvalam Rd, Katpadi, Vellore,Tamil Nadu 632014.	karthikeyanfun@gmail.com +91 8870458556 +91 9100973372
40.	Mr. K. Pandikumar	Ph.D (Pursuing)	Image Processing	10	Assistant Professor Dhanalakshmi College of Engg. Dr. V. P. R Nagar, Off. TambaramSriperumbudur Road Manimangalam Post, Chennai - 601 301	mailtopandikumar@gmail.com + 91 9840287653
41.	Dr. P. Manimaran	Ph.D	Opto Electronic Devices & Optical Communication	10	Vellore Institute of Technology VIT, Vellore Campus, Tiruvalam Rd, Katpadi, Vellore, Tamil Nadu 632014	maran_pm@yahoo.co.in 9840989145

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**ENGINEERING COLLEGE**  
(An Autonomous Institution)  
Puducherry

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**M.TECH. - VLSI AND EMBEDDED SYSTEMS**

**PROGRAMME OUTCOMES (POs)**

**PO1: Exploration of Research:** An ability to independently carry out research/investigation and development work to solve practical problems.

**PO2: Technical Skill:** An ability to write and present a substantial technical report/document.

**PO3: Expertise in Academics:** Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

**PO4: Problem solving:** An ability to discriminate, analyze, evaluate and synthesize the technologies to provide solution for multidimensional engineering problems.

**PO5: Usage of Modern Tools:** Create, select, learn and apply appropriate techniques, resources, and modern engineering and IT tools, including prediction and modelling, to complex engineering activities with an understanding of the limitations.

**PO6: Ethical Practices and Social Responsibility:** Acquire professional and intellectual integrity, professional code of conduct, ethics of research and scholarship, consideration of the impact of research outcomes on professional practices and an understanding of responsibility to contribute to the community for sustainable development of society.

## **PROGRAM EDUCATIONAL OBJECTIVES (PEOs)**

**PEO1: Technical Knowledge** :To Provide a solid basis and experience in the field of VLSI and Embedded System to meet the unique needs of the society

**PEO2: Leadership Skill** : To develop good leadership skills in the respective domain to perform creative and collaborative work

**PEO3: Research and Development** :To inculcate practical knowledge to undertake quality interdisciplinary research projects addressing industrial and social needs

**PEO4: Professional Behavior**: To train students to be an active collaborators and responsible engineers to solve social, technical and engineering challenges.

## **Program Specific Outcomes (PSOs)**

**PSO1-Technical Knowledge in VLSI and Embedded Systems** : Ability to apply cutting-edge technology to solve complex problems and provide an optimized solution in the domain of VLSI and Embedded Systems.

**PSO2- Competency in VLSI Design** : Ability to design and test systems by applying the concepts of FPGA, ASIC, System On Chip etc

**PSO3-Competency in Embedded System** :Ability to develop and analyze Embedded Systems to solve real time societal problems.



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**M.TECH. – Electronics and Communication Engineering**

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## **PROGRAM EDUCATIONAL OBJECTIVES (PEOs)**

**PEO1: Technical Knowledge** : To develop intellectual combination of technology with modern electronics and communication systems through well-built technical acquaintance

**PEO2: Leadership Skill** : To endure changes and challenges in the areas of Electronics and Communication Engineering with good leadership skills

**PEO3: Research and Development** : To identify the requisite of the nation, industry and come out with innovative solutions to maintain a sustainable position

**PEO4: Professional Behavior:** To promote competitive graduates global wise in the area of Electronics and Communication Engineering

## **PROGRAM SPECIFIC OUTCOMES (PSOs)**

**PSO1- Technical Knowledge in Electronics and Communication Engineering** :Ability to understand the technological advancements in the field of electronics and communication by using modern design tools and sub system end processes.

**PSO2- Competency in Electronics** : Apply research ideas to offer solutions for extant problems in areas including signal processing, image processing, consumer electronics, VLSI, Embedded with given requirements

**PSO3- Competency in Communication:**Ability to develop and provide optimal solutions to subsystems like RF, baseband of modern communication systems and networks.