



SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)

(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution &
Accredited by NAAC with "A" Grade)

Madagadipet, Puducherry - 605 107



Department of Computer Science and Engineering

Minutes of 3rd BoS Meeting (PG)

Venue

EEE 202 Lecture Hall, Department of CSE,
Sri Manakula Vinayagar Engineering College
Madagadipet, Puducherry – 605 107

Date & Time

21st Aug 2021 at 10:45 A.M



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Department of Computer Science and Engineering

Minutes of 3rd Board of Studies Meeting (PG and Ph.D)

The third Board of Studies meeting of Department of Computer Science and Engineering was held on 21st Aug 2021 at 10:45 A.M in the EEE 202 Lecture Hall, Department of CSE, Sri Manakula Vinayagar Engineering College with the Head of the Department in the Chair.

The following members were present for the BoS meeting

Sl.No	Name of the Member with Designation and official Address	Responsibility in the BoS
1	Dr. K.Premkumar, Professor, Department of CSE, SMVEC	Chairman
2	Dr. S. R. Balasundaram, Professor and Head Department of Computer Applications, National Institute of Technology, Trichy.	Pondicherry University Nominee
3	Dr. Chokkalingam Subramanian, Professor & Head, Department of Information Technology, Saveetha University, Chennai.	Academic Council Nominee
4	Dr.S.Udhayakumar, Professor, Department of Computer Science and Engineering, Saveetha University, Chennai.	Academic Council Nominee
5	S.Diwahar, M.Tech., Senior Engineer, Dell Technologies, Bangalore	Member
6	R.Sakthi Murugan, Director, Interjet India Pvt. Ltd., Puducherry.	Member
7	Dr. N. Danapaquiame, Professor, Department of CSE, SMVEC.	Member
8	Dr.E.Kodhai, Professor, Department of CSE, SMVEC.	Member
9	Dr.P.Iyappan, Associate Professor, Department of CSE, SMVEC	Member
10	Dr.V.Vijayakumar, Associate Professor, Department of CSE, SMVEC	Member
11	Dr.M.A.Ishrath Jahan Associate Professor, Department of English, SMVEC	Member
12	Dr.T.Jayavarthan Professor, Department of Physics, SMVEC	Member
13	Prof.M.Rajeswari, Assistant Professor, Department of Chemistry, SMVEC	Member
14	Prof.K.Raja, Assistant Professor, Department of Mathematics, SMVEC	Member

Agenda of the Meeting

Item No. : BoS / 2021 / CSE / PG / 3.1
Welcome Address, Introduction about the Institution, Department and BoS Members.
Item No. : BoS / 2021 / CSE / PG / 3.2
Confirmation of minutes of II BoS meeting and Discuss about any updation needed in the curriculum Structure and syllabus of M.Tech – Computer Science and Engineering.
Item No. : BoS / 2021 / CSE / PG / 3.3
To discuss about any updation needed in the M.Tech. Degree Regulations 2020 (R-2020), Curriculum and approval of the syllabus from III to IV semesters for the M.Tech – Computer Science and Engineering and the students admitted in the Academic Year 2020-21. (Second Year)
Item No. : BoS / 2021 / CSE / PG / 3.4
To discuss and recommend the panel of examiners to the Academic Council.
Item No. : BoS / 2021 / CSE / PG / 3.5
To discuss and approve the Ph.D Programme in Computer Science and Engineering for the admission criteria and the course work structures to be followed as norms.
Item No. : BoS / 2021 / CSE / PG / 3.6
Any other item with the permission of chair.

Dr. K. Premkumar, Chairman, BoS / Computer Science and Engineering officially announced the opening of the meeting and welcomed the members. He also thanked them for accepting the invitation and giving us their valuable time. The meeting thereafter deliberated on agenda items that had been approved by the Chairman.

Minutes of the Meeting

Dr. K. Premkumar, Chairman, BoS / Computer Science and Engineering officially announced the opening of the meeting. The meeting thereafter deliberated on agenda items that had been approved by the Chairman.

Item: BoS Chairman of Computer Science and Engineering department welcomed the board members. He also thanked them for accepting the invitation and giving us their valuable time.
BoS/ 2021/ CSE/ PG/ 3.1

Confirmation of minutes of II BoS meeting held on 10.04.21 and reviewed about the curriculum Structures and syllabus of M.Tech - Computer Science and Engineering. The following changes are incorporated in the syllabus as per the members' suggestion.

S. No	Regulation	Semester	Subject Name with code	Unit	Particulars
1	R-20	III Semester Elective	Mobile Application Development / III Semester Elective	I, II	Combine Unit I & II. Focus on Android.
2	R-20	III Semester Elective	Blockchain and Cryptocurrency / III Semester Elective	I, III	Remove Unit III and include UNIT I as applications of Blockchain
3	R-20	III Semester Elective	Game Design and Augmented Reality / III Semester Elective	I, II, III, IV, V	Reduce the whole syllabus to light weight.
4	R-20	III Semester Elective	Mobile and Pervasive Computing / III Semester Elective	I, II, IV	Reframe the units
5	R-20	III Semester Elective	Advances in Data Bases / III Semester Elective	V	Need to include graph data model

Item: BoS/ 2021/ CSE/ PG/ 3.2


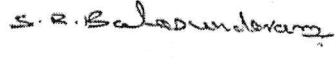
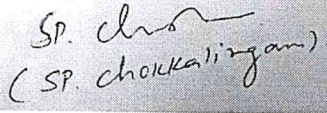

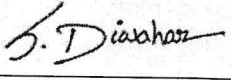
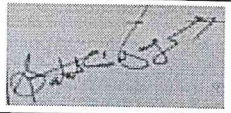



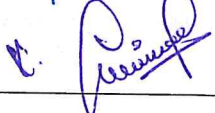


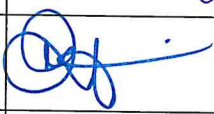
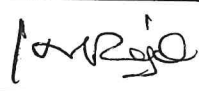
The Curriculum and syllabus of Regulation 2020 were revised as per members' suggestions and the same were approved by the academic council and confirmed by the BoS members.

Item: BoS/ 2021/ CSE/ PG/ 3.3	The M.Tech Degree curriculum and syllabus modification for III and IV semesters under Autonomous Regulations 2020 for the M.Tech programme and the students admitted in the AY 2020-21 were discussed and recommended without any modifications.
Item: BoS/ 2021/ CSE/ PG/ 3.4	Discussed and recommend the panel of examiners to the Academic Council. It was approved by BoS members and enclosed in Annexure- I.
Item: BoS/ 2021/ CSE/ PG/ 3.5	<p>The Regulations for Ph.D. programme in R-2020, were discussed.</p> <p>The revisions in the Ph.D. regulations are listed below.</p> <ul style="list-style-type: none"> • List of PG programmes eligible for admission into Ph.D can adopt the same norms followed in the Pondicherry University. • Two theory subject (each 4 credits) of course work in the research area that is prescribed by the Doctoral Committee for the scholar to undergo as a part of the programme research. • Two courses such as research methodology and publication ethics (each 3 credits) shall be prescribed to the research scholar by the recommendations of the Doctoral Committee. • Two publications in the SCI indexed journals and one International conference presentation are essential for the submission of synopsis.
Item: BoS/ 2021/ CSE/ PG/ 3.6	Nil

The meeting for the above Agenda regarding M.Tech – Computer Science and Engineering was concluded by 1:30 pm with by **Dr. K. Premkumar**, Chairman-BOS and Head of Department, Department of Computer Science and Engineering, Sri Manakula Vinayagar Engineering College.



Dr. K. Premkumar
Chairman / BoS - CSE

Sl.No	Name of the Member with Designation and official Address	Responsibility in the BoS	Signature
1	Dr. K.Premkumar, Professor, Department of CSE, SMVEC	Chairman	
External Members			
2	Dr. S. R. Balasundaram, Professor and Head Department of Computer Applications, National Institute of Technology, Trichy.	Pondicherry University Nominee	
3	Dr. Chokkalingam Subramanian, Professor & Head, Department of Information Technology, Saveetha University, Chennai.	Academic Council Nominee	
4	Dr.S.Udhayakumar, Professor, Department of Computer Science and Engineering, Saveetha University, Chennai.	Academic Council Nominee	
5	S.Diwar, M.Tech., Senior Engineer, Dell Technologies, Bangalore	Member	
6	R.Sakthi Murugan, Director, Interjet India Pvt. Ltd., Puducherry.	Member	
Internal Members			
7	Dr.N. Danapaquame, Professor, Department of CSE, SMVEC.	Member	
8	Dr.E.Kodhai, Professor, Department of CSE, SMVEC.	Member	
9	Dr.P.Iyappan, Associate Professor Department of CSE, SMVEC	Member	
10	Dr.V.Vijayakumar, Associate Professor, Department of CSE, SMVEC	Member	
Co-opted Members			
11	Dr.M.A.Ishrath Jahan Associate Professor, Department of English, SMVEC	Member	
12	Dr.T.Jayavarthanam Professor, Department of Physics, SMVEC	Member	
13	Prof.M.Rajeswari, Assistant Professor, Department of Chemistry, SMVEC	Member	
14	Prof.K.Raja, Assistant Professor, Department of Mathematics, SMVEC	Member	

ANNEXURE - I



Department of Computer Science and Engineering

Details of Examiners for Question Paper Setter and Evaluators

Sl.No	Name of the Examiner	Specialization	Designation, Department and Institution in which currently working	Contact number and mail id
1	Dr. K. RAJA	Computer Graphics, Computer Networks, Multimedia	Assistant Professor / IT, Annamalai University, Chidambaram	9894304053, rajak_cdm@yahoo.co.in
2	Dr.B.MURUGANANTHAM	Artificial Intelligence, Service Oriented Architecture, Webservices	Associate .Professor / CSE SRM Institute of Science and Technology, Chennai.	9940023373, muruganb@srmist.edu.in
3	Dr. V.TAMIZHAZHAGAN	Wireless Networks	Assistant Professor / IT, Annamalai University, Chidambaram	8925122220 rvtamizh@gmail.com
4	Dr. D. JAGANATHAN	Artificial Intelligence, Computer Networks	Assistant Professor / CSE, Vel Tech Rangarajan Dr. Sagunthala R & D institute of Science and Technology, Chennai	9994524148 djagannathan@veltech.edu.in
5	Mr. V. PRABHU	DBMS, Data Structures	Assistant Professor / CSE, Vel Tech Rangarajan Dr. Sagunthala R & D institute of Science and Technology, Chennai	9597739629 vprabhu@veltech.edu.in
6	Dr. LAKSHMI DHEVI	Internet of Things, Compute Organization, Computer Graphics	Assistant Professor / CSE, Vel Tech Rangarajan Dr. Sagunthala R & D institute of Science and Technology, Chennai	9551145796 blakshmidhevi@veltech.edu.in

7	Dr.MANJUNATHAN	Database Management Systems, Operating Systems	Assistant Professor / CSE, Vel Tech Rangarajan Dr. Sagunthala R & D Institute of Science and Technology, Chennai	9791060024 nmanjunathan@veltech.edu.in
8	Dr. A. RAMACHANDRAN	Digital Design, Computer Design, Web Technology	Assistant Professor and Head (i/c), Department of Computer Science and Engineering, University College of Engineering, Panruti.	9790900771 ramautpc@gmail.com , ram@ucep.edu.in
9	Dr. C. NAVANEETHAN	Java, Python, Artificial Intelligence, Data Structures	Associate Professor / CSE Department of Software and Systems Engineering, School of Information Technology & Engineering, VIT, Vellore - 632014	9962327007 navaneethan.c@vit.ac.in
10	Dr. G. GUNASEKARAN.	Artificial Intelligence, AR & VR, Compiler Design	Associate Professor / CSE Department of Smart Computing, School of Information Technology & Engineering, VIT, Vellore - 632014	9443049982 ggunasekaran@vit.ac.in
11	Dr. AMRITHA SARAVANAN	Data Structures, Operating Systems	Associate Professor / CSE Department of CSE, University college of Engineering, Villupuram	9791555778 aasaravanan777@gmail.com
12	Dr. BALAJI. N	Computer Networks, Cloud Computing	Professor and Head, Department of CSE, Sri Venkateswara College of Engineering and Technology, Puducherry.	9944199803 nbalajime1983@gmail.com