



# SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)

(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)  
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution &  
Accredited by NAAC with "A" Grade)



Madagadipet, Puducherry - 605 107

## Department of Electrical and Electronics Engineering

### Minutes of 2<sup>nd</sup> Board of Studies Meeting (PG and Ph.D)

The Second Board of Studies meeting for M.Tech and Ph.D Programmes of Department of Electrical and Electronics Engineering was held on 31<sup>st</sup> March 2021 at 2.15 P.M in the Seminar Hall, Department of EEE, Sri Manakula Vinayagar Engineering College with Head of the Department in the Chair.

The following members were present for the BoS meeting

Sl.No	Name of the Member with Designation and official Address	MEMBERS AS PER UGC NORMS
1	<b>Dr.S.Anbumalar</b> Professor and Head Department of EEE SMVEC, Madagadipet-605107	Chairman
2	<b>Dr.A.Kavitha</b> Professor Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (University Nominee)
3	<b>Dr. P. Lakshmi</b> Professor Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (Academic Council Nominee)
4	<b>Dr. J. Kanakaraj</b> Professor and Head Department of EEE PSG College of Technology (Autonomous) Coimbatore – 641 004.	Subject Expert (Academic Council Nominee)
5	<b>Er.S. Selva Kumar, B.Tech.</b> Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001	Representative from Industry
6	<b>Er.K.Ramraj</b> Technical Director LED FORSE India Poornankuppam Puducherry – 605 007.	Postgraduate Alumnus (nominated by the Principal)
7	<b>Dr. P. Jamuna</b> Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member
8	<b>Dr.D.Raja</b> Associate Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member

9	<b>Dr.M.Susithra</b> Associate Professor Department of EEE,SMVEC , Madagadipet-605107	Internal Member
10	<b>Dr.S.Ganesh Kumaran</b> Associate Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member
11	<b>Dr.T.Gayathri</b> Professor and Head Dept of Mathematics,SMVEC, Madagadipet-605107	Internal Member
12	<b>Dr.K.Kathikeyan</b> Associate Professor Dept. of Chemistry, SMVEC, Madagadipet-605107	Internal Member
13	<b>Mrs.G.Namita</b> Associate Professor Dept. of English, SMVEC Madagadipet-605107,	Internal Member
14	<b>Dr.D.Mohan Radheep</b> Associate Professor Dept. of Physics, SMVEC, Madagadipet-605107	Internal Member
15	<b>Mr. A. Janagiraman</b> Assistant Professor Department of EEE,SMVEC, Madagadipet-605107	Internal Member

#### Agenda of the Meeting

- 1) Confirmation of minutes of 1<sup>st</sup> BOS meeting
- 2) To discuss and approve the revised programme Educational Objectives, Programme Outcomes and Programme Specific Outcomes.
- 3) To discuss and approve Academic Calendar for the Even Semester of Academic year 2020-21
  - ❖ Online class scheduled for II year.
- 4) To discuss and approve the Eligible programmes for admission into M.Tech Power Electronics and Drives under Autonomous Regulations 2020.
- 5) To discuss and recommend the panel of examiners to the Academic Council.
- 6) To discuss and approve the modifications in the regulation of Ph.D. Programme.
- 7) To appraise about the recognized research guides in the department of Electrical and Electronics Engineering.


#### Minutes of the Meeting


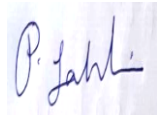







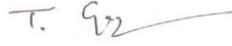


The meeting deliberated on the agenda items that had been approved by the Chairman.

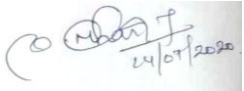
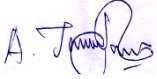
<b>Item:1</b>	The chairman BoS Confirmed the minutes of 1 <sup>st</sup> Board of Studies ( <b>Given in Annexure - I</b> ) meeting with no corrections.
<b>Item:2</b>	The revised Programme Educational Objectives, Programme Outcomes and Programme Specific Outcomes for the M.Tech programme were <b>discussed and approved by the BoS members and given in Annexure - II.</b>
<b>Item:3</b>	The Academic Calendar for the Even Semester of Academic year 2020-21 ( <b>Given in Annexure – III</b> ) were discussed and approved.

	<ul style="list-style-type: none"> <li>The classes are scheduled in the Online mode for II years.</li> </ul>										
<b>Item:4</b>	<p>The Eligible UG programmes for admission into M.Tech Power Electronics and Drives under Autonomous Regulations 2020 were <b>discussed and approved by the BoS members</b></p> <p>The Eligible UG Degree for admission of M.Tech Programmes B.Tech/B.E/ Equivalent Degree in</p> <ul style="list-style-type: none"> <li>Electrical &amp; Electronics Engineering</li> <li>Electronics &amp; Communication Engineering</li> <li>Electronics and Telecommunication Engineering</li> <li>Electronics &amp; Instrumentation Engineering</li> <li>Instrumentation &amp; Control Engineering</li> <li>Electronics Engineering</li> <li>Instrumentation Engineering/ Technology</li> <li>Bio Medical Engineering</li> <li>Medical Electronics</li> <li>or any Equivalent Degree</li> </ul>										
<b>Item:5</b>	The list of question paper setters and Evaluators given in <b>Annexure-IV</b> was presented and recommended by the BoS members to the academic council										
<b>Item:6</b>	<p>The revised Regulations for Ph.D. programme in R-2020, were discussed. The revisions in the Ph.D. regulations are listed below.</p> <ul style="list-style-type: none"> <li>Two theory subject (each 4 credits) of course work in the research area that is prescribed by the Doctoral Committee for the scholar to undergo as a part of the programme research.</li> <li>Two courses such as research methodology and publication ethics (each 3 credits) shall be prescribed to the research scholar by the recommendations of the Doctoral Committee.</li> <li>Two publications in the SCI indexed journals and one International conference presentation are essential for the submission of synopsis.</li> </ul>										
<b>Item:7</b>	<p>The following recognized research guides in the department were <b>approved by the BoS members.</b></p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Name of the faculty Member</th> <th>Designation</th> </tr> </thead> <tbody> <tr> <td>Dr. S. Anbumalar</td> <td>Professor</td> </tr> <tr> <td>Dr. P. Jamuna</td> <td>Professor</td> </tr> <tr> <td>Dr. S. Ganesh Kumaran</td> <td>Associate Professor</td> </tr> <tr> <td>Dr. M. Susithra</td> <td>Associate Professor</td> </tr> </tbody> </table>	Name of the faculty Member	Designation	Dr. S. Anbumalar	Professor	Dr. P. Jamuna	Professor	Dr. S. Ganesh Kumaran	Associate Professor	Dr. M. Susithra	Associate Professor
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Dr. P. Jamuna	Professor										
Dr. S. Ganesh Kumaran	Associate Professor										
Dr. M. Susithra	Associate Professor										

The meeting was concluded at 3:00 PM with vote of thanks by **Dr. S.Anbumalar**, Chairman, Board of Studies, Department of Electrical and Electronics Engineering, Sri Manakula Vinayagar Engineering College Madagadipet, Puducherry.

SI.No	Name of the Member with Designation and official Address	MEMBERS AS PER UGC NORMS	Signature
1	<b>Dr.S.Anbumalar</b> Professor and Head Department of EEE SMVEC, Madagadipet-605107	Chairman	
2	<b>Dr.A.Kavitha</b> Professor Department of EEE	Subject Expert (University Nominee)	

	College of Engineering Guindy Anna University Chennai. 600 025.		
3	<b>Dr. P. Lakshmi</b> Professor Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (Academic Council Nominee)	
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14	<b>Dr.D.Mohan Radheep</b> Associate Professor Dept. of Physics, SMVEC, Madagadipet-605107	Internal Member	
15	<b>Mr. A. Janagiraman</b> Assistant Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member	

**Annexure – I**

**Minutes of 1<sup>st</sup> Board of Studies Meeting (BoS)**



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Madagadipet, Puducherry - 605 107



## Department of Electrical and Electronics Engineering

### Minutes of 1<sup>st</sup> Board of Studies Meeting (PG and Ph.D)

The first Board of Studies meeting for M.Tech and Ph.D Programmes of Department of Electrical and Electronics Engineering was held on 18<sup>th</sup> July 2020 at 3:00 P.M in the Seminar Hall, Department of EEE, Sri Manakula Vinayagar Engineering College with Head of the Department in the Chair.

The following members were present for the BoS meeting

SI.No	Name of the Member with Designation and official Address	MEMBERS AS PER UGC NORMS
1	<b>Dr.S.Anbumalar</b> Professor and Head Department of EEE SMVEC, Madagadipet-605107	Chairman
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7	<b>Dr. K. Suresh</b> Professor Department of EEE, SMVEC	Internal Member
8	<b>Dr. P. Jamuna</b> Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member

Department of EEE – First BoS Meeting

9	<b>Dr.M.Susithra</b> Associate Professor Department of EEE,SMVEC , Madagadipet-605107	Internal Member
10	<b>Dr.S.GaneshKumaran</b> Associate Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member
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16	<b>Mr. A. Janagiraman</b> Assistant Professor Department of EEE,SMVEC, Madagadipet-605107	Internal Member

#### Agenda of the Meeting

- 1) To discuss and approve the Pondicherry University M.Tech. Degree Regulations R2011,its curriculum for 1 to 4 semesters and syllabi for 1 to 4 semesters for the M.Tech – Power Electronics and Drives, students admitted in the Academic Year 2019-20(present Final Year)
  - ❖ Examination and Evaluation-SMVEC Autonomous System
- 2) To discuss and approve the SMVEC Autonomous Regulations for M.Tech. Degree,R2020,its curriculum for 1 to 4 semesters and syllabi for 1 to 4 semesters for the M.Tech – Power Electronics and Drives, students admitted from the Academic Year 2020-21 onwards
  - ❖ Course structures
  - ❖ Professional Core Courses
  - ❖ Professional Elective Courses
- 3) Uniqueness of the Curriculum
  - ❖ Employability Skills Enhancement Courses introduced from I to VI semesters
  - ❖ Internship
  - ❖ Modern tool usage / Model Making / Assignment in all the courses
  - ❖ Mandatory Course – NPTEL / SWAYAM
  - ❖ Mandatory to publish papers in reputed Journals (Scopus Indexed Journal)
  - ❖ Teaching Learning Practice
- 4) To discuss and approve the Evaluation Systems
  - ❖ Mark weightage for Continuous Assessment and End Semester Examinations



- ❖ Question paper pattern
  - ❖ Marks requirement to pass the course
  - ❖ Semester Grade Point Average (SGPA), Cumulative Grade Point Average (CGPA) and Percentage Conversion
  - ❖ Classification of Degree
- 5) To discuss and recommend the panel of examiners to the academic council
  - 6) To discuss and approve the SMVEC Autonomous Regulations for Ph.D. programme R2020, for the Ph.D., Research scholars registered from the academic year 2020-2021
  - 7) Any other item with the permission of chair
    - ❖ To suggest Exam fee and Remuneration to the examiners

### Minutes of the Meeting

The meeting deliberated on the agenda items that had been approved by the Chairman.

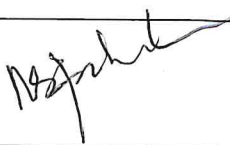
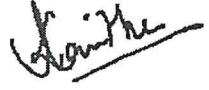
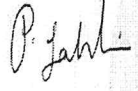


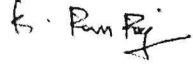


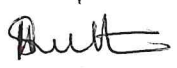
Item:1	<p>The Pondicherry University M. Tech. Degree Regulations R2011, its curriculum for 1 to 4 semesters and syllabi for 1 to 4 semesters for the M.Tech – Power Electronics and Drives were <b>discussed and approved for the present Final Year students</b> admitted in the Academic Year 2019-20. <b>It has also been approved to conduct the Examination and Evaluation by SMVEC Autonomous System.</b></p>																
Item:2	<p>The SMVEC Autonomous Regulations for M. Tech. Degree, R2020, its curriculum for 1 to 4 semesters and syllabi for 1 to 4 semesters for the M. Tech - Power Electronics and Drives, were discussed.</p> <p>The following comments were given in the meeting by the BoS members.</p> <p>Curriculum</p> <ul style="list-style-type: none"> <li>• BOS members suggested that Audit course may be included in the curriculum</li> <li>• Technical Report Writing and Seminar may be allotted with one credit</li> <li>• In Semester III, in Journal Publication, it may be stated as UGC indexed journal instead of Scopus indexed Journal</li> <li>• NPTEL course need not be a compulsory one.</li> </ul> <p>Syllabus</p> <table border="1"> <thead> <tr> <th>Sl.No</th> <th>Subject / general point</th> <th>Comments</th> </tr> </thead> <tbody> <tr> <td colspan="3" style="text-align: center;"><b>Comments for syllabi of R-2019 Regulations</b></td> </tr> <tr> <td>1.</td> <td>Design and Simulation of Power Electronic Circuits Laboratory</td> <td> <ul style="list-style-type: none"> <li>• Have to change simulation lab as 'Power converters Simulation Laboratory' instead of 'Design and Simulation of Power Electronic Circuits Laboratory'</li> <li>• Details of analysis on simulation experiment</li> <li>• Have to remove series and parallel inverter experiment.</li> </ul> </td> </tr> <tr> <td>2.</td> <td>Design Techniques for Switch Mode Power Conversion</td> <td> <ul style="list-style-type: none"> <li>• Buck , Boost, Buck-Boost converters may be combined in one unit</li> </ul> </td> </tr> <tr> <td>3.</td> <td>Electric and Hybrid Vehicles</td> <td> <ul style="list-style-type: none"> <li>• Power management topic can be removed</li> <li>• Unit IV can be changed and topics can be included based on hybrid vehicle</li> </ul> </td> </tr> </tbody> </table>		Sl.No	Subject / general point	Comments	<b>Comments for syllabi of R-2019 Regulations</b>			1.	Design and Simulation of Power Electronic Circuits Laboratory	<ul style="list-style-type: none"> <li>• Have to change simulation lab as 'Power converters Simulation Laboratory' instead of 'Design and Simulation of Power Electronic Circuits Laboratory'</li> <li>• Details of analysis on simulation experiment</li> <li>• Have to remove series and parallel inverter experiment.</li> </ul>	2.	Design Techniques for Switch Mode Power Conversion	<ul style="list-style-type: none"> <li>• Buck , Boost, Buck-Boost converters may be combined in one unit</li> </ul>	3.	Electric and Hybrid Vehicles	<ul style="list-style-type: none"> <li>• Power management topic can be removed</li> <li>• Unit IV can be changed and topics can be included based on hybrid vehicle</li> </ul>
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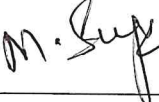


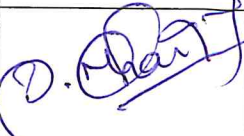

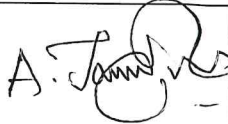
4.	Power Converters and Drives Laboratory	<ul style="list-style-type: none"> <li>• Have to add experiment based on PIC and DSP</li> </ul>
5.	Advanced Control Systems	<ul style="list-style-type: none"> <li>• Unit 2- title should be changed as nonlinear control</li> </ul>
6.	Solar Photovoltaic System Design	<ul style="list-style-type: none"> <li>• Reduce the content in unit 4</li> <li>• Remove conversion and inversion</li> <li>• Include battery management system</li> <li>• Replace Lithium ion</li> </ul>
7.	MEMS Technology	<ul style="list-style-type: none"> <li>• Content given in unit 5 can be given in simulation</li> <li>• It would be better if the unit 5 is taught using simulation software.</li> </ul>
8.	Energy Storage Systems	<ul style="list-style-type: none"> <li>• Reduce content in Unit 4</li> <li>• Include various energy storage components</li> </ul>
9.	Automotive Electrical and Electronic Systems	<ul style="list-style-type: none"> <li>• Need to include more content like batteries.</li> <li>• Need to rearrange the syllabus.</li> </ul>
10.	Analysis and Design of Inverters	<ul style="list-style-type: none"> <li>• Single phase and three phase voltage source inverters have to combine in one unit</li> <li>• Order of the unit can be changed</li> <li>• Can include the z- source inverter in separate unit</li> <li>• Remove the repeated simulation in the instructional activity</li> </ul>
11.	Power Quality	<ul style="list-style-type: none"> <li>• Mention the Power quality problems</li> </ul>

**The above comments were incorporated and the corrected version of M.Tech, R2020 Autonomous Regulations, its curriculum for 1 to 4 semesters and Syllabi for 1 to 4 semesters are approved by the BoS members for the students admitted from the Academic Year 2020-21 onwards.**

<b>Item:3</b>	The uniqueness of the Curriculum such as Employability Skills Enhancement Courses, Internship, Modern tool usage / Model Making / Assignment and Teaching Learning Practice etc., were discussed by the BoS members
<b>Item:4</b>	The Examination and Evaluation Systems related items such as Mark weightage for Continuous Assessment and End Semester Examinations, Question paper pattern, Marks requirement to pass the course, Semester Grade Point Average (SGPA), Cumulative Grade Point Average (CGPA) and Percentage Conversion and Classification of Degree etc., were discussed and approved by the BoS members.
<b>Item:5</b>	The list of question paper setters and Evaluators was presented and recommended by the BoS members to the academic council
<b>Item:6</b>	The SMVEC Autonomous Regulations for Ph.D. programme R2020, were discussed and approved for the Research scholars registered from the academic year 2020-2021.
<b>Item:7</b>	Any other matters with the permission of Chair <ul style="list-style-type: none"> <li>❖ The External members stated that the fee structure and Remuneration to the examiners can be finalized by the college based on its financial conditions.</li> </ul>

The meeting was concluded at 5:00 PM with vote of thanks by **Dr. S.Anbumalar**, Chairman, Board of Studies, Department of Electrical and Electronics Engineering, Sri Manakula Vinayagar Engineering College.Madagadipet,Puducherry.

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## Annexure – II

### (Revised POs, PSOs and PEOS)



## SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

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(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)  
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution &  
Accredited by NAAC with "A" Grade)

Madagadipet, Puducherry - 605 107



## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

### M.Tech (Power Electronics and Drives)

#### PROGRAMME EDUCATIONAL OBJECTIVES (PEOs)

##### **PEO1: Proficiency**

To evolve post graduates with a strong foundation in design, analytics and problem solving skills in the field of Power Electronics and Drives.

##### **PEO2: Leadership skills**

To attain intellectual leadership skills to cater the changing needs of power industry, academia and society.

##### **PEO3: Innovation**

To design and develop innovative products and services in the field of Power Electronics and Drives.

##### **PEO4: Lifelong Learning**

To enhance the skills by adapting the current trends through continuous learning.

#### PROGRAMME SPECIFIC OUTCOMES (PSOs)

##### **PSO1: Analytical Skills**

Ability to apply the technical knowledge and solve complex problems in the field of power electronics, controllers and machines using modern tools and technologies.

##### **PSO2: Societal Needs**

Capability to solve real-world Engineering problems in promising fields like Smart Grid, Renewable Energy interfaces and Electric Vehicles.

##### **PSO3: Competence in Research**

Ability to incorporate creative research to address the recent problems in the electrical power industry.

## **PROGRAMME OUTCOMES (POs)**

### **PO1: Exploration of Research:**

An ability to independently carry out research/investigation and development work to solve practical problems.

### **PO2: Technical Skill:**

An ability to write and present a substantial technical report/document.

### **PO3: Expertise in Academics:**

Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

### **PO4: Problem solving:**

An ability to discriminate, analyze, evaluate and synthesize the technologies to provide solution for multidimensional engineering problems.

### **PO5: Usage of Modern Tools:**

Create, select, learn and apply appropriate techniques, resources, and modern engineering and IT tools, including prediction and modelling, to complex engineering activities with an understanding of the limitations.

### **PO6: Ethical Practices and Social Responsibility:**

Acquire professional and intellectual integrity, professional code of conduct, ethics of research and scholarship, consideration of the impact of research outcomes on professional practices and an understanding of responsibility to contribute to the community for sustainable development of society.

**Annexure – III**

**Academic Calendar (M.Tech)**

### Use of Cell Phones

It has been decided **not to permit cell phones inside the college campus**. If any student is found using the cell phone inside the college campus, it would be confiscated and will not be returned back on any circumstances. Hence the students are instructed not to attend the college with the mobile phones.

### Dress Code

The students are requested to attend the college neatly dressed. While the male students should attend the college with the shirts neatly tucked in and with the shoes, the female students are permitted to come with churidar and dupatta properly pinned. Students wearing full hand shirts should wear it as such without folding it to half etc. Casual wears like jeans, T-shirts etc., both for boys and girls are strictly prohibited inside the campus. Each department has prescribed uniforms for the labs. The students are requested to strictly adhere to the dress codes as well as the rules and regulations of the college.

### Maintenance of Discipline

Discipline is an important factor that shapes one's personality. It is considered as a golden key capable of opening many doors. This institution expects each and every student to follow the rules and regulations in total. Maintaining discipline in the campus will promote a conducive environment for studies.

### Working hours

I hr	9.00	to	9.50
II hr	9.50	to	10.40
Break	10.40	to	10.55
III hr	10.55	to	11.45
IV hr	11.45	to	12.35
V hr	1.15	to	2.05
VI hr	2.05	to	2.55
Break	2.55	to	3.10
VII	3.10	to	4.00
VIII	4.00	to	4.50

Lunch break 12.35 p.m. to 1.15 p.m.

# SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)

(Accredited by NBA-AICTE, New Delhi, NAAC with "A" Grade)

Madagadipet, Puducherry - 605 107



## Academic Calendar

January 2021 to April 2021

Name :

Program : M.Tech.,

Year / Sem :

### நீங்கள்



அன்பு செய்துள்ளீர்கள், யாருக்கும் அடிமையாகாதீர்கள்;  
இரக்கம் காட்டுங்கள், எவரிடத்தும் ஏமாந்துவிடாதீர்கள்;  
பணியைப் போற்றுங்கள், எந்த நிலையிலும் கோழியாகாதீர்கள்;  
கண்டிப்பாக இருங்கள், எப்போதும் கோய்ப்பாதீர்கள்;  
சிக்கலாக வாழ்வுகள், கருமியாக மாறாதீர்கள்;  
வீரமாக இருங்கள், போக்கிரிகளாக மாறாதீர்கள்;  
சுறுசுறுப்பாக இருங்கள், பதட்டம் அடையாதீர்கள்;  
வொருளைத் தேடுங்கள், பேராசைப் படாதீர்கள்;  
உழைப்பை நம்புங்கள், உருப்படுவீர்கள்;  
உண்மையை நம்புங்கள், உயர்வடைவீர்கள்!

என்று அன்புடன்,  
ஆசிரியர்கள்.



## About Autonomous

Sri Manakula Vinayagar Engineering College has been conferred with Autonomous Status by the University Grants Commission on 26<sup>th</sup> September 2019 and the same was approved by Pondicherry University on 19<sup>th</sup> June 2020. The Pondicherry University Regulations R2018 will be followed for the students admitted in the Academic Year 2019 -20 (present Final year) and for the students admitted in the Academic Year 2020 -21 (present First year). SMVEC Autonomous Regulations R2020, will be followed.

### HIGHLIGHTS OF SMVEC AUTONOMOUS REGULATIONS 2020

- ❖ Industry 4.0 ready curriculum
- ❖ Updated towards skill development to create more job opportunities
- ❖ Multidisciplinary curriculum
- ❖ More entrepreneurship opportunities
- ❖ IELTS model curriculum / Foreign Languages learning opportunities
- ❖ Department wise Gold Medals
- ❖ Results will be declared within a month after completion of examinations
- ❖ **Ethnotech / Mandatory course**

The Institute has Established 17 Center of Excellence to provide 75 International Certification courses from IBM, Google, Cisco, E Plan, Microsoft, Autodesk, Texas instruments, Festo, Bentley, Schneider Electric, Amazon web services, Siemens, Tally, DELL EMC<sup>2</sup>, Harita Techserv, PTC, LN an Excellence in Technology & Didactic solutions. All students should enroll in one of the certification course in every semester

### ❖ Industrial Training / Internship

Students may undergo training or internship during summer / winter vacation at Industry/ Research organization. students are also permitted to undergo internships during their eighth semester after the theory classes are over

June 2021

Date	Day	Schedule	Working day/ Holiday
1	Tue		
2	Wed		
3	Thu		
4	Fri		
5	Sat		
6	Sun		Holiday
7	Mon		
8	Tue		
9	Wed		
10	Thu		
11	Fri		
12	Sat		
13	Sun		Holiday
14	Mon		
15	Tue		
16	Wed		
17	Thu		
18	Fri		
19	Sat		
20	Sun		Holiday
21	Mon		
22	Tue		
23	Wed		
24	Thu		
25	Fri		
26	Sat		
27	Sun		Holiday
28	Mon		
29	Tue		
30	Wed		
<b>Total number of working days :</b> <b>Total number of holiday :</b>			
வெற்றி என்பது, லட்சியத்தைப் படிப்படியாகப் புரிந்து கொள்வது - ரைட்டிங்கேல்			

**May 2021**

Date	Day	Schedule	Working day/ Holiday
1	Sat		Holiday
2	Sun		Holiday
3	Mon		75(55)
4	Tue		76(56)
5	Wed		77(57)
6	Thu		78(58)
7	Fri	End Semester Exam Commences for I year M.Tech	79(59)
8	Sat		Holiday
9	Sun		Holiday
10	Mon		
11	Tue		
12	Wed		
13	Thu		
14	Fri		
15	Sat		Holiday
16	Sun		Holiday
17	Mon		
18	Tue		
19	Wed		
20	Thu		
21	Fri		
22	Sat		Holiday
23	Sun		Holiday
24	Mon		
25	Tue		
26	Wed		
27	Thu		
28	Fri		
29	Sat		Holiday
30	Sun		Holiday
31	Mon		
<b>Total number of working days : 05</b>			
<b>Total number of holiday : 02</b>			
வெற்றி என்பது, டைசியத்தைப் படிப்படியாகப் புரிந்து கொள்வது - நைட்டிங்கேல்			

### ❖ Supplementary Examinations

In order to complete the program within 2 years, only the student with a maximum of two arrears will be permitted to appear for supplementary examination. The supplementary examination will be conducted at the end of second and fourth semesters only. For supplementary examination the continuous assessment marks of the last attempt will be considered.

### Benefits

- ❖ More number of students will receive the degree within the stipulated time
- ❖ The industries prefers to recruit students having nil arrears. If the supplementary examinations is conducted then more number of students will be eligible for the recruitment

### **Punctuality in Attendance**

The students are requested to keep up punctuality in attending the college. The late commers will be losing their attendance and in turn the internal marks. Hence all the students are requested to attend the college in time. A student shall be permitted to appear for the End Semester Examination at the end of the semester only if he / she **secures not less than 75% of overall attendance.**

### **Repeating the Course**

A student who secures overall attendance which is less than 60% has to repeat the course with the approval, when it is next offered.

### **Mentor - Mentee System**

In the mentor - mentee system, 30 students are allotted to a tutor who will be taking care of these students. The students are requested to utilize the facility.

The internal marks will be provided fully based on the continuous assessment tests (CAT 1 to 3)

Marks Distribution of Continuous Assessment Marks (CAM) and End Semester Examination Marks (ESM)

**Scheme for Continuous Assessment Test**

S. No	Course Type	Conti nues Assessment components									
		Test Marks	Average of pre / post test / viva for each experiment	Average marks of report for each Test	Model Exam / Report	Assignment	Review - 1	Review - 2	Review - 3	Attendance	Total Marks
1.	Theory	25	-	-	-	10	-	-	-	05	40
2.	Practical	-	15	15	10	-	-	-	-	10	50
3.	Project Phase - I	-	-	-	-	-	15	15	20	-	50
4.	Project Phase - II	-	-	-	-	-	15	15	20	-	50

**Question Paper Pattern**

Question paper pattern for CAT and ESE will be based on the patterns shown in Table (a) and (b)

**Table (a) Question Paper patterns for CAT1 and 2**

Exam	2 Marks	10 Marks	Total Marks
ESE	5 (At least two questions from each unit)	3 (out of 4 Questions and at least two questions from each unit)	40

**Table (b) Question paper patterns for CAT 3 and End semester Examination**

Exam	2 Marks	12 Marks	Total Marks
ESE	6 (At least one questions from each unit)	4 (out of 6 Questions and at least two questions from each unit)	60

April 2021

Date	Day	Schedule	Working day/ Holiday
1	Thu		55(34)
2	Fri	<b>Good Friday</b>	Holiday
3	Sat		Holiday
4	Sun		Holiday
5	Mon		56(35)
6	Tue		57(36)
7	Wed		58(37)
8	Thu		59(38)
9	Fri		60(39)
10	Sat		Holiday
11	Sun		Holiday
12	Mon		61(40)
13	Tue		62(41)
14	Wed	<b>Tamil New Year / Dr. Ambedkar Jayanthi</b>	Holiday
15	Thu	CAT 3 for I year M Tech (4 units)	63(42)
16	Fri		64(43)
17	Sat		Holiday
18	Sun		Holiday
19	Mon		65(44)
20	Tue		66(45)
21	Wed	<b>Third Review for II year M Tech</b>	67(46)
22	Thu		68(47)
23	Fri		69(48)
24	Sat		Holiday
25	Sun		Holiday
26	Mon		70(49)
27	Tue		71(50)
28	Wed	<b>ESE- Project Viva- Voce for II Mtech</b>	72(51)
29	Thu	<b>ESE- Practical Exam for I Mtech</b>	73(52)
30	Fri		74(53)

**Total number of working days : 20**

**Total number of holiday : 10**

அன்றாட வாழ்வின் சாதாரண விஷயங்களையும், அசாதாரண முறையில் செய்யும்போது உலகின் கவனத்தை உன் மீது திருப்ப முடியும். - ஜார்ஜ்வாஷிங்டன் கார்வெர்

**March 2021**

Date	Day	Schedule	Working day/ Holiday
1	Mon		32(11)
2	Tue		33(12)
3	Wed		34(13)
4	Thu		35(14)
5	Fri		36(15)
6	Sat		Holiday
7	Sun		Holiday
8	Mon	First Review for II year M Tech	37(16)
9	Tue		38(17)
10	Wed		39(18)
10	Thu		40(19)
12	Fri		41(20)
13	Sat		Holiday
14	Sun		Holiday
15	Mon		42(21)
16	Tue		43(22)
17	Wed		44(23)
18	Thu		45(24)
19	Fri		46(25)
20	Sat		Holiday
21	Sun		Holiday
22	Mon	CAT-2 for I year M Tech (2 units)	47(26)
23	Tue		48(27)
24	Wed		49(28)
25	Thu		50(29)
26	Fri		51(30)
27	Sat		Holiday
28	Sun		Holiday
29	Mon	Second Review for II year M Tech	52(31)
30	Tue		53(32)
31	Wed		54(33)

Total number of working days : 23

Total number of holiday : 8

உலகை வெல்வதற்கு, உலகை தெரிந்து கொள்வது முக்கியமல்ல,

உன்னை அறிந்து கொள்வதுதான் முக்கியம் . . .

**Distribution of Attendance marks for theory : 5 marks**

The distribution of 5 marks for theory class attendance is as follows :

5 marks for 95% attendance and above

4 marks for 90% attendance and above but below 95%

3 marks for 85% attendance and above but below 90%

2 marks for 80% attendance and above but below 85%

1 mark for 75% attendance and above but below 80%

**Distribution of Attendance marks for practical : 10 marks**

The distribution of 10 marks for practical class attendance is as follows :

10 marks for 95% attendance and above

8 marks for 90% attendance and above but below 95%

6 marks for 85% attendance and above but below 90%

4 marks for 80% attendance and above but below 85%

2 marks for 75% attendance and above but below 80%

**Note :**

Continuous assessment mark will be based on the performance of the students in the continuous assessment test, Assignment and attendance percentage.

### Importance of Continuous Assessment marks

The continuous assessment marks once earned are carried over to the subsequent exams also. Hence the students are requested to work hard to get the maximum of the continuous assessment marks. If the continuous assessment marks are lower, it will pull down chances of getting the first class, distinction, gold medals and ranks.

### Importance of CAT-I/CAT-II/CAT-III

Continuous assessment marks are awarded for the performance in the CAT-I, CAT-II & CAT-III. Hence all the students are requested to prepare well for each test / examination to earn the maximum continuous assessment marks.

### Undertaking Minor / Major Projects

Each student is advised to take atleast one minor project. Involving in the project will be helping to understand the basics of the subject. Some of the minor / major project will also be benefiting the society. Moreover, the Management awards cash prizes for the best projects in each department.

### Participation in the Curricular / Co-curricular / Extra curricular Activities

All the students are encouraged to participate in the curricular / co-curricular / extra curricular activities. Involvement in these activities will improve their knowledge level in the subject. If a student or a team gets cash prize / award at the technical event organised by the recognised institutions, then the management of this institution will also sanction an amount equivalent to the award / cash prize as a token of appreciation.

### Leave Account Record

For each student, leave account record has been provided. The students are instructed to show the leave record to their parents and strictly adhere to the instructions given for availing the leave. The leave account record should be maintained properly and prior approval must be obtained for availing the leave. In exceptional cases, the students are permitted to get the approval after availing the leave.

### Transport Facility

56 buses have been arranged for the students to reach the college from Puducherry, Kanagachetikulam, Villupuram, Neyveli, Panruti, Cuddalore, Nellikuppam, Madukarai and Tindivanam covering almost all the areas. Separate transport facility has been arranged for the students who remain in the college after 5 p.m. for utilising computer lab, library and sports facilities. The students are requested to utilise the transport facility.

All the students are requested to avoid mobile phones and travel by two wheelers considering their safety and security.

### February 2021

Date	Day	Schedule	Working day/ Holiday
1	Mon		12
2	Tue		13
3	Wed		14
4	Thu		15
5	Fri		16
6	Sat		Holiday
7	Sun		Holiday
8	Mon		17
9	Tue		18
10	Wed		19
10	Thu		20
12	Fri		21
13	Sat		Holiday
14	Sun		Holiday
15	Mon	Commencement of class for MTech II year	22 (1)
16	Tue		23(2)
17	Wed		24(3)
18	Thu		25(4)
19	Fri		26(5)
20	Sat		Holiday
21	Sun		Holiday
22	Mon	CAT-1 for I year M Tech (2 units) / Zeroth Review for II year M Tech	27(6)
23	Tue		28(7)
24	Wed		29(8)
25	Thu		30(9)
26	Fri		31(10)
27	Sat		Holiday
28	Sun		Holiday

Total number of working days : 20

Total number of holiday : 8

தரளராத இதயம் உள்லவனூக்கு, இவ்வுலகில் முடியாதது என்று எதுவுமே இல்லை

- புக்கன்ஸ்

## January 2021

Date	Day	Schedule	Working day/ Holiday
1	Fri		Holiday
2	Sat		Holiday
3	Sun		Holiday
4	Mon		
5	Tue		
6	Wed		
7	Thu		
8	Fri		
9	Sat		
10	Sun		Holiday
11	Mon	<b>Commencement of class for MTech I year</b>	1
12	Tue		2
13	Wed		Holiday
14	Thu		Holiday
15	Fri		Holiday
16	Sat		Holiday
17	Sun		Holiday
18	Mon		3
19	Tue		4
20	Wed		5
21	Thu		6
22	Fri		7
23	Sat		Holiday
24	Sun		Holiday
25	Mon		8
26	Tue	<b>Republic day</b>	Holiday
27	Wed		9
28	Thu		10
29	Fri		11
30	Sat		Holiday
31	Sun		Holiday

**Total number of working days : 12**

**Total number of holiday : 10**

நீ வெற்றியடைவதை உன்னைத் தவிர, வேறு யாராலும் தடுக்க முடியாது - ப்ரமர்

### Placement and Training Division

The placement cell functions round the clock throughout the year to establish contact with reputed multinational companies, well established industrial organizations and plays an important role in locating various job opportunities and placing large number of the students every year at these organizations.

### Activities of the Training Division

- ☆ Arranges trainings for personality and interpersonal skill development.
- ☆ Assists the students to get in-plant training
- ☆ Arranges industrial visits
- ☆ Creates awareness on the opportunities open for higher studies.
- ☆ Arranges coaching classes for GATE, GRE, TOFEL, IELTS, IAS, IES etc.

### Placement Record

Academic Year	Students Placed
2012-13	75%
2013-14	85%
2014-15	95%
2015-16	95%
2016-17	93%
2017-18	95%
2018-19	95%
2019-20	95%

### Details of Placed Students : 2020 - 21

1 Kaar	9	12 Makolet	3
2 TCS(Ninja and Digital)	210	13 O2Saver	4
3 Hexaware	15	14 OPPO Mobiles	3
4 Yellow Messenger	4	15 Infosys	1
5 Unisys	1	16 Sutherland	56
6 EmbedUr	1		
7 Virtusa	3		
8 ZOHO	8		
9 CTS	101		
10 Milekel Engineering	1		
11 Mantec Electronic	1	<b>Total</b>	<b>*421</b>

### Wi-Fi Campus

*\* till 31<sup>st</sup> January 2021*

Our campus has been enabled by high speed uninterrupted Wi-Fi connectivity. The Computer Centre is open till 8.00 p.m. on all the working days except on the dates of University examinations.

### Library Working Hours

8.30 a.m. to 8.30 p.m. (On all the working days)  
8.30 a.m. to 10.00 p.m. (During the examination days)

### Women Cell


For the benefit of the girl students, a Women Cell has been constituted in the college. The girl students may approach the Chairperson / members for assistance.


























### Grievance Redressal Cell

There is a Grievance Redressal Cell under the Chairmanship of the Director of the institution. Students are requested to approach the Chairman / members to redress their grievances. Mail ID : grievance@smvec.ac.in

### Gold Medals and Top Ten Ranks

Your seniors were sincere, hard working and got the Gold medals of the Pondicherry University and the top ten ranks in all the branches. The details of the University Goldmedals and Top Ten Ranks won by the students are given below.

 Indicates the Gold medal and University First Rank.

Name of the Course	Year		
	2017	2018	2019
B.Tech. EEE	2, 4, 6, 7		 2,3,4,6,7,8,9,10
B.Tech. ECE	2,3,4,5,6,7,8,9,10		 3,4,5,6,7,9,10
B.Tech. CSE	 2, 3, 4, 10		 2,4,6,7,8,10
B.Tech. IT	 2,3,4,5,6,7,8,9,10		 2,3,5,6,8
B.Tech. ICE	 2,3,4,5,6,7,8,9,10		 2,3,4,5,6,7,8,9,10
B.Tech. Mech	 4, 5, 7, 9, 10		3,7,8,10
B.Tech. Civil	2, 3, 10		2,3,4,6,7,10
MCA	3,4,7,9,10		 2,6,7,8,9,10,11
MBA	 3,4,6,7,8	 2,4,5,9,10	 2,3,4,5,7,8,10
MTech. CSE	 2, 3,4,5,7,8,9		 7
MTech. ECE	2, 3, 6, 7, 8, 9		2,3,4,5
MTech. PED			 2,3
MTech. NW	 2, 3,4,5,7, 8,9		 2,3
M.Tech(VLSI)			 2,3,4
M.Tech(MF)	 2		

### Important points for the kind attention of the Parents

Dear Parent

**The I semester classes commence on 11<sup>th</sup> January 2021.** The above mentioned semester is a very short term, including working days meant for CAT-III. The students have to complete a lot of work within a short period. **Hence the parents are kindly requested not to permit their wards to avail frequent leave during this semester period for the following reasons.**

It is compulsory for all the students to complete two Employability Enhancement Certificate Course, along with their Academic Courses. These courses will enhance the students to upgrade their required skills to cope up with the Industry.

Marks in the **continuous assessment test** decide the major part of the continuous assessment marks. So, availing leave for the continuous assessment test must be avoided at any cost as this would seriously affect the assessment marks.

Practicals are very important not only to score more marks but also it will help to understand the theory part of the subject, hence advice your ward not to cut the practical classes.

Please spare your **valuable time to talk to your son/daughter every day and try to understand what he/she is doing in respect of his/her studies.** Kindly extend all your support to your son/daughter which will help them to come out successfully. For any assistance from our side you may always feel free to contact the respective Coordinator / HOD any time during the working hours.

**Annexure – IV**



**SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE**

**(An Autonomous Institution)**

(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)  
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution &  
Accredited by NAAC with "A" Grade)

Madagadipet, Puducherry - 605 107



**Department of ELECTRICAL AND ELECTRONICS ENGINEERING**  
**DETAILS OF EXAMINERS FOR QUESTION PAPER SETTER AND EVALUATORS**

Sl.No	Name of the Examiner	Specialization	Year of Experience	Designation, Department and Institution in which currently working	Contact number and mail id
1.	Dr.S.P.Mangaiyarkarasi	Power System	16	Asst.Professor , Department of EEE, University college of Engineering, Panruti.	mangaisowmeya@gmail.com 8903678363
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