



**SRI MANAKULA VINAYAGAR
ENGINEERING COLLEGE**

(AN AUTONOMOUS INSTITUTION)

(APPROVED BY AICTE, NEW DELHI AND AFFILIATED TO PONDICHERRY UNIVERSITY)
(ACCREDITED BY NBA-AICTE, NEW DELHI, ACCREDITED BY NAAC WITH "A" GRADE)
MADAGADIPET, PUDUCHERRY - 605 107



OFFICE OF THE CONTROLLER OF EXAMINATIONS

SMVEC/CoE/Exam-Schedule/2025-26/043

18/06/2026

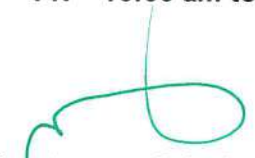
End Semester Examinations – APRIL 2026

Schedule for M. Tech I Year / II Semester Students (Batch: 2025-2027)

Date	Dept	Year/ Sem	Session	Course Code	Course
01/07/2026	VLSI	I/II	FN	P23VETC01	Advanced Digital System Design
	CSE			P23CST203	Advanced Software Engineering and Testing
	AIDS			P23ADT204	Parallel Programming Paradigms
03/07/2026	VLSI	I/II	FN	P23VETC02	Embedded Processors
	CSE			P23CST204	Adhoc and Wireless Sensor Networks
	AIDS			P23ADT205	Natural Language Processing
06/07/2026	VLSI	I/II	FN	P23VETC03	Embedded System Design
	CSE			P23CST205	Advanced Operating Systems
	MF			P23MET206	Metal Cutting Theory and Practice
	AIDS			P23ADT206	Advanced Deep Learning
08/07/2026	VLSI	I/II	FN	P23VET204	Low Power Digital VLSI Design
	CSE			P23CST206	Advanced Python Programming
	AIDS			P23ADT207	AI and Robotic Process Automation
10/07/2026	VLSI	I/II	FN	P23VEEC02	PE II: Internet of Things and its Implementation
	CSE			P23CSEC02	PE II: Soft Computing
	AIDS			P23ADE205	PE II: Data Visualization using Tableau and Power BI
13/07/2026	VLSI	I/II	FN	P23VEE212	PE III: Soft Computing
	CSE			P23CSEC03	PE III: Text, Web and Social Media Analytics
	AIDS			P23ADE213	PE III: Machine learning on Cloud platform

FN – 10:00 am to 1:00 pm


Controller of Examinations
Dr. S.JAYAKUMAR, M.Tech., Ph.D.,
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Director cum Principal
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