

## SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(AN AUTONOMOUS INSTITUTION) (APPROVED BY AICTE, New Delhi and Affiliated to Pondicherry University) (Accredited by NBA-AICTE, New Delhi, Accredited by NAAC with "A" Grade) MADAGADIPET, PUDUCHERRY - 605 107



## OFFICE OF THE CONTROLLER OF EXAMINATIONS **End Semester Examinations, March 2024**

Schedule for M. Tech | Year / | Semester (Batch: 2023-2025)

Date	Department	Year/ Sem	Session	Course Code	Course
20/03/24	VLSI	1/1	FN	P23MAT102	Applied Mathematics for VLSI
	CSE			P23MAT103	Mathematical Foundation of Formal Approach
	MF			P23MAT106	Engineering Probability and Statistics
	AIDS			P23MAT105	Probability and Statistics
22/03/24	VLSI	1/1	FN	P23VET101	Electronic Design Automation Tools
	CSE			P23CSTD01	Advanced Data Structures and Algorithms
	MF			P23MET101	Mechanical Behavior of Materials
	AIDS			P23ADT101	Machine Learning Algorithms
25/03/24	VLSI	1/1	FN	P23VET102	FPGA Based System Design
	CSE			P23CST102	Cloud and Big Data Analytics
	MF			P23MET102	Advanced Manufacturing Processes
	AIDS			P23ADT102	Computing Systems for Data Science
27/03/24	VLSI	1/1	FN	P23VET103	VLSI Design Techniques
	CSE			P23CSTD02	Speech Language Processing
	MF			P23MET103	Advanced Tool Engineering
	AIDS			P23ADT103	Artificial Intelligence and Intelligent Systems
30/03/24	VLSI	- - I/I	FN	P23HSTC01	Research Methodology and IPR
	CSE				
	MF				
	AIDS				
02/04/24	VLSI	- 1/1	FN	P23VEE102	PE I: VLSI Architecture
	CSE			P23CSE104	PE I: Block chain and Crypto Currency
	MF			P23MEE105	PE I: Cellular Manufacturing System
	AIDS			P23ADE101	PE I: Python for Data Science

FN - 10:00 am to 1:00 pm

Controller of Examinations Br. S. JAYAKUMAR, M. Tech., Ph.D.,

Controller of Examinations Sri Manakula Vinayagar Engineering College (An Autonomous Institution) Madagadipet, Puducherry-605 107,

Director cum Principal

Dr. V.S.K Venkatachalapathy, M.E., Ph.D.,

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