

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE

(An Autonomous Institution)

Department of Electrical and Electronics Engineering

Minutes of 4th Meeting of BoS (UG)

Venue

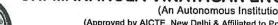
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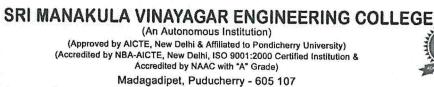
Department of EEE,

Sri Manakula Vinayagar Engineering College

Date & Time

22nd February, 2022 at 10:00 A.M







DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of 4th Meeting of Board of Studies (UG)

The Fourth meeting of Board of Studies in Electrical and Electronics Engineering Department was held on 22nd February 2022 at 10:00 A.M in the Seminar Hall, Department of EEE, Sri Manakula Vinayagar Engineering College, with Head of Department in the Chair.

The following members were present for the BoS meeting

SI. No.	Name of the Member	Designation
Head of	the Department (Chairman)	
1	Dr. S. Anbumalar, M.E., Ph.D., Professor and Head Specialization: Control System Years of Experience: 29 years Sri Manakula Vinayagar Engineering College saravanan.anbumalar@gmail.com 9443179533	Chairman
The ent	ire faculty of each specialization	
2	Dr. P. Jamuna, M.E., Ph.D., Professor Specialization: Power Electronics and Drives Years of Experience: 16 Sri ManakulaVinayagar Engineering College jamuna1981@gmail.com 9789544379	Member
3	Dr. D. Raja, M.Tech., Ph.D., Professor Specialization: Electrical Drives and Control Years of Experience: 15 Sri Manakula Vinayagar Engineering College rajaapeee@gmail.com 9944337970	Member
4	Dr. K. Gowrishankkar, M. Tech., Ph.D., Professor Specialization: Instrumentation and control Years of Experience: 16 Sri Manakula Vinayagar Engineering College gowri200@yahoo.com 9095555412	Member
5	Dr. S. Ganesh Kumaran, M.E., Ph.D., Associate Professor Specialization: Electrical Machines Years of Experience:10 Sri Manakula Vinayagar Engineering College ganeshphd4u@gmail.com 9677624378	Member

	Dr. T. Gayathri	
6	Professor, Dept. of Mathematics, SMVEC	Member
7	Dr. K. Kathikeyan Associate Professor, Dept. of Chemistry, SMVEC	Member
8	Mrs. G. Namita Associate Professor, Dept. of English, SMVEC	Member
9	Dr. P. Jayavardhane Associate Professor Dept. of Physics, SMVEC, Madagadipet-605107	Member
wo su	bject experts from outside the Parent University nominated	by the Academic Council
10	Dr. J. Kanagaraj, M.E., Ph.D., Professor & Head (In charge) Specialization: Control System Years of Experience:22 PSG College of Technology (Autonomous) Coimbatore – 641 004. Jkr.eee@psgtech.ac.in 94436 54496	Subject Expert
11	Dr. P. Lakshmi, M.E., Ph.D., Professor Specialization: Electrical Engineering Years of Experience:20 College of Engineering Guindy, Anna University, Chennai. 600 025. p_lakshmi@annauniv.edu 9444266117	Subject Expert
	pert nominated by the Vice-Chancellor from a panel of six re	ecommended by the
college	principal.	
12	Dr. A. Kavitha, M.Tech., Ph.D Professor Specialization: Electrical Engineering Years of Experience: 22 College of Engineering Guindy, Anna University, Chennai-600025 akavitha@annauniv.edu, 9444388778	Subject Expert
One re	presentative from industry/corporate sector/allied area relat	ing to placement.
13	Er. S. Selva Kumar, B.Tech. Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001	Member
One p	ostgraduate meritorious alumnus nominated by the Chairma	an, Board of Studies, with
the ap	proval of the principal of the college Er. K. Ramraj, M.Tech	
14	Technical Director, Specialization: Power Electronics Years of Experience:8 LED FORSE India, Poornankuppam, Puducherry – 605 007. ramrajeee@gmail.com,	Member

Agenda of the Meeting

Agenda 1/ BoS/ 4 /2022 /EEE /UG	Confirmation of minutes of 3 rd meeting of BoS and the Curriculum Structure of B.Tech Electrical and Electronics Engineering of R-2019 and R-2020 Regulations – Modifications if any
Agenda 2 / BoS/ 4 /2022 /EEE /UG	To discuss modifications in the syllabi of IV year, VII and VIII semesters, under Autonomous Regulations R-2019 for the B.Tech — Electrical and Electronics Engineering students admitted in the Academic Year 2019-20.
Agenda 3 / BoS/ 4 /2022 /EEE /UG	To discuss modifications in the syllabi of II and III years (IV to VI semester), under Autonomous Regulations R-2020 for the B.Tech — Electrical and Electronics Engineering students admitted from the Academic Year 2020-2021.
Agenda 4 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the Academic Calendar for odd/even Semester of Academic year 2021-22. The classes commenced from 21.01.2022 in online mode and in offline mode for all the years from 02.02.2022 onwards.
Agenda 5 / BoS/ 4 /2022 /EEE /UG	To approve the professional and open Elective courses offered to the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.
Agenda 6 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the on-line SWAYAM/MOOCS courses offered for the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.
Agenda 7 / BoS/ 4 /2022 /EEE /UG	To discuss and approve the Certification courses offered for the II year and III year students under R-2019 and R-2020 regulations during the period January 2022 to May 2022.
Agenda 8 / BoS/ 4 /2022 /EEE /UG	To discuss and recommend the panel of examiners to the Academic Council
Agenda 9 / BoS/ 4 /2022 /EEE /UG	 To discuss various Research activities in the department Implementation of AICTE-MODROB during the period 2021-2023. Patents Publication and submission Submission of research proposals Journal Paper publications and submission
Agenda 10 / BoS/ 4 /2022 /EEE /UG	Any other additional points to be discussed with the permission of Chair.

Minutes of the Meeting

Dr. S. Anbumalar, Chairman, BoS opened the meeting by welcoming the external members, the internal members and the meeting thereafter deliberated on agenda items that had been approved by the Chairman.

Agenda 1/ BoS /4 /2022 /EEE /UG

Confirmation of minutes of 3rd meeting of BoS and the Curriculum Structure of B.Tech Electrical and Electronics Engineering of R-2019 and R-2020 Regulations – Modifications if any.

Chairman, BoS, apprised the minutes of 3rd BoS, its implementation and then it is confirmed with the approval in 4th BoS meeting.

Agenda 2/ BoS /4 /2022 /EEE /UG

To discuss modifications in the syllabi of IV year, VII and VIII semesters, under Autonomous Regulations R-2019 for the B.Tech – Electrical and Electronics Engineering students admitted in the Academic Year 2019-20.

The modifications to be carried out in the syllabi of IV year, VII and VIII semesters (R2019 Regulations) were discussed and the following suggestions are given by BoS members.

S. No.	Regulations	egulations Semester Course Name with Code		Unit	Changes incorporated				
1	R2019	VII	Industrial Automation and Control U19EET71	V	loT based experiments are included in the lab course. Hence, Unit V has changed as Introduction to IoT				
2	R2019	VII	Industrial Automation and Control Lab U19EEP72	sunet 	Two loT based experiments are included by replacing the SCADA experiments.				
1000 1000 1000 1000			nen witasiji r grooti numening Tigitu ili maga	Sin to Si	 The following topics were additionally present in UNIT- II and they can be removed OOK Systems, OPSK, QAM, MSK, GMSK 				
3	R2019	VII Professional Elective	Communication Engineering U19EEE71	in M	The following topics were additionally present in UNIT – III and they can be removed Source coding: Shaum, Fao, Huffman coding: noiseless coding				
4	R2019	VII Professional Elective	SMPS and UPS U19EEE75		theorem The topic Cascaded Boost Converters is not required in UNIT- I and it can be removed.				

The above corrections are incorporated and the Syllabi (Given in Annexure- I) are approved by the BoS members.

Agenda 3/ BoS /4 /2022 /EEE /UG

To discuss modifications in the syllabi of II and III years (IV to VI semester), under Autonomous Regulations R-2020 for the B.Tech – Electrical and Electronics Engineering students admitted from the Academic Year 2020-2021.

The modifications to be carried out in the syllabi of II & III years, IV to VI semesters (R2020 Regulations) were discussed and the following suggestions are given by BoS members.

S. No.	Regulations	Semester	Course Name with Code	Unit	Changes incorporated
				Instrui Engine few to	content in Measurement and mentation for Electrical eering Course is vast and hence, pics need to be removed as per ggestions given below.
				. 1	Systematic and random errors, propagation of errors, Limiting errors of instruments has to be removed.
					Principle of operation, construction, Torque equation, types, testing and Calibration using direct and
				П	phantom loading has to be removed. Testing, application of measuring CT and VT has
			*		to be removed by retaining Instrument Transformers. Decibel meters - Q meter - tan-
1	R2020	IV	Measurement and Instrumentation for Electrical Engineering U20EET411	III	delta meter - Modulation index meter - Sampling theory and its applications in current, voltage, power, energy measurements has to be removed.
				IV	The following topics has to be removed Owens and Heavy side Campbell bridges De Sauty bridges localization of cable faults by Murray and Varley loop test - Methods of reducing bridge errors - Wagner Earthing Device. Suggested to include Schering Bridge.
sin C				V	The following topics has to be removed stroboscope, gyroscope – Force: Strain gauge – Torque: magnetostricitive, Position: synchro-Transmitter and receiver. Pressure: Manometers, Bourdon.
2	R2020	IV	Microprocessor and Microcontroller U20EET412	I, II, III, IV, V	 Students faced difficulties in understanding both 8-bit and 16-bit processors under R-2019 regulations. Hence, the Units are rearranged only using 8-bit processor.
3	R2020	IV	Measurements and Instrumentation Lab U20EEP408	-	Experiment 2(b): Measurement of inductance and Q-factor using Owen Bridge can be replaced by Maxwell Bridge Experiment 9: Measurement of strain, Load and Level using strain gauges has to be removed

					 Experiment 10 Measurement of torque and pressure using strain gauges has to be removed 			
4	R2020	IV	Microcontroller and its Applications Lab U20EEP409	-	 Experiments based on 16 bit processor has to be removed and suggested to include the experiment "Relay interfacing with PIC microcontroller". 			
5	R2020	V	Power Electronics U20EET513	1	Included the topic TRIAC			
6	R2020	VI	Renewable Energy Sources	need	content in Renewable Energy se is vast and hence, few topics to be removed as per the estions given below. Suggested to remove the terms radiation, Extraterrestrial, Spectral distribution, Solar constant, solar radiation on earth, measurements			
			U20EET617	. II	Suggested to remove the terms power extracted - wind distribution and speed prediction - wind map of India, Fundamentals — types of machines and their characteristics, Case study on Wind power generation using micro wind turbine for residential purpose.			
7	R2020	VI	Power System Analysis		Suggested to remove Power Scenario in India.			
			U20EET618	٧	Suggested to remove the term Contingency selection and ranking for the Power system.			
		IV Drafe as is	Energy Storage	ı	Suggested to remove demand for Portable Energy, Demand and Scale Requirements			
8	R2020	Profession al Elective	Technology U20EEE405	III	Suggested to remove Shaped Metal Batteries			
The		tasihi di pansoria tiri	DCOrporated and th	II, III	Removed Case Study or Hybridization of different Energy Storage Devices			

The above corrections are incorporated and the Syllabi (Given in Annexure- II) are approved by the BoS members.

Agenda 4/ BoS /4 /2022 /EEE /UG

To discuss and approve the Academic Calendar for odd/even Semester of Academic year 2021-22. The classes commenced from 21.01.2022 in online mode and in offline mode for all the years from 02.02.2022 onwards.

The Academic Calendars are prepared for this semester and it includes the schedule for CAT, Model Exam, QCM, Project review and Internal Marks distributions were discussed and approved (given in Annexure-III)

- The classes are scheduled in the online mode for II, III years from 21.01.2022 to 01.02.2022.
- As per Government order, the classes for all years had commenced from 02.02.2022 onwards in offline mode.

Agenda 5/ BoS /4 /2022 /EEE /UG

To approve the Professional and Open Elective Courses offered to the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.

 The Professional Elective and Open Elective courses opted by III year / VI semester students under R-2019 regulations during the period January 2022 to May 2022 is listed below are approved by the BoS members.

S. No.	Course Name	Course Code				
	Professional Elective	- III A resolution of the control of				
1	Smart Grid	U19EEE61				
2	Special Electrical Machines	U19EEE63				
	Open Elective - III	म्मान्य राग एक वर्ष प्रश्न ते । स				
1	Mobile App Development	U19ITO64				
2	Web Programming	U19CCO64				

Agenda 6/ BoS /4 /2022 /EEE /UG

To discuss and approve the on-line SWAYAM/MOOCS courses offered for the III year/ VI semester students under R-2019 regulations during the period January 2022 to May 2022.

- The list of online SWAYAM / MOOCS courses (given in Annexure- IV) offered for III
 year / VI semester students under R-2019 regulations during the period January
 2022 to May 2022 was presented and approved by the BoS members.
- The list of online SWAYAM / MOOCS courses (given in Annexure- IV) offered for the Faculty of EEE department during the period January 2022 to May 2022 was presented and approved by the BoS members

Agenda 7/ BoS /4 /2022 /EEE /UG

To discuss and approve the Certification courses offered for the I, II and III year students under R-2019 and R-2020 regulations during the period January 2022 to May 2022.

 The semester-wise list of Certification courses offered for II and III year students under R-2019 and R-2020 regulations during the period January 2022 to May 2022 was presented and approved by the BoS members.

Semester	Certification Course
The state of the s	R-2019 (Batch: 2019 – 2023)
VI	Artificial Intelligence and Edge computing / CCNA
3.5	R-2020 (Batch: 2020 – 2024)
III	Solar Smart Energy system with IOT
	R-2020 (Batch: 2021 – 2025)
	AutoCAD for Electrical

Agenda 8/ BoS /4 /2022 /EEE /UG

To discuss and recommend the panel of examiners to the Academic Council.

 The list of Question Paper Setters and Evaluators (given in Annexure-V) was presented and recommended by the BoS members to the academic council.

Agenda 9/ BoS /4 /2022 /EEE /UG

To discuss various Research activities in the department

- Implementation of AICTE-MODROB during the period 2021-2023.
- Patents Publication and submission
- Journal Paper publications and submission

The efforts taken to improve the Research activities in the department were presented and the **BoS noted the Agenda**.

- Implementation of AICTE approved MODROB for the Power Electronics and Drives Lab with a sanctioned amount of 11 Lakhs is in progress.
- The Department has published 10 design patents and Planned to improve in the forthcoming years.
- Each Faculty in the department is advised to publish one SCI and one Scopus Journal for this semester.

The Fourth meeting of BoS approval was concluded at 12.30 PM by **Dr. S. Anbumalar**, Chairman, Board of Studies, Department of Electrical and Electronics Engineering, Sri Manakula Vinayagar Engineering College.

SI.No	Name of the Member with Designation and official Address	MEMBERS AS PER UGC NORMS	Signature
1	Dr.S.Anbumalar Professor and Head Department of EEE SMVEC,Madagadipet-605107	Chairman	1961
2	Dr.A.Kavitha Professor, Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (University Nominee)	Laithe
3	Dr. P. Lakshmi Professor, Department of EEE College of Engineering Guindy Anna University Chennai. 600 025.	Subject Expert (Academic Council Nominee)	P. Jahl.
4	Dr. J. Kanakaraj Professor & Head Department of EEE PSG College of Technology (Autonomous) Coimbatore – 641 004.	Subject Expert (Academic Council Nominee)	J. Konordy
5	Er.S. Selva Kumar Senior Engineer Qualcomm India Private Limited Bengaluru, Karnataka - 560001	Representative from Industry	S. S.J.L.
6	Er.K.Ramraj Technical Director LED FORSE India Poornankuppam Puducherry – 605 007.	Postgraduate Alumnus (nominated by the Principal)	Es Pam Rej
7	Dr. P. Jamuna Professor Department of EEE,SMVEC	Internal Member	Form
8	Dr.D.Raja Professor Department of EEE,SMVEC, Madagadipet-605107	Internal Member	poratus

9	Dr. K. Gowrishankar Professor Department of EEE,SMVEC, Madagadipet-605107	Internal Member	Qh. <u>u</u>
10	Dr.S.Ganesh Kumaran Associate Professor Department of EEE, SMVEC, Madagadipet-605107	Internal Member	S. Brust
11	Dr.T.Gayathri Professor and Head Dept of Mathematics, SMVEC, Madagadipet-605107	Internal Member	7 52
12	Dr.K.Kathikeyan Associate Professor Dept. of Chemistry, SMVEC, Madagadipet-605107	Internal Member	A September
13	Mrs.G.Namita Associate Professor Dept. of English, SMVEC Madagadipet-605107,	Internal Member	Not
14	Dr. P. Jayavardhane Assistant Professor Dept. of Physics, SMVEC, Madagadipet-605107	Internal Member (Science & Humanity)	J.8-9-

Annexure – I

R2019 Syllabi

U19EET71

INDUSTRIAL AUTOMATION AND CONTROL

Hrs 0 n 3 45

Course Objectives

- To apprehend the basic architecture of Industrial automation system.
- To study about the components used in PLC.
- To practice the ladder logic programming of PLC.
- To learn about the building blocks of SCADA.
- To brief about the communication protocols and IoT.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the type of Automation system and its architecture in detail.(K3)
- CO2 Discuss the history of PLC, main parts and its functions. (K3)
- CO3 Illustrate the operation of Relays, contactors, Motor Starters, Switched, Sensors, Output Control Devices, etc., (K3)
- CO4 Acquire knowledge about the operation of SCADA and its sub-systems. (K3)

CO5 - Gain knowledge on fundamentals of IoT. (K3)

UNIT I INTRODUCTION TO AUTOMATION

(9 Hrs)

Automation overview - requirement of automation systems - architecture of industrial automation system -Levels of Automation-basic elements of an automated system - industrial bus systems: modbus and profibus.

UNIT II PROGRAMMABLE LOGIC CONTROLLERS

Introduction to PLC, Principles of Operation - Size and Application. Hardware Components: I/O Section, Discrete /Analog I/O Modules, Special I/O Modules, CPU, Memory Design, Memory Types, Programming Terminal Devices, Recording and Retrieving Data - Processor Memory Organization, Relay-Type Instructions, Instruction Addressing, Branch Instructions, Internal Relay Instructions.

UNIT III LADDER LOGIC PROGRAMMING

(9 Hrs)

PLC Wiring Diagrams and Ladder Logic Programs: Electromagnetic Control Relays, Contactors, Motor Starters, Manual/Mechanical Operated Switches, Sensors, Output Control Devices, Seal-in Circuits, Latching Relays, Converting Relay Schematics into PLC Ladder Programs, Programming Timers: Mechanical Timing Relays, Timer Instructions, On-Delay /Off-Delay Timer Instruction, Retentive Timer, Cascading Timers.

UNIT IV SCADA FUNDAMENTALS

Introduction, Open system: Need and advantages, Building blocks of SCADA systems, RTU-Evolution, Components, Communication, Logic, Termination and Testing and HMI subsystem - Power supplies, Advanced RTU functionalities, IEDs, Data concentrators and merging units.

Master Station: Software /Hardware components, Server systems in the master station, Small, medium, and large master stations, GPS.

UNIT V INTRODUCTION TO IOT

loT fundamentals, IoT Architecture and protocols, Various Platforms, IoT components and Communication Technologies, Challenges in IoT, Case study.

Text Books

- 1. Frank D. Petruzella, "Programmable Logic Controllers", McGraw Hill, 4th Edition, 2011
- Mini S. Thomas, "Power System SCADA and Smart Grids", CRC Press,3rd Edition April 2015.
- S. Mukhopadhyay, S. Sen and A. K. Deb, "Industrial Instrumentation, Control and Automation", Jaico Publishing House, 1st Edition, 2013.
- 4. Jeeva Jose, "Internet of Things", Khanna Publishing House, 1st Edition, 2018.

Reference Books

- Gary Dunning, "Introduction to Programmable Logic Controllers", Cengage Learning, 3rd India Edition, 2007.
- Frank lamb, "Industrial Automation: Hands On", McGraw-Hill Education, 1st Edition, 2013.
- T. Huges, "Programmable Logic Controllers", ISA press, 1994. William T. Shaw, "Cybersecurity for SCADA systems", Penn Well Books, 2006.
- 5. Arshdeep Bahga, Vijay Madisetti, "Internet of Things: A Hands-on Approach", 1st Edition, 2014

Web References

- 1. https://electrical-engineering-portal.com/download-center/books-and-guides/automation-control/plc-laddersequential-programming
- https://www.beckhoff.com/english.asp?start/?pk_campaign=AdWords-AdWordsSearch-IndustrialAutomationEN&pk_kwd=industrial%20automation
- https://www.plantautomation-technology.com/articles/an-overview-of-distributed-control-systems-dcs
- https://www.controleng.com/articles/scada-remains-relevant-for-industrial-automation/
- https://sw.aveva.com/monitor-and-control/scada Page | 10



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U19EEP72

INDUSTRIAL AUTOMATION AND CONTROL LAB

Hrs 30

Course Objectives

- To gain practical knowledge regarding the automation components.
- To perform delay operations using the PLC.
- To gain practical knowledge on interfacing of different sensors, counter, timer, RTD using PLC.
- To equip the students to provide the solution for real time industrial applications.
- To equip the students to develop a fault monitoring system using SCADA.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the ladder logic programs and components used for process control.(K2)
- CO2 Design PLC-relay logic for the real time applications (K3)
- CO3 Implement Industrial processing system. (K3)
- CO4 Design a SCADA monitoring system for real time applications.(K3)
- CO5 Diagnose the fault in Power generation and distribution networks, etc. (K3)

List of Experiments

- 1. Study of basic programming of PLC
- 2. Arithmetic operation, Timer, Counter operation using PLC
- 3. PLC based control of Level Process, Temperature Process, Speed.
- 4. Annunciator system using PLC
- 5. PLC based control of batch process system
- 6. Bottle filling system using PLC
- 7. a. Interfacing of lamp and button with PLC for ON/OFF operation.
 - b. Perform Delayed Operation of Lamp By Using Push Button.
- 8. Combination of Counter and Timer for Lamp ON/OFF operation.
- 9. DOL Starter and Star Delta Starter operation by using PLC.
- 10. Develop/ Execute ladder program for the Control of automatic bottle filling system.
- 11. Develop/ Execute ladder program for Traffic Light Control
- 12. Develop/ Execute ladder program for Reversal of DC Motor Direction
- 13. Develop/ Execute ladder program for Stair case lighting
- 14. IoT based Street light monitoring and control
- 15. IoT based Industrial pollution monitoring system.

Reference Books

- S. Mukhopadhyay, S. Sen and A. K. Deb, "Industrial Instrumentation, Control and Automation", Jaico Publishing House, 1st Edition, 2013.
- Gary Dunning, "Introduction to Programmable Logic Controllers", Cengage Learning, 3rd India Edition, 2.
- 3. Frank lamb, "Industrial Automation: Hands On", McGraw-Hill Education, 1st Edition, 2013.
- T. Huges, "Programmable Logic Controllers", ISA press, 1994.
- R. Krishnan, "Electric Motor Drives, Modelling, Analysis and Control", Pearson Education India, 1st Edition,
- Viswanandham, "Performance Modeling of Automated Manufacturing Systems", PHI, 1st Edition, 2009.
- Jose A. Romagnoli, Ahmet Palazoglu, "Introduction to Process control", CRC Taylor and Francis group, 3rd Edition, 2020.

Web References

- https://electrical-engineering-portal.com/download-center/books-and-guides/automation-control/plcladder-sequential-programming
- https://www.beckhoff.com/english.asp?start/?pk_campaign=AdWords-AdWordsSearch-IndustrialAutomationEN&pk_kwd=industrial%20automation
- https://www.advantech.com/solutions/ifactory 3.
- https://www.plantautomation-technology.com/articles/an-overview-of-distributed-control-systems-dcs 4.
- https://www.controleng.com/articles/scada-remains-relevant-for-industrial-automation/
- https://sw.aveva.com/monitor-and-control/scada

COs		Program Outcomes (POs)												ram Spe omes (P	
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COMMUNICATION ENGINEERING

L T P C Hrs 3 0 0 3 45

U19EEE71

Course Objectives

- To introduce different methods of analog communication systems and their significance.
- To introduce Digital Communication methods for high bit rate transmission.
- To introduce MAC used in communication systems for enhancing the number of users.
- To explain the various media for digital communication.
- To deliberate the use of Power Lines for communication.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Comprehend the basic Characteristics of the signals and analog modulation techniques. (K2)
- CO2 Comprehend the needs of modulation and various Digital modulation techniques. (K3)
- CO3 Describe multiple access techniques and its applications. (K2)
- CO4 Explain the advanced communication systems. (K2)
- CO5 Explore the role of Communication Engineering in the realization of Smart grids (K2)

UNIT I ANALOG COMMUNICATION

(9 Hrs)

Modulation –need for modulation - AM – Frequency spectrum – vector representation – power relations – generation of AM – DSB, DSB/SC, SSB, VSB AM Transmitter and Receiver; FM and PM – frequency spectrum – power relations: NBFM and WBFM, Generation of FM and PM, Armstrong method and Reactance modulator:

UNIT II DIGITAL COMMUNICATION

(9 Hrs)

Pulse modulations: PAM, PWM, PPM - Sampling theorems - quantization, PCM, DM, DPCM - Digital modulations: ASK, FSK, PSK, BSK, applications of Data communication.

UNIT III MULTIPLE ACCESS TECHNIQUES

(9 Hrs)

SS and MA techniques: FDMA, TDMA, CDMA, SDMA application in wire and wireless communication : Advantages (merits)

UNIT IV SATELLITE AND FIBRE OPTIC COMMUNICATION

(9 Hrs)

Orbital aspects-Geostationary satellites-Satellite Uplink - Satellite Downlink - Satellite Transponder - Modulation techniques for satellite links - Satellite Earth station

Principle of light propagation in fibre - Index profiles - Modes of propagation - Losses in fibre -Dispersion -Light sources and detectors- Fibre optic communication link

UNIT V POWER LINE COMMUNICATION

(9 Hrs)

Power Supply networks - Narrowband and Broadband PLC - Structure of PLC access network - PLC network elements - connection to core network - structure campus communication network and performance issues - Architecture of smart grid technology.

Text Books

- 1. Taub & Schiling, "Principles of communication systems", Tata McGraw hill, 2007.
- 2. J.Das, "Principles of digital communication", New Age International, 1986.
- 3. Halid Harasnica, Ralf Lehneri, "Broad band Power line Communications Design", John Wiley and Sons, 2005

Reference Books

- 1. Simon Haykin, "Communication Systems", Tata McGraw Hill, 4th Edition.
- 2. Kennedy and Davis, "Electronic Communication Systems", Tata McGraw hill, 4th Edition, 1993.
- 3. Sklar, "Digital Communication Fundamentals and Applications", Pearson Education, 2001.
- 4. Baryle, Memuschmidt, "Digital Communication", Kluwer Publication, 2004.
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- 6. K. Muralibabu, "Communication Engineering", Lakshmi Publications, 2013.

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- 3. yoonc01.tistory.com/attachment/fk10000000032.ppt
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Correlation Level: 1 - Low, 2 - Medium, 3 - High

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Course Objectives

- To provide conceptual knowledge of various types of DC DC converters.
- To impart the knowledge on various types of switched mode power converters and its voltage control
- To understand the importance of Zero voltage and Zero current switching used in resonant converters
- To analyze the PWM techniques and harmonic reduction techniques in DC AC converters.
- To explain the various types of filters and techniques to improve the power quality.

After completion of the course, the students will be able to

- CO1 Design the DC-DC converters for different applications. (K2)
- CO2 Analyze, design and select the converters used for switched mode power supplies in Computers, Laptop, and TV. (K4)
- CO3 Describe the importance of resonant Converters in reducing power loss and improving the life time of the power semiconductor device. (K2)
- CO4 Conclude the different voltage and harmonics reduction techniques used for DC-AC converters. (K4)
- CO5 Interpret knowledge on the techniques used to improve the power quality and design of filters for UPS.(K2)

UNIT I DC - DC CONVERTERS

(9 Hrs)

Principles of DC-DC Converters - Analysis and state space modeling of Buck, Boost, Buck- Boost and Cuk converters, Choice of switching frequency - Device Selection - EMI issues

UNIT II SWITCHED MODE POWER CONVERTERS

(9 Hrs)

SMPS Types: Self-Oscillating Flyback, Forward, Push pull, Luo, Half bridge and fullbridge converters- control circuits and PWM techniques - SMPS with multiple outputs - Choice of switching frequency - Device Selection - State space modeling.

UNIT III RESONANT CONVERTERS

(9 Hrs)

Introduction- classification - Load Resonant converters - ZVS, ZCS, Clamped voltage topologies- DC link inverters with Zero Voltage Switching- Series and parallel Resonant inverters- Voltage control. Multi energy storage element resonant converters - two, three and four element RPS - Application of Regulated Power Supply.

UNIT IV DC - AC CONVERTERS

Single phase and three phase inverters - control techniques, harmonic elimination techniques - Multilevel inverters -Concepts - Types: Diode clamped, Flying capacitor, Cascaded types; Switched Inductor and Capacitor multilevel Inverter - Applications.

UNIT V POWER CONDITIONERS. UPS AND FILTERS

Introduction- Power line disturbances- Power conditioners -UPS: offline UPS, Online UPS - Filters: Voltage filters, Series-parallel resonant filters, filter without series capacitors, filter for PWM VSI, current filter, DC filters - Design of high frequency inductor and transformer - Selection of capacitor and Batteries

- 1. Simon Ang, Alejandro Oliva, "Power-Switching Converters", CRC Press, 3rd Edition, 2010.
- Kjeld Thorborg, "Power Electronics In theory and Practice", Overseas Press India Private Ltd, 1st Edition.
- 3. M. H. Rashid, "Power Electronics handbook", Elsevier Publication, 4th Edition, 2017.

Reference Books

- Philip T Krein, "Elements of Power Electronics", Oxford University Press, 2nd Edition, 2014.
 Erickson, W. Robert, "Fundamentals of Power Electronics", Springer, 2nd Edition, 2010.
- 3. Joseph Vithayathil, "Power Electronics, Principles and Applications", McGraw Hill Series, 6th Reprint, 2013.
- Ned Ned Mohan, Tore M. Undeland, William P. Robbins, "Power Electronics: -Converters, Applications, and Design", John Wiley and sons Publication, 3rd Edition, 2010.
- 5. Fang Lin Luo, "Advanced DC/AC converters: Applications in renewable Energy", CRC press, 1st Edition, 2013.

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Annexure - II

R2020 Syllabi

U20EET411

MEASUREMENTS AND INSTRUMENTATION FOR L T P ELECTRICAL ENGINEERING 3 0 0

Course Objectives

- To give the students an insight into the constructional details and working principles of various measuring instruments.
- To provide the use of different types of analog and digital meters for measuring electrical and physical quantities.
- To demonstrate various Bridges for the measurement of resistance, inductance and capacitance.
- To provide the procedure to calibrate an energy meter.
- To understand and apply different types of sensors for the measurement of physical quantities such as speed, torque, pressure, displacement, temperature, etc.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Acquire knowledge on the characteristics of measuring instruments and their classification. (K2)
- CO2 Conversant in construction, working of A.C / D.C meters and their proficient use. (K3)
- CO3 Acquire knowledge in various methods of digital meters and its measurement. (K3)
- CO4 Acquire knowledge on construction and working principle of various types of display devices and bridge comparison methods for R, L and C measurement. (K3)
- CO5 Demonstrate the various types of transducers used for physical measurements. (K3)

UNIT I INTRODUCTION TO MEASUREMENT

(9 Hrs)

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Hrs

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Functional elements of Generalized measurement system - Types of measurement - Classification of instruments - Static and Dynamic characteristics of instruments - Mean, Standard deviation - error - Accuracy, Precision, Sensitivity, Linearity, Resolution, Hysteresis, Threshold, Input impedance - loading effects - Probability of errors - Errors in Measurements.

UNIT II ELECTRICAL INSTRUMENTS

(9 Hrs)

Essential requirements of an instrument - Ammeter and voltmeter - Moving coil - Moving Iron - Extension of voltmeter and ammeter range - Electro dynamo meter type Wattmeter - Induction type Energy meter - Measurement of active and reactive powers in balanced and unbalanced systems - Instrument Transformers - Construction, phasor diagrams - Magnetic measurements - Determination of B-H curve and measurements of iron loss

UNIT III DIGITAL INSTRUMENTS

(9 Hrs)

Digital Volt Meter and its design - Voltage ratio measurement techniques - Digital ohmmeter, capacitance meter - impedance meters (Polar and Cartesian types) - Signal analyzers: spectrum and logic analyzers - Digital Frequency Meter - Measurement of Frequency - Study of Phasor Measurement Units (PMU).

UNIT IV BRIDGES AND DISPLAY DEVICES

(9 Hrs)

Bridges: Measurement of resistances – D.C potentiometer - Wheat stone, Kelvin and Kelvin's Double bridge - A.C bridges for measurement of L and C - Maxwell, Anderson, Hay, Wein and Schering bridges - Measurement of earth resistance.

Display Devices: CRT display, analog and digital CRO, LED, and LCD.

UNIT V TRANSDUCERS

(9 Hrs)

Transducers - Definition and classification - Linear Displacement: Resistive Potentiometers, strain gauge, LVDT, Capacitive Piezoelectric - Rotational Displacement: magnetic, Position: synchro Transmitter and receiver - speed: Magnetic and photo electric pickup transducer - Temperature: Thermistors, thermocouple - Flow: Electromagnetic, Ultrasonic - Level: DP cell, Ultrasonic - Density: Hydrometer - Voltage, current and power: Hall Effect transducer

Text Books

- 1. A.K. Sawhney, "A Course in Electrical & Electronic Measurements and Instrumentation", Dhanpat Rai and Co., New Delhi, 19th Edition, 2015.
- 2. J. B. Gupta, "A Course in Electronic and Electrical Measurements", S. K. Kataria & Sons, Delhi, 12th Edition, 2009
- 3. E. O. Doebelin and D. N. Manik, "Measurement Systems Applications and Design", Tata McGraw Hill Education Pvt. Ltd., Special Indian Edition, 2007.

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Reference Books

- David Bell, "Electronic Instrumentation and Measurements", Oxford University Press, 1st Edition, 2013.
 A. J. Bouwens, "Digital Instrumentation", Tata McGraw Hill Publications, 16th Reprint Edition, 2008.
 H.S. Kalsi, "Electronic Instrumentation", Tata McGraw Hill Education, 4th Edition, 2019.
 C.S. Rangan, G.R. Sharma and V. S. V. Mani, "Instrumentation Devices and Systems", Tata McGraw Hill Book Co., New Delhi, 1st Edition, 2004.

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Correlation Level: 1 - Low, 2 - Medium, 3 - High



U20EET412

MICROPROCESSOR AND MICROCONTROLLER

L T P C Hrs
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Course Objectives

- To get familiar with basic architecture and programming techniques of microprocessor 8085.
- To learn interfacing of memory and data transfer techniques using microcontroller.
- To understand the interfacing of input/output devices required for real time applications.
- To introduce the basic concepts of embedded system design using microcontroller.
- To equip the student with ability to design PWM control for various application such as AC-DC, DC-DC converter, etc.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Illustrate the architecture of microprocessor and to develop skills in writing assembly language program.(K3)
- CO2 Have a clear understanding of microcontroller architecture with functional details of each pin. (K3)
- CO3 Write and debug Assembly and C programs for 8 bit Microcontroller.(K3)
- CO4 Interface input/output peripheral devices and to implement the advanced communication protocol like I²C and SPI using PIC Microcontroller. (K4)
- CO5 Design and develop microcontroller based real-time applications. (K4)

UNIT I ARCHITECTURE AND PROGRAMMING OF 8085 MICROPROCESSOR

UNIT IV SERIAL COMMUNICATION PROTOCOLS AND ITS PROGRAMMING

(9 Hrs)

8085 Microprocessor: Architecture, Addressing modes, Instruction set - Assembly language programs – Machine cycles and Timing diagrams. Application: Interfacing of stepper motor control with 8085 microprocessor

UNIT II INTRODUCTION TO PIC16F MICROCONTROLLER

(9 Hrs)

Microprocessor and Microcontroller difference - RISC and CISC programmer's model - Criteria for selecting microcontroller - Overview of PIC family - PIC16F877A architecture, status register, Special function registers, memory organization, On-Chip peripherals - PIC16F877A pin configuration - Fuse bits of PIC

UNIT III PIC16F PROGRAMMING

(9 Hrs)

Data types and assembler directives – Addressing modes – Instruction set – Bit addressability – MACROs – Intel HEX file – Programming Tools: MPLAB IDE – I/O Port Programming, Timer programming, PWM programming, External Interrupt programming, ADC programming, EEPROM programming

(9 Hrs)

Introduction to UART protocol and its programming – I2C protocol and its Programming - SPI protocol and its Programming - Serial Port programming.

UNIT V PERIPHERAL INTERFACING AND ITS PROGRAMMING

(9 Hrs)

LCD and Keyboard Interfacing – Relay interfacing – Stepper and DC Motor control – RTC Interfacing – LM35 Temperature sensor interfacing – MAX7219 display controller interfacing – Ultrasonic sensors interfacing – Introduction to PIC24F and dsPIC33EV

Text Books

- Muhammad Ali Mazidi, Sarmad Naimi and Sepehr Naimi, "PIC Microcontroller and Embedded Systems: Using Assembly and C for PIC18", Micro Digital Education, Illustrated Edition, 2017.
- 2. Ramesh S Gaonkar, "Microprocessor Architecture: Programming and Applications with the 8085", Prentice Hall of India, New Delhi, 5th Edition, 2011.

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- Sunil Mathur, Jeebananda Panda, "Microprocessor and microcontroller", PHI Learning Private Limited, New Delhi, 1st Edition, 2016.
- 2. dsPIC33EV data sheet

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- 2. https://www.youtube.com/watch?v=S1QCZW92fU4
- https://www.microchip.com/promo/explorer-8-development-board
- https://www.mikroe.com/easymx-pro-stm32
- https://www.microchip.com/DevelopmentTools/ProductDetails/DM240001-2#utm_source=MicroSolutions&utm_medium=Link&utm_term=FY17Q3&utm_content=MCU16&utm_campaign=Article

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MEASUREMENTS AND INSTRUMENTATION LAB

L T P C Hrs

Course Objectives

- To give the students an insight into the constructional details of various measuring instruments for better understanding of their working principles.
- To demonstrate various Bridges for the measurement of resistance, inductance and capacitance using simulation and hardware set ups.
- To understand the concept of magnetism and to determine the B-H curve for magnetic material specimen.
- To provide the procedure to calibrate an energy meter.
- To test and apply different types of sensors for the measurement of physical quantities such as speed, torque, pressure, displacement, temperature, etc. by conducting an appropriate experiment.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Realize the advantages and the necessity of measurement systems in all Engineering and Scientific works. (K2)
- CO2 Measure Resistance, Inductance and capacitance using AC and DC bridges. (K3)
- CO3 Determine the magnetization characteristics and hysteresis loss of Iron specimen using BH curve. (K3)
- CO4 Calibrate single phase and three phase energy meters used in domestic and commercial applications. (K3)
- CO5 Determine the characteristics of RTD, thermostat, strain gauge and LVDT transducers and to apply for the physical quantities measurements. (K3)

List of Experiments

- 1. (a) Measurement of resistance using Wheatstone bridge
 - (b) Measurement of insulation resistance.
- 2. (a) Measurement of capacitance and loss angle of capacitor using Schering Bridge.
 - (b) Measurement of inductance and Q-factor using Maxwell Bridge.
- 3. Extension of voltmeter and ammeter.
- 4. Calibration of single phase and three phase Energy meter using loading method.
- 5. Determination of B-H Curve for the magnetic material specimen.
- 6. Calibrate Current Transformer and Potential Transformer to determine ratio error and phase errors.
- 7. Characteristic of Temperature transducers (LDR, thermistor and thermocouple).
- 8. Measurement of Displacement using transducers.
- 9. Measurement of Voltage, current and power using Hall Effect transducer.
- 10. Characteristics of Optical Transducers (LDR, Phototransistor, Photovoltaic and photoconductive cells)
- 11. Measurement of speed using Magnetic and photo electric pickup transducers.
- 12. Measurement of Position using synchro Transmitter and receiver
- 13. Spectrum analyser and its use for analysing frequency spectra of periodic and non-periodic signals.

Reference Books

- A. K. Sawhney, "A course in Electrical and Electronics Measurement and Instrumentation", Dhanpat Rai and Sons, 19th Edition, 2015.
- William D. Coopers and Albert D. Helfrick, "Modern Electronic instrumentation and Measurements Techniques", Pearson Education India, 1st Edition, 2002.
- 3. E. W. Golding and F. C. Widdis, "Electrical Measurements and Measuring Instruments", Medtech Publication, 6th Edition, 2019.
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- 9. Science, Measurement and Technology, IET Journal.
- 10. Measurements, Elsevier Journal.

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- 4. http://vlabs.iitkgp.ernet.in/asnm/
- 5. http://www.wisegeek.com/what-are-transducers.htm-

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U20EEP409

MICROCONTROLLER AND ITS APPLICATIONS LAB 0

L T P C Hrs

Course Objectives

- To become familiar with architecture and instruction set for 8085.
- · To provide hands-on training of interfacing external sensors and actuators with microcontroller
- · To impart knowledge for on-chip peripheral programs
- To impart knowledge to generate pulses for electrical applications.
- To impart knowledge to do minor projects using microcontroller for solving real world engineering problems

Course Outcomes

After completion of the course, the students will be able to

- CO1 Develop assembly language program for microprocessor 8085. (K3)
- CO2 Analyze various platforms for programming by knowing the complete hardware configurations. (K4)
- CO3 Analyze abstract problems and apply a combination of hardware and software to address the problem.
 (K4)
- CO4 Design a control algorithm for various applications using microcontrollers. (K3)
- CO5 Design and generate pulses for real time electrical applications. (K3)

List of Experiments

Microprocessor Experiments using 8085:

- 1. 8 bit Addition, Subtraction, Multiplication and Division
- 2. Assembly Language Programming: Subroutines, parameter passing to subroutines

Microcontroller Experiments using PIC:

- 3. PIC Assembly language- Programming using the PIC Instruction Set.
- 4. a) PIC Timer to generate accurate delay
 - b) PIC Timer to generate waveforms
- 5. Seven Segment Display interfacing with PIC
- 6. a) 16x2 LCD interfacing with PIC
 - b) 4x4 matrix keyboard interfacing with PIC
- 7. PIC UART programming
- 8. PIC on-chip ADC for interfacing analog sensors

Application of Microcontroller using PIC:

- 9. Experimentation of DC Motor Interfacing and Speed/Direction Control with PIC
- 10. Stepper motor interfacing with PIC
- 11. Relay interfacing with PIC
- 12. DS1307 RTC Interfacing with PIC
- 13. MAX7219 LED matrix driver Interfacing with PIC using SPI protocol
- 14. Interface to peripherals and use of the I2C bus

Reference Books

- Muhammad Ali Mazidi, Sarmad Naimi and Sepehr Naimi, "The AVR Microcontroller and Embedded Systems Using Assembly and C", Micro Digital Education, Illustrated Edition, 2017.
- Ramesh S Gaonkar, "Microprocessor Architecture: Programming and Applications with the 8085", Prentice Hall of India, New Delhi, 5th Edition, 2011.
- 3. Sunil Mathur, Jeebananda Panda, "Microprocessor and microcontroller", PHI Learning Private Limited, New Delhi, 1st Edition, 2016.
- 4. dsPIC33EVdata sheet.

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- https://www.mikroe.com/easymx-pro-stm32
- https://www.microchip.com/DevelopmentTools/ProductDetails/DM240001-2#utm_source=MicroSolutions&utm_medium=Link&utm_term=FY17Q3&utm_content=MCU16&utm_campaign=Article

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U20EET513

POWER ELECTRONICS

L T P C Hrs 3 0 0 3 45

Course Objectives

- · To explain about the operations, switching characteristics of power semiconductor devices
- To study the operations and performance parameters of controlled Rectifiers.
- To analyze the operation and performance of dc to dc converters.
- To impart knowledge on different control techniques for inverters.
- · To familiarize the principle of operation of AC voltage controllers and cyclo converters

Course Outcomes

After completion of the course, the students will be able to

- CO1 Discriminate the switching characteristics of power devices and to use for power conversion. (K2)
- CO2 Inspect the performance of control rectifiers in continuous and discontinuous modes. (K2)
- CO3 Acquire knowledge on operation and analysis of DC to DC converters (K2)
- CO4 Outline the operating principles of various types of inverters. (K2)
- CO5 Gain knowledge on the operation of AC to AC converters and its applications (K2)

UNIT I POWER SEMI-CONDUCTOR DEVICES

(9 Hrs)

Study of switching characteristics of MOSFET, IGBT, SCR and TRIAC, Turn on and Turn off methods of SCR – Protection circuits – Triggering circuits.

UNIT II PHASE-CONTROLLED CONVERTERS

(9 Hrs)

Operation and analysis of single and three phase controlled rectifiers – half and fully controlled Converters with R, RL and RLE loads – Effect of source inductance on controlled rectifiers – Power factor and harmonic improvement methods - series converter, twelve pulse converter, Dual converter- circulating and non-circulating current mode.

UNIT III DC TO DC CONVERTERS

(9 Hrs)

Principles of step down and step up chopper – Class A, B, C, D and E chopper, voltage commutated, current commutated chopper, multi-phase chopper, principle of operation of buck, boost and buck boost regulators – switching schemes.

UNIT IV INVERTERS (9 Hrs)

Single phase and three phase voltage source inverters – Voltage and harmonic control techniques – Capacitor commutated current source inverter and auto sequential current source inverter.

UNIT V AC CHOPPER AND CYCLO CONVERETERS

(9 Hrs

Single phase and Three-phase AC voltage controllers – Control strategy – Single phase step-up/step-down midpoint type and bridge type cyclo-converters – Three phase cyclo-converters. Applications – regulated power supply, UPS, solid-state motor starter.

Text Books

- 1. P. S. Bimbhra, "Power Electronics", Khanna Publishers, New Delhi, 6th Edition, 2018.
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U20EET617

RENEWABLE ENERGY SOURCES

L T P C Hrs. 3 0 0 3 45

Course Objectives

- To impart knowledge on renewable energy sources and technologies.
- To gain adequate knowledge on variety of issues in harnessing renewable energy sources.
- To outline about the alternate renewable energy sources for both domestics and industrial applications.
- To provide knowledge about grid connectivity in renewable energy systems.
- To provide in-depth knowledge in the key concepts of energy policies.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Analyze the national and international energy scenario of renewable energy Sources. (K2)
- CO2 Design the aerodynamics of wind turbines and calculate their energy production. (K3)
- CO3 Analyze electrical power generation from biomass, geothermal, tidal, wave etc. (K2)
- CO4 Analyze technical and sustainability issues involved in the integration of renewable energy systems. (K2)
- CO5 Compare the cost economics of using renewable energy sources with non-renewable energy sources. (K2)

UNIT I SOLAR ENERGY

9 Hrs

Overview - Limitations of conventional energy resource - Importance of renewable sources - Types - Present Indian and international energy scenario. Solar Energy: solar thermal power and its energy conversion - solar collectors - types and applications. Photovoltaic (PV) technology - photovoltaic effect - efficiency of solar cells - Design Concept of solar PV system - standards and applications.

UNIT II WIND AND HYDRO POWER ENERGY

(9 Hrs)

Wind Energy: wind data – properties - speed and power relation - wind turbines and electric generators - horizontal and vertical wind mills - wind energy farms - off-shore plants- Selection factors. Hydro Energy: small, mini and micro hydro power plants and their resource assessment - plant layout with major components -selection factors-application.

UNIT III ALTERNATE ENERGY SOURCES

(9 Hrs

Biomass: Photosynthesis and origin of biomass energy – terms and definitions – pyrolysis, thermo-chemical biomass conversion to energy, Geothermal: resources, hot spring, steam system, site selection, challenges. Ocean and Tidal energy: principle of OTEC – wave energy conversion machines – fundamentals of tidal power, conversion systems and limitations – Introduction to fuel cells.

UNIT IV GRID INTEGRATION

(9 Hrs

Wind power interconnection requirement - low-voltage ride through (LVRT), ramp-rate limitations, supply of ancillary services for frequency and voltage control - load and reserve requirement - issues in interconnection - steady - state and dynamic performance of power system - interfacing solar system with grid - protective relaying, islanding, Power quality issues.

UNIT V RENEWABLE ENERGY POLICY

(9 Hrs)

Renewable energy policies: Five Year Plan programmes - Feed-in tariffs - portfolio standards - policy targets, tax incentives - bio-fuels mandates - International policies for climate change and energy security - Economic analysis and comparisons - Life cycle analysis - financial analysis - cost of conserved energy and externalities - Cost assessment of supply technologies versus energy - Efficiency - Renewable Energy Certification - Carbon contents.

Text Books

- G. N. Tiwari and M. K. Ghosal, "Renewable Energy Resources: Basic Principle and Application", Alpha Science International Ltd, New Edition, 2005.
- 2. B. H. Khan, "Non-Conventional Energy Resources", Tata McGraw Hill, 2nd Edition, 2009.
- 3. R. Loulou, P. R. Shukla and A. Kanudia, "Energy and Environment Policies for a sustainable Future", Allied Publishers Ltd, New Delhi, 1997.

Reference Books

- Solanki Chetan Singh, "Solar Photovoltaic Fundamentals, Technologies and Applications", PHI, New Delhi, 3rd Edition, 2015.
- 2. Mukund R Patel, "Wind and Solar Power Systems", CRC Press, New York, 2nd Edition, 2011.
- D. P. Kothari, K. C Singal, Rakesh Ranjan, "Renewable Energy Sources and Emerging Technologies", PHI Learning Pvt. Ltd, New Delhi, 2nd Edition, 2013.
- 4. John Twidell and Tony Weir, "Renewable Energy Resources", Routledge publication, 3rd Edition, 2015.
- Godfrey Boyle, "Renewable Energy: Power for a Sustainable Future", Oxford University Press, 3rd Edition, 2014.

Focus

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- 6. A. K. Mukerjee and Nivedita Thakur, "Photovoltaic Systems: Analysis and Design", PHI Learning Private Limited, New Delhi, 2011.
- 7. Ali Keyhani, "Design of Smart Power Grid Renewable Energy Systems", Wiley Publication, 1st Edition, 2011.

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- 1. https://nptel.ac.in/courses/103/107/103107157/
- 2. www.renewableenergyworld.com/rea/tech/home
- 3. www.eschooltoday.com/energy/renewable-energy
- 4. https://www.chetansinghsolanki.in/course.php
- 5. https://nptel.ac.in/courses/108/108/108108078/

COs/POs/PSOs Mapping

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Correlation Level: 1 - Low, 2 - Medium, 3 - High



U20EET618

POWER SYSTEM ANALYSIS

L T P C Hrs 2 2 0 3 60

Course Objectives

- To create computational models for power system using per unit analysis.
- To perform load flow analysis using Gauss Seidal and Newton-Raphson methods.
- To analyze the sequence network using symmetrical components.
- To import the knowledge about symmetrical and unsymmetrical faults in power system.
- To demonstrate different methods and factors influencing on power system stability

Course Outcomes

After completion of the course, the students will be able to

- CO1 Compute the reactance diagram and network matrices. (K4)
- CO2 Apply the iterative techniques to solve the power flow analysis used in power system planning. (K3)
- CO3 Analyze the Sequence networks using positive, negative and zero sequence network. (K4)
- CO4 Carry out short circuit studies to design the circuit breaker ratings in power system (K4)
- CO5 Analyze stability problems in power system during pre-fault and post-fault conditions (K4)

UNIT I MODELING OF POWER SYSTEM COMPONENTS

(12 Hrs)

Need for system planning and operational studies - Power system components - Representation - Single line diagram - Per unit quantities - P.U. impedance / reactance diagram - Formulation of network matrices for the power systems - Bus impedance and bus admittance matrices - Reduction techniques on network matrices for network changes - Z bus Building algorithm.

UNIT II LOAD FLOW STUDIES

(12 Hrs)

Classification of buses - formulation of load flow problem - Load flow solution by Gauss - Seidal, Newton - Raphson and Fast Decoupled Load Flow (FDLF) Analysis - Comparison - Computation of slack bus power, transmission loss and line flow - Voltage Control Methods - Tap-changing and phase - shifting transformers.

UNIT III SYMMETRICAL COMPONENTS AND SEQUENCE NETWORKS

(12 Hrs)

Symmetrical components – Simple problems to calculate symmetrical voltages and currents - Sequence networks - positive, negative and zero sequence networks - Sequence networks of Series impedance, loads and Rotating machines – Advantages and Limitations.

UNIT IV FAULT ANALYSIS

(12 Hrs)

Need for fault analysis - Types of faults - Symmetrical fault analysis through bus impedance matrix - Analysis of unsymmetrical faults- LG, LL and LLG - Analysis of simultaneous unbalanced short circuit and open conductor faults in power systems – short circuit capacity – circuit breaker selection - Representation of various types of faults in sequence networks.

UNIT V STABILITY STUDIES

(12 Hrs)

Definition - Importance of stability analysis- classifications - Steady state and transient stability - Angle and voltage stability - Single Machine Infinite Bus (SMIB) system - swing equation - Swing Curve - Numerical integration methods - Equal area criterion - Critical clearing angle and time - Factors affecting stability - Methods of improving transient stability.

Text Books

- P. Kundur, "Power System Stability and Control", Tata McGraw Hill Education Pvt. Ltd., New Delhi, 10th Reprint, 2013.
- 2. D. P. Kothari and I. J. Nagrath, "Power System Engineering", Tata McGraw-Hill Education, 3rd Edition, 2019.
- 3. Hadi Saadat, "Power System Analysis", Tata McGraw Hill Education Pvt. Ltd., New Delhi, 21st Reprint, 2010.

Reference Books

- M. A. Pai, "Computer Techniques in Power System Analysis", Tata McGraw-Hill Publishing Company Ltd., New Delhi, 2nd Edition, 2012.
- 2. J. Duncan Glover, Mulukutla S. Sarma, Thomas J. Overbye, "Power System Analysis and Design", Cengage Learning, 5th Edition, 2016.
- 3. John J. Grainger, Jr. William D. Stevenson, "Power System Analysis", McGraw Hill Education (India) Private Limited, New Delhi, 2nd Edition, 2015.



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- 1. https://nptel.ac.in/courses/108/105/108105067/
- 2. https://nptel.ac.in/courses/108/107/108107127/
- 3. https://pserc.wisc.edu/webinars/systems_webinars.aspx
- 4. https://www.classcentral.com/course/swayam-power-system-analysis-14243

COs/POs/PSOs Mapping

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Correlation Level: 1 - Low, 2 - Medium, 3 - High



U20EEE405

ENERGY STORAGE TECHNOLOGY

L T P C Hrs 3 0 0 3 45

Course Objectives

- To understand the purpose of energy storage systems.
- To learn the different energy storage techniques.
- To learn about the different types of batteries available for energy storage.
- · To impart knowledge regarding on the advanced energy storage systems.
- To learn about the different vehicular energy storage schemes.

Course Outcomes

After completion of the course, the students will be able to

- CO1 Familiarize the need for energy storing.(K2)
- CO2 Analyze the various energy storage techniques in the form of electrical, magnetic and chemical systems. (K3)
- CO3 Analyze the different batteries and its characteristics used for storing the energy in electric vehicles, nano-tubes etc.(K4)
- CO4 Impart the concepts of Superconducting Magnet Energy Storage Systems and super-capacitors in digital cameras, PC cards, electric vehicles, medical applications etc.(K3)
- CO5 Analyze the various energy storage techniques used in Electric vehicles and its hybridization concepts, power grid stabilization, rail-system power models etc.(K4)

UNIT I ENERGY STORAGE NEEDS

(9 Hrs)

Energy Storage Need of energy storage - Different modes of Energy Storage - Potential energy - Pumped hydro storage - Kinetic Energy and Compressed gas system - Flywheel storage, compressed air energy storage - Environmental and sustainability issues.

UNIT II ENERGY STORAGE TYPES

(9 Hrs)

Electrical and Magnetic energy storage, Capacitors, electromagnets - Chemical Energy storage - Thermochemical, photo-chemical, bio-chemical, electro-chemical, fossil fuels and synthetic fuels - Hydrogen for energy storage, Solar Ponds for energy storage. Electrochemical Energy Storage Systems, Case study on perovskite solar cell.

UNIT III BATTERIES (9 Hrs)

Batteries - Primary, Secondary, Lithium, Solid-state and molten solvent batteries - Lead acid batteries - Nickel Cadmium Batteries - Advanced Batteries - Role of carbon nano-tubes in electrodes - Flow battery operation - Flexible fiber battery- air batteries

UNIT IV SUPERCONDUCTING MAGNET ENERGY STORAGE SYSTEMS

(9 Hrs)

Superconducting Magnet Energy Storage(SMES) systems - Capacitor and Batteries: Comparison and application - Super capacitor - Electrochemical Double Layer Capacitor (EDLC), principle of working, structure, performance and application, role of activated carbon and carbon nano-tube - Super Capacitors - power calculation - operation and design.

UNIT V VEHICULAR ENERGY STORAGE SYSTEMS

(9 Hrs)

Energy storage technologies in hybrid vehicles – flywheel, hydraulic, fuel cell and hybrid fuel cell energy storage system – ultra capacitors – comparison – battery charging control

Text Books

- 1. Jiujun Zhang, Lei Zhang, Hansan Liu, Andy Sun, Ru-Shi Liu, "Electrochemical Technologies for Energy Storage and Conversion-2 Volume set", John Wiley and Sons, 1st Edition, 2011.
- Detlef Stolten, "Hydrogen and Fuel Cells: Fundamentals, Technologies and Applications", Wiley, 1st Edition, 2010.
- 3. Robert Huggins, "Energy Storage: Fundamentals, Materials and Applications", Springer, 2nd Edition, 2016.
- Andrei G. Ter-Gazarian, "Energy Storage for Power Systems", Institution of Engineering and Technology, 3rd Edition, 2020.

Reference Books

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- Francois Beguin and Elzbieta Frackowiak, "Super capacitors: Materials, Systems and Applications", Wiley-VCH, 1st Edition, 2013.
- 2. Doughty Liaw, Narayan and Srinivasan, "Batteries for Renewable Energy Storage", The Electrochemical Society, New Jersy, 2010.
- Ali Emadi, Mehrdad Ehsani, John M. Miller, "Vehicular Electric Power Systems: Land, Sea, Air and Space Vehicles", CRC Press, 1st Edition, 2003.
- Chris Mi, M. Abul Masrur, David Wenzhong Gao, "Hybrid Electric Vehicles: Principles and Applications with Practical Perspectives", Wiley, 1st Edition, 2011.

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 https://energystorage.org/why-energy-storage/technologies/
 https://www.renewableenergyworld.com/2019/10/22/which-new-energy-storage-technologies-might-outcompete-lithiumion-in-the-2020s/
- https://www.sciencedirect.com/topics/engineering/energy-storage-technology https://en.wikipedia.org/wiki/Energy_storage https://www.energy.gov/oe/activities/technology-development/energy-storage

COs/POs/PSOs Mapping

COs					Progr	am O	utcom	es (Po	Os)					ram Spe omes (PS	
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Correlation Level: 1 - Low, 2 - Medium, 3 - High



Annexure – III

Academic calendar (IV Year)

Use of Cell Phones

It has been decided not to permit cell phones inside the college campus, if any student is found using the cell phone inside the college campus, it would be confiscated and will not be returned back on any circumstances. Hence the students are instructed not to attend the college with the mobile phones.

The students are requested to atrend the college neatly dressed. While the male students should attend the college with the shirts neatly tucked in and with the shoes, the fennale students are permitted to come with churidar and dupatup preperly prined. Students wearing full hand shirts should wear it as such without folding; it to half etc. Casual wears like jeans. Teshirts etc., both for boys and girls are strictly prohibited inside the campus. Each department has prescribed uniforms for the labs. The students are requested to strictly adhere to the dress codes as well as the rules and resultations of the college. and regulations of the college

Maintenance of Discipline

Discipline is an important factor that shapes one's personality. It is considered as a golden key capable of opening many doors. This institution expects each and every student to follow the roles and regulations in total. Maintaining discipline in the campus will promote a conducive environment for studies.

I hour	09,30 a.m 10	10.20 a.m	Ihour	09.00am to	09.50 a.m
Break	10.20 a.m to	10.30 a.m	Hienr	09.50 a.m. to	10.40 a.sr
Hhour	10.30am to	11.20 a.m	Percak	10.40 a.m to	10.55 a.m
Ilweak	11.20 a.m to	11.30a.m	III hour	10.55 a.m to	11.45 a.m
III hour	11.30a.m. to	12.20 p.m	Whom	II.45 a.m. to	12.35 p.n
IV how	01.30 p.m to	02.20 p.m	Vhour	01.15pm to	02.05 p.n
Freak.	02.20 p.m to	02.30 p.m	VIhour	02.05 p.m to	02.55 pm
V hour	02.30 p.m to	03.20 pm	VIIhou	2.55 pm to	03.45 p.n
Break	63.20 p.m to	03.30 p.m	VIIIhour	03.40 pm to	04.35 p.m
Vihour	03.30 p.m to	04.20 p.m			

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE An Autonomous Institution (Accredited by NBA-AICTE. New Delhi, NAAC with "A" Grade) Madagadipet, Puducherry - 605 107 Academic Calendar January 2022 to May 2022

Programme : B.Tech

Department :

Year / Sem : IVYr / VIII Sem

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بالمهام فويفاده بالمادة كري

About Autonomous

Sri Manakula Vinayagar Engineering College has been conferred with Antonomous Status by the University Grants Commission on 26° September 2019 and the same was approved by Pondicherry University on 19° June 2020. The Pondicherry University Regulations R2013 is followed for the students admitted in the Academic

University Regulations R2013 is followed for the students admitted in the Academic Year 2018-19 (present Final years).

End semester examinations process Le, question paper setting, answer script evaluation and result will be published by SAVYEC College.

[Functuality in Attendance]

The students are requested to keep up punctuality in attending the college. The late contex will be long their attendance and in uru the internal marks. Hence all the students are requested to attend the college in time. A student shall be permitted to appear for the End Semester Examination at the end of the semester only if he/she secures not less than 75% of overall attendance.

Repealing the Course
A student who secures overall attendance which is less than 60% has to repeat the course with the approval of the University when it is next offered.

Tutor Ward System

In the tutor ward system, 30 students are allotted to a tutor who will be taking care of these students. The students are requested to utilize the facility.

of these students. The students are requested to utilize the facility.

Continuous Assessment Marks for Theory; 25 Marks

25 marks are allotted for continuous assessment for a theory paper. Out of 25 marks
20 marks are awarded for the continuous assessment tests based on the performance
of the student in the CAT-I. It & Model exam and the remaining for marks are awarded
for class attendance. The distribution of 5 marks for continuous assessment are as follows:
marks for 95% attendance and above but below 95%
1 marks for 95% attendance and above but below 95%
2 marks for 80% attendance and above but below 95%
2 marks for 55 attendance and above but below 85%

Continuous Assessment Marks for Paractical: 5 Marks

Continuous Assessment Marks for Paractical: 5 Marks

Continuous Assessment Marks for Paractical: 5 Marks

mark 107 75% attendance and above but below 80% [Continuous Assessment Marks for Practical: 50 Marks

For a practical subject, where there is a end sequester examination, 50 marks for external examination and 50 marks for continuous assessment are allocated. The distribution of 50 marks for continuous assessment are allocated. The distribution of 50 marks for continuous assessment are allocated. The form of the 10 marks for 10 marks for 10 marks for Model Examination 10 marks

For average of marks for experiment report for each exp. 15 marks

For average of Pre/post-test viva for each exp. 10 marks

Total 50 marks

WI-FI Campus

Our campus has been enabled by high speed uninterrupted Wi-Fi connectivity.

The Computer Centre is open till 8.00 p.m. on all the working days except on the dates of University examinations.

Library Working Hours

8.30 a.m. to 8.30 p.m. (On all the working days)
8.30 a.m. to 10.00 p.m. (During the examination days)

[Women Cell]

For the benefit of the girl students, a Women Cell has been constituted in the college. The girl students mayapproach the Chairperson/members for assistance.

Grievance Redressal Cell

There is a Crievance Redressal Cell under the Chairmanship of the Director of the institution. Students are requested to approach the Chairman/members to redress their grievances. Mail ID: grievance@suvec.ac.iu

Distribution of Aftendance marks for theory : 5 marks

The distribution of 5 marks for theory class attendance is as follows: 5 marks for 95% attendance and above

4 marks for 90% attendance and above but below 95% 3 marks for 85% attendance and above but below 90% 2 marks for 80% attendance and above but below 85% 1 mark for 75% attendance and above but below 80%

Distribution of Attendance marks for practical: 10 marks

The distribution of 10 marks for practical class attendance is as follows:

110 marks for 90% attendance and above 8 marks for 90% attendance and above but below 95% 6 marks for 85% attendance and above but below 90% 4 marks for 80% attendance and above but below 85%

2 marks for 75% attendance and above but below 80%

Students should not be absent for the online classes/regular classes. Attendance for the online classes/regular classes are monitored regularly and it is recorded. Continuous assessment mark will be based on the performance of the students in the continuous assessment test. assignment and attendance percentage.

Assignments: 5 marks

Out of 25 continuous assessement marks, 5 marks will be awarded for the assignment. The assignment questions will be different for each and every smalent. The students have to submit 3 assignments in each subject. Best of 2 out of 3 assignments will be consider.

Importance of Continuous Assessment Marks(CAM)

The continuous assessment marks cuce carned are carried over to the subsequent exams also. Hence the students are requested to work hard to get the maximum contineus assessment marks. If the continuous assessment marks are lower, they up the down chances of getting the first class, distinction, gold medals and ranks.

Importance of CAT-I/CAT-II/Model Examination

Continuous assessment marks are awarded for the performance in the CAT-I, CAT-II & Model Exam, Hence all the students are requested to prepare well for each text/examination to earn the maximum continuous assessment marks.

Undertaking Minor/Major Projects

Each student is advised to take attest one minor project. Involving in the project will be helping to understand the basics of the subject. Some of the minor / major project will also be benefiting the society. Moreover, the Management awards cash prizes for the best projects in each department.

Participation in the Curricular / Co-curricular / Extra curricular Activities

All the students are encouraged to participate in the curricular / co-curricular / extra curricular activities. Involvement in these activities will improve their knowledge level in the subject. If a tudent or a term gets can by nize / award in the technical event organized by the recognised institutions, then the unanagement of this institution will also sanction an amount equivalent to the winning award / cash prize as a token of appreciation.

Leave Account Record

For each student, leave account record has been provided. The students are instructed to show the leave record to their parents and strictly adhere to the instructions given for availing the leave. The leave account record should be maintained properly and prior approval must be obtained for availing the leave. In exceptional cases, the students are permitted to get the approval after availing the leave.

Transport Facility

Litausport raculty!

Kanagachertikulam, Viliquiram, Neyyeli, Panivit, Cuddalore, Nellikuppam, Madukarai, Tindivanam, Tiruvannamahi and virudhachalam covering almost all the areas. Separate transport facility last been arranged for the students who remain in the college after 5 µm. Eruthising computer lab, library and sports facilities. The students are requested to utilise the transport facility.

All the sindents are requested to avoid mobile phones and travel by two wheelers considering their safety and security.

Date	Day	Schedule	Working da Holiday
1	Wed		
2	Thu		
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		[1	'Iacemen'	Record			
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Students Placed	95%	95%	93%	95%	95%	95%	96%
	[Details	f Placed	Students :	2021 - 22	1	
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				Total		F745	

Gold Me	dals and Top Ten	Ranks
and the top ten ranks it als and Top Ten Ra	all the branches uks won by the	The details of the University students are given below:
alone should be considere te subjects from 14 to 24 se	d and it is mandato mester in the first a	ry that the candidate should have
/II	Year	
2017	2018	2019
2, 4, 6, 7	W	£23,467,89,10
2.3,4,5,6,7,8,9,10		\$ 3.45.67.9.10
% ,2, 3, 4, 10	-	3,2,4.6,7,8,10
12,3,4.5,6,7,8,9,10	Я	¥ 2,3,5,6,8
,2,3.4.5,6,7,8,9,10	9	2,3,4,5,6,7,8,9,10
4, 5, 7, 9, 10		3,7.8,10
2, 3, 10		2.3,4.6,7.10
3,4,7,9,10	8	£2.67.89,10.11
\$ 3,4,6.7,8		% 23A,578,10
× 2,3,4,5,7,8,9		9.7
2, 3, 6, 7, 8, 9		2.3.45
9		N 23
2,3,4,5,7,8,9		% 23
Я		×2.3.4
9 ,2		8
	s were sincere, hard we and the top ten ranks it als and Top Ten Ra tes the Gold medal at do Gold Medal and rank alone should be considere e subjects from 1° to 5° s fixt five candidates in en 2017 2.4.6.7 2.3.4.5.6.7.8.9.10 2.3.4.10 2.3.4.5.6.7.8.9.10 2.3.4.5.6.7.8.9.10 2.3.4.5.6.7.8.9.10 2.3.4.5.6.7.8.9.10 2.3.4.5.6.7.8.9.10 2.3.4.5.7.8.9.10 2.3.4.5.7.8.9.10 2.3.4.5.7.8.9.10 2.3.4.5.7.8.9.10 2.3.4.5.7.8.9.10 2.3.4.5.7.8.9	2017 2018 2.4.6,7

Date	Day	Schedule	Working day Holiday
1	Frì		58
2	Sat		59
3	Sun		Holiday
4	Mon		60
5	Tue	Feedback from the students & Analysis	61
6	Wed	QCM meeting-3	62
7	Thu		63
8	Fri		64
9	Sat		65
10	Sun		Holiday
11	Mon	Model for IV Year (5 units)	66
12	Tue		67
13	Wed		68
14	Thu	Tamil New Year	Holiday
15	Fri	Good Friday	Holiday
16	Sat		69
17	Sun		Holiday
18	Mon		70
19	Tue		71
20	Wed		72
21	Thu		73
22	Fri	Model Result Analysis & Report Submission	74
23	Sat	Submission of Attendance & Assessment-Term III	75
24	Sun		Holiday
25	Mon	Tentative End Semester Examination starts	
26	Tue		
27	Wed		
28	Thu		
29	Fri		
30	Sat		
انت		Total number of working days: 1 Total number of holiday: 7	7

Date	Day	Schedule	Working day Holiday
1	Tue		31
2	Wed	Phase II Second review	32
3	Thu		33
4	Frì		34
5	Sat		35
6	Sun		Hohday
7	Mon		36
8	Tue	Feedback from the students & Analysis	37
9	Wed	QCM meeting - 2	38
10	Thu		39
11	Fri		40
12	Sat		41
13	Sun		Holiday
14	Mon	CAT-II for IV Year (1 % units)	42
15	Tue		43
16	Wed		44
17	Thu	†	45
18	Fri		46
19	Sat	1	47
20	Sun		Holiday
21	Mon	CAT-II Result Analysis & Report Submission	48
22	Tue	Submission of Attendance & Assessment -Term II	49
23	Wed	Final Project Review	
24	Thu		51
25	Fri		52
26	Sat		53
*********			· Commong panels of the same

March 2022

Total number of working days : 27
Total number of holiday : 04
சவித்துக் கொள்பவன் ஒவ்வொரு வாய்பாலும் உள்ள ஆபத்தைப் பார்க்கிறான். சநியவன் ஒவ்வைகு ஆபத்திலும் உள்ள வாய்காகணி பார்க்கிறான்.

January 2022

57

Important points for the kind attention of the Parents

Dear Parent
The VIII semester classes commences on 24th January 2022. The above mentioned semester is a very short term, including working days meant for model exam. The students have to complete a lot of work within a short period. Hence the parents are kindly requested not to permit their wards to avail frequent leave during this semester period for the following reasons.

Marks in the continuous assessment test decide the major part of the continuous assessment marks. So, availing leave for the continuous assessment test must be avoided at any cost as this would seriously affect the continuous assessment marks.

Practicals are very important not only to score more marks but also it will help to understand the theory part of the subject, hence advice your ward not to avail leaves during practical classes.

Please spare your valuable time to talk to your son/daughter every day and try to understand what he/she is doing in respect of his/her studies. Kindly extend all your support to your son/daughter which will help them to come out successfully. For any assistance from our side you may always fed free to contact the respective Coordinator / HOD any time during the working boars.

Date	Day	Schedule	Working day Holiday
1	Sat	New Year	Holiday
2	Sun		Holiday
3	Mon		
4	Tue	100	1 141
5	Wed	- 1 - 1	
6	Thu	327	
7	Fri		
8	Sat	245	
9	Sun	**************************************	Holiday
10	Mon	a vizina avan A vizi ka es e versane kan e vana anna Januaria.	
11	Tue		
12	Wed		
13	Thu	Bogi Festival	Holiday
14	Fri	pongal	Holiday
15	Sat	Thiruyalluvar Day	Holiday
16	Sun	Uzhavar Thiruna	Holiday
17	Mon		
18	Tue		
19	Wed		
20	Thu		
21	Fri		
22	Sat		
23	Sun	200404-000000	Holiday
24	Mon	Commencement of VM semester classes	1
25	Tue		2
26	Wed	Republic day	Holiday
27	Thu		3
28	Fn		4
29	Sat		5
30	Sun		Holiday
31	Mon		В
			
	L	Total number of working days : 0 Total number of holiday :03	6

Date	Day	Schedule	Working day Holiday
1	Tue		7
2	Wed	4	8
3	Thu		9
4	Fri		10
5	Sat		-11
6	Sun		Holiday
7	Mon		12
8	Tue	Feedback from the students & Analysis	13
9	Wed	QCM meeting - 1 Phase II First review	14
10	Thu		15
11	Fri		16
12	Sat		17
13	Sun	Charles and the second	Holiday
14	Mon	CAT4 for IV Year (1 ½ units)	18
15	Tue		19
16	Wed		20
17	Thu		21
18	Fri		22
19	Sat		23
20	Sun		Holiday
21	Mon	CAT-I Result Analysis & Report Submission	24
22	Tue		25
23	Wed	Submission of Attendance & Assessment - Term I	26
24	Thu		27
25	Fri	***************************************	28
26	Sat		29
27	Sun		Holiday
28	Mon		30
		Total number of working days : 24	
		Total number of holiday: 04 ouguneer effectivementum, monganeer arrogation 6	

Academic calendar (III & II Year)

Use of Cell Phones

It has been decided not to permit cell phones inside the college campus. If any student is found using the cell phone inside the college campus, it would be confiscated and will not be returned back on any circumstance. Hence the students are instructed not to attend the college with the mobile phones.

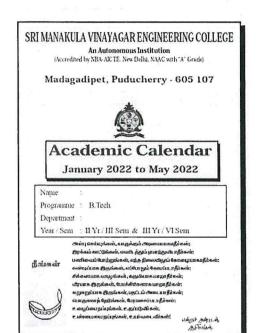
Dress Code

The students are requested to attend the college neatly dressed. While the male students should attend the college with the shirts neatly tucked in and with the shoes, the female students are permitted to crane with churidar and dispatia properly inted. Students wearing full hand shirts should wear it as such without folding it to half etc. Casual wears like jeans, Teshirts etc., both for boys and girls are strictly prohibited inside the campus. Each department has prescribed uniforms for the labs. The students are requested to strictly adhere to the dress codes as well as the rules and resultations of the college. and regulations of the college.

Maintenance of Discipline

Discipline is an important factor that shapes one's personality. It is considered as a golden key capable of opening many doors. This institution expects each and every student to follow the rules and regulations in total. Maintaining discipline in the campus will promote a conducive environment for studies.

I hour	09.30 a.m to	10.20 a.m	Ihour	09.00 a.m 10	09.50 a.m
Break	10.20 a.m to	10.30 a.m	Thour	09.50 a.m to	10.40 a.rr
11 hour	10.30a.m to	11.20 a.m	Feeak	10,40 a.m to	10.55 a.n
Cheak	11.20a.m to	11.30a.m	III hour	10.55 a.m. to	11.45 a.m
III hour	11.30a.m. to	12.20 p.m	Whom	11.45 a.m to	12.35 p.n
IV hour	01.30 p.m to	02.20 p.m	Vhour	01.15pm to	02.05 p.n
Preak.	02.20 p.m. to	02.30 p.m	VIhour	02.05 p.m to	02.55 pm
V hom	02.30 p.m to	01.20 p.m	VIIhou	2.55 pm 10	03.45 p.n
Fireak	03.20 p.m to	03.30 p.m	VIIIhour	03.40 pm to	04.35 p.n
Vihour	03.30 pm to	04.20 p.m			



Sri Manakula Vinayagar Engineering College has been conferred with Autonomous Stans by the University Grants Commission on 26° September 2019 and the same was approved by Pondicherry University on 19° June 2020. SMYEC Autonomous Regulations R2019, is followed for the students admitted in the Academic Year 2019-20 (present Third Year) SMYEC Autonomous Regulations R2020, is followed for the students admitted from the Academic Year 2020-21 onwards (present first year for season). & second year)

HIGHLIGHTS OF SAIVEC AUTONOMOUS REGULATIONS 2019/2020 & CURRICULUM

- Industry 4.0 ready curriculum
- Curriculum towards skill development and to create more job opportunities
- Multidisciplinary curriculum
- Oriented towards entreprenurship development Choice to Jearn IELTS? Foreign Languages
- Department wise Gold Medals
- Results will be declared within a month after completion of examinations
- Supplementary Examination in $5^{\rm th}$ and $8^{\rm th}$ semester for the students having 2

· Ethnotech/Mandatory course

Ellanofich/Mandatory course. The Institute has Established 17 Center of Excellence to provide 91 International Certification courses from IBM, Geogle, Cisco, e Plan, Microsoft, Autodesk, Texas instruments, Festo, Bentley, Schneider Electric, Amazen web services, Siemens, Tally, DELL EMC², Harita Techserv, PTC, LN an Excellence in Technology & Didactic solutions. All students should enroll in certification course from semester-I to semester-VI.

· Industrial Training / Internship

Students may undergo training or internship during summer / winter vacation at Industry/ Research organization. Students are also pennitted to undergo internships during their eighth semester after the completion of theory classes.

Date	Day	Schedule	Working day Holiday
1	Wed		
2	Thu		
3	Fri		
4	Sat		
5	Sun		Holiday
6	Mon	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	
7	Tue		
8	Wed		
9	Thu		
10	Fri		
11	Sat		
12	Sun		Holiday
13	Mon		
14	Tue		
15	Wed		
16	Thu		
17	Fri		- Land Committee
18	Sat		
19	Sun		Holida
20	Mon		
21	Tue		
22	Wed		
23	Thu		
24	Fri		
25	Sat		
26	Sun		Holida
27	Mon	***************************************	
28	Tue		
29	Wed		
30	Thu		

	To	otal number of working days Total number of holiday:	

	322

Date	Day	Schedule	Working day Holiday
1	Sun		Holiday
2	Mon		84
3	Tue	Ramzan	Holiday
4	Wed		85
5	Thu		86
6	Fri		87
7	Sat		88
8	Sun		Holiday
9	Mon	Tentative End Semester Theory examination Starts	
10	Tue		
11	Wed		-
12	Thu		
13	Fri		
14	Sat		
15	Sun		Holiday
16	Mon		
17	Tue		
18	Wed		
19	Thu		
20	Fri		
21	Sat		
22	Sun		Holiday
23	Mon		
24	Tue		1
25	Wed	Tentative Commencement of next semester classes	-
26	Thu		·
27	Fri	anne de la company de la compa	************
28	Sat		Holiday
29	Sun		
30	Mon		
31	Tue		1

Total number of holiday : 03

р быццубины льту слістту репу, быцу итридні 1966-ж другиду — ізбупі

Supplementary Examinations

Supplementary examination is an additional examination conducted within a ougarementary examination is an additional examination conducted within a mouth of time after declaring the results of end semester examination. In each to complete the program within 4 years, only the student with maximum of two arrests will be permitted to appear for supplementary examination. The supplementary examination will be conducted in fifth and eighth semester only. For supplementary examination, the continuous assessment marks of the last attempt will be considered.

- Benefits

 More number of students will receive the degree within the stipulated time
 - The industries prefers to recruit students with no standing arrear. If the supplementary examinations is conducted then more number of students will be eligible for the recruitment.

Photo copy of answer book

After the publication of the result, photocopy of the answer books shall be provided to the student on request with stipulated fee fixed by the College from time to time

Punctuality in Attendance

The students are requested to keep up punctuality in attending the college. The late corners will be losing their attendance and in turn the internal marks. Hence all the students are requested to attend the college in time. A student shall be penuited to appear for the End Semester Examination at the end of the semester only if he/she secures not less than 75% of overall attendance.

Redo Category

A student who secures overall attendance which is less than 60% has to repeat the course with the approval, when it is next offered.

Tutor Ward System

In the tutor ward system, 30 students are allosted to a tutor who will be taking care of these students. The students are requested to utilize the facility.

Marks Distribution of Continuous Assessment Marks (CAM) and End Semester Examination Marks (ESM) Scheme for Continuous Assessment Test(CAT)

		Continues Assessment components									
S. No	Course Type	Test Marks	Average of prepost test viva for each experiment	Average of maris for experient report for each experient	Model Exam	Assignment	Review-1	Review-2	Review-3	Attendance	Total
1.	Theory	15	-			5	-	-	-	5	25
2	Practical		10	15	15	-	-	-		10	50
3.	Project work			-	-	-	10	10	30		40

The internal marks will be provided fully based on the continuous assessment

Weightage of Assessment for Theory Course Test Marks Duration of Test S. No. Model 5 Units 50 75 1 % hours
3 hours Continuous Assessment for Theory Course

Question Paper Pattern
Question paper for CAT and ESE will be based on the patterns shown in Table (a) and (b)

Table (a) Question Paper pattern for CAT/Model Exam

Test Type	2 Marks	5 Marks	10 Marks	Total Marks
CAT 1 to 2	5(questions) (10 Marks)	4(questions) (20 Marks)	2 (questions) (20 Marks)	.50
Model	End sem	75		

Table (b) Question paper pattern for End semester Examination(ESE) 2 Marks 5 Marks 10 Marks Total Marks 5 (25 Marks) 3 (30 Marks) 75 10(20 Marks) requestions from each unit) (out of 5 questions

	Day	Schedule	Working day Holiday
1	Fri		60
2	Sat		61
3	Sun		Holiday
4	Mon		62
5	Tue		63
6	Wed	Assignment - III	64
7	Thu	Feedback from the students & Analysis	65
8	Fri	QCM meeting - 3	66
9	Sat		67
10	Sun		Holiday
11	Mon	Model for II, III Year (5 units)	68
12	Tue		69
13	Wed		70
14	Thu	Tamil New Year	Holiday
15	Fri	Good Friday	Holiday
16	Sat		71
17	Sun		Holiday
18	Mon		72
19	Tue		73
20	Wed		74
21	Thu	Last working day for II, III Year	75
22	Frí	Model Result Analysis & Report Submission	76
23	Sat	Submission of Attendance & Assessment -Term III	77
24	Sun		Holiday
25	Mon	Tentative End Semester practical examination starts	78
26	Tue		79
27	Wed	67 10000 1000 1	80
28	Thu		81
29	Fri		82
30	Sat	Tentative End Semester practical examination ends	83

வெற்றி என்று, லாசிரத்தைப் மூர்கராகப் புரிந்து கொள்வது -- தைப்றங்கேல்

March 2022

Date	Day	Schedule	Working day Hotiday
1	Tue		33
2	Wed		34
3	Thu		35
4	Fri		36
5	Sat		37
6	Sun		Holiday
7	Mon		38
8	Tue		39
9	Wed	Assignment - II	40
10	Thu	Feedback from the students & Analysis	41
11	Fri	QCM meeting - 2	42
12	Sat	•	43
13	Sun		Holiday
14	Mon	CAT-II for II, III Year (1 1/2 units)	44
15	Tue		45
16	Wed		46
17	Thu		47
18	Fri		48
19	Sat		49
20	Sun		Holiday
21	Mon	CAT-II Result Analysis & Report Submission	50
22	Tue		51
23	Wed	Submission of Altendance & Assessment-Term II	52
24	Thu		53
25	Fri		54
26	Sat	L symbol v	55
27	Sun		Holiday
28	Mon		56
29	Tue		57
30	Wed		58
31	Thu	1	59

Total number of working days : 27
Total number of holiday : 04
எலித்துக் கொள்பவன் ஒலில்வாகு வாய்பிறும் உள்ள ஆயத்தைப் பார்க்கிறான். எலித்துக் கொள்பவன் ஒலில்வாகு வாய்பிறும் உள்ள ஆயத்தைப் பார்க்கிறான்.

Distribution of Attendance marks for theory : 5 marks

The distribution of 5 marks for theory class attendance is as follows: 5 marks for 95% attendance and above 4 marks for 95% attendance and above but below 95% 3 marks for 85% attendance and above but below 95% 3 marks for 85% attendance and above but below 95% 2 marks for 85% attendance and above but below 85% mark for 85% attendance and above but below 80% attendance 80% attend

Distribution of Attendance marks for practical: 10 marks

The distribution of 10 marks for practical class attendance is as follows; 10 marks for 95% attendance and above 8 marks for 90% attendance and above but below 95% 6 marks for 80% attendance and above but below 90% 4 marks for 85% attendance and above but below 55%; 2 marks for 80% attendance and above but below 55%; 2 marks for 75% attendance and above but below 80%

Students should not be absent for the online classes/regular classes. Attendance for the online classes/regular classes are monitored regularly and it is recorded. Continuous assessment mark will be based on the performance of the students in the continuous assessment test, assignment and attendance percentage.

Assignments : 5 marks

Out of 25 continuous assessment marks, 5 marks will be awarded for the assignment. The assignment questions will be different for each and every student. The students have to submit 3 assignments in each subject. Best of 2 out of 3 assignments will be consider.

Women Cell

For the benefit of the girl students, a Women Cell has been constituted in the college. The girl students may approach the Chairperson / members for assistance.

Grievance Redressal Cell

There is a Grievance Redressal Cell under the Chairmanship of the Director of the institution. Students are requested to approach the Chairman / members to redress their grievances. Mail ID: grievance@smvec.ac.in

Importance of Continuous Assessment Marks(CAM)

The continuous assessment marks once carned are carried over to the subsequent exams also. Hence the students are requested to work hard to get the maximum continuous assessment marks. If the continuous assessment marks are lower, it will pull down chances of getting the first class, distinction, gold medals and ranks.

Importance of CAT-I/CAT-II/Model Examination

Continuous assessment marks are awarded for the performance in the CAT-I, CAT-II & Model Exam, Hence all the students are requested to prepare well for each test/examination to earn the maximum continuous assessment marks.

Undertaking Minor / Major Projects

Each student is advised to take atleast one uninor project. Involving in the project will be helping to understand the basics of the subject. Some of the uninor / major project will also be benefiting the society. Moreover, the Management awards cash prizes for the best projects in each department.

Participation in the Curricular / Co-curricular / Extra curricular Activities

All the students are encouraged to participate in the curricular / co-curricular / extra curricular activities. Involvement in these activities will improve their knowledge level in the subject. If a student or a team gets cand prize / award in the technical event organized by the recognised institutions, then the management of this institution will also stanction an amount equivalent to the winning award / cash prize as a token of appreciation.

Leave Account Record

For each student, leave account record has been provided. The students are instructed to show the leave record to their parents and strictly adhere to the instructions given for availing the leave. The leave account record should be maintained properly and prior approval must be obtained for availing the leave. In exceptional cases, the students are permitted to get the approval after availing the leave.

Transport Facility

(Transport Facility)

61 buses have been arranged for the students to reach the college from Puducherry,
Kansapachetrikulam, Villippuram, Neyveli, Panruti, Cuddalore, Nellikupparam,
Madukarai, Timduvamam Travnommanial and virtudhachalam covering almost all the
nreas. Separate transport facility has been arranged for the students who remain in the
ordege after 5 pm. for twilbing conquient lab, library and sports facilities. The students are
requested to utilise the transport facility.
All the students are requested to avoid mobile phenes and travel by two wheelers
considering their safety and security.

February 2022

Day	Schedule	Working day Holiday
Tue		9
Wed		10
Thu		31
Fri		12
Sat		13
Sun		Holiday
Mon		14
Tue		15
Wed	Assignment - I	16
Thu	Feedback from the students & Analysis	17
Fri	QCM meeting - 1	18
Sat		19
Sun		Holiday
Mon	CAT-I for IL III Year (1 1/2 units)	20
Tue		21
Wed		22
Thu		23
Fri		24
Sat		25
Sun		Holiday
Mon	CAT-I Result Analysis & Report Submission	26
		27
	Submission of Attendance & Amessment - Term I	28
		29
	***************************************	30
Sat	······································	31
Sun		Holiday
		32

	Tue Wed Ihu Fri Sal Sun Mon Tue Wed Thu Fri Sal Sun Mon Tue Wed Thu Fri Sal Sun Mon Tue Wed Thu Fri Sal Sun Mon Tue Fri Fri Fri	Tue Wed Thu Fri Sat Sun Mon CAT-I for It. III Year (1 1/2 units) Fin Sat Sun Mon CAT-I for It. III Year (1 1/2 units) Fin Sat Sun Mon CAT-I for It. III Year (1 1/2 units) Fin Sat Sun Mon CAT-I Result Analysis & Report Submission Tue Wed Submission of Altendance & Assessment - Term I Thu Fri Sat Sun

Total number of holiday: 04

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Date	Day	Schedule	Working da Holiday
1	Sat	New Year	Holiday
2	Sun		Holiday
3	Mon		
4	Tue		
5	Wed		
6	Thu		******
7	Fri		
8	Sat	***************************************	
9	Sun		Holiday
10	Mon		
11	Tue		*****
12	Wed		
13	Thu	Bogi Festival	Holiday
14	Fri	pongal	Holiday
15	Sat	Thiruvalluvar Day	Holiday
16	Sun	Uzhavar Thiruna	Holiday
17	Mon		
18	Tue	***************************************	*****
19	Wed		
20	Thu		***************************************
21	Fri	Commencement of III & VI semester classes	1
22	Sat		2
23	Sun		Holiday
24	Mon		3
25	Tue		4
26	Wed	Republic day	Holiday
27	Thu		5
28	Fri		6
29	Sat		7
30	Sun		Holiday
31	Mon		8

		Total number of working days: 08 Total number of holiday:03	

s trainings	for personality and inter			
ha and and			mal skill development	
ne stonems	to get in-plant training		and the samplified	
s industrial	visits			
awareness	on the opportunities ope	n for	higher studies.	
				٤.
	•	laced	Students: 2021-22	
	KAAR Technologies			3
				(
85%				
95%				
C CCO				
93%	Zucpi			
95%		1 1		2
05%		124		
1				
96%		*		18
		17		13
	Secure Cloud	6		
	Support Studio	1 4		
	Capgemini	6		
	Eaton	1 4		
	Harman	13		
	Mindtree	3		
	HCL.	5	Total	745
	awareness of coaching on Record ic Students. Placed 1 85% 5 95% 6 95% 6 95% 6 95% 6 95% 6 95% 6	awareness on the epportunities ope- scoeching classes for GATE, General desired states for GATE, General solution of the control of the contr	awareness on the eppertunities open for scoaching CRF. TOF mtRecord Details of Pilacet	awareness on the opportunities open for higher studies, seconching classes for GATE, GRF. (TOFL. IELT'S, 1AS, IES et al. (Control of the control of the cont

	Gold Me	dals and Top Ten R	anks
University	and the top ten ranks i	n all the branches uks won by the st	old medals of the Pondicherry The details of the University udents are given below. Rank.
§6 semester passed all ti	alone should be considere	d and it is mandstory muester in the first atto	dy, the CGPA secured from 1° to that the condidate should have mpt. Rank certificates would be
Name of the	T	Year	
Сонизе	2017	2018	2019
BTate EEE	2,4,6,7	8	£23,467,89,10
Black ECE	23,45,6,7,8,9,10		× 3.45.67.9.10
Black (SE	% 2.3,4,10		%,2,4,6,7,8,10
B.Tach II	% ,2,3,4,5,6,7,8,9,10	9	2,3,5,6,8
Black KE	,2,3.4.5,6,7,8,9,10	9	2,3,4,5,6,7,8,9,10
Hich Md	% .4.5, 7, 9, 10		3,7,8,10
Blade Civil	2, 3, 10		2.3,4,6,7,10
MCA	1,4,7,9,10	R	£2,6789,10.11
MBA	9 3,4,6.7,8		¥23.45.78.10
MToda CSE	% 2,3.4,5.7,8,9		N 7
MToh ECE	2, 3, 6, 7, 8, 9		2.3,4,5
MTch FED	8		· 23
MichNW	2, 3, 4, 5, 7, 8, 9		R 23
M Redn(NLSD)	Я	165	% 2,3,A
MRch(M)	₩,2	737 721 72 172 772	- 19

Important points for the kind attention of the Parents

Dear Parent

The III and VI semester classes commences on 21° January 2022. The above mentioned semester is a very short term, including working days meant for model exam. The students have to complete a lot of work within a short period. Hence the parents are kindly requested not be permit their wards to avail frequent leave during this semester period for the following reasons.

III Semester (II Year) & VI Semester (III Year): All the III & VI semester papers are considered as analytical papers. Hence, regular attendance and more concentration are required to clear these semester papers.

Marks in the continuous assessment test decide the major part of the continuous assessment marks. So, availing leave for the continuous assessment test must be avoided at any cost as this would seriously affect the continuous assessment marks.

Practicals are very important not only to score more marks but also it will help to understand the theory part of the subject, hence advice your ward not to avail leaves during practical classes,

Please spare your valuable time to falk to your son/daughter every day and try to understand what he/she is doing in respect of his/her studies. Kindly extend all your support to your son daughter which will help them to come our successfully. For any assistance from our side you may always feel free to contact the respective Coordinator / HOD any time during the working flours.

Academic calendar (I Year)

Use of Cell Phones

It has been decided not to permit cell phones inside the college campus. If any student is found using the cell phone inside the college campus, it would be confusated and will not be returned back on any circumstances. Hence the students are instructed not to attend the college with the mobile phones.

Dress Code

The students are requested to artend the college neatly dressed. While the male students should attend the college with the shirts neatly tucked in and with the shoes, the female students are permitted to come with churidar and dupatta property pined. Students wearing fall hand shirts should wear it as such without folding it to half etc. Castal wears like jeans, T-shirts etc., both for boys and girls are strictly probibited inside the campus. Each departured has prescribed uniforms for the labs. The students are requested to strictly adhere to the dress codes as well as the rules and remulations of the college.

Maintenance of Discipline

Discipline is an important factor that shapes one's personality. It is considered as a golden key capable of opening many doors. This institution expects each and every student to follow the rules and regulations in total. Maintaining discipline in the campus will promote a conducive environment for studies.

Ihour	09.30 a.m to	10.25 a.m	I hour	8.45 am to	09.35 a.m
Break	10.25 a.m to	10.30 a.m	II hour	09.35 a.m. to	10.25 a.m
II hour	10.30 a.m to	11.25 a.m	III hour	10.25 a.m to	11.15 a.m
Break	11.25a.m to	11.30 p.m	Break	11.15 a.m. to	11.35 a.m
III hour	11.30 a.m to	12,25 p.m	IV hour	11.35 a.m. to	12.25 p.u
IV hour	01.30pm ta	02.25 p.m	Vhour	12.25 p.m to	01.15 p.n
Break	02.25 p.m to	02.30 p.m	Vibour	01.55 pm to	02.45 p.u
Vhour	02.30 pm ta	03.25 p.m	VIIhour	02.45 p.m to	03.35 p.n:
Break	03,25 p.m to	03.30 p.m	VIII hour	03.35 p.m to	04.25 p.n
VIhour	03.30 p.m to	04.25 p.m	5		

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE (An Autonomous Institution) Occorded to Belanti, we then and Aureland by NMC with W Grade Madagailipet, Puducherry - 603 107 Academic Calendar November 2021 to April 2022 Name: Programme: B. Tech. Department: Year / Sem: 1 year / 1 semester Chart Barding dansile, usual, depict geomologie the metals and in submignification engaglished, south depict geomologie the merganished in the consistence of the south of the consistence of the consistence of the south of the consistence of t

n gynggilung Lightanni, tygl i it comu um fil nair Etungsama fi Gallalanni, Gugurum'i i um fil nair; Canglions guluplanni, rugʻunfulkani; Cangrouwan guluplanni, curinsam alibani)

व्यक्ति स्वत्राद्ध सुर्वेश्वय

About Autonomous

Sri Manakula Vinayagar Engineering College has been conferred with Autonomous Status by the University Grants Commission on 26³ September 2019 and the same was approved by Pondicherry University on 19³ June 2020. SMYEC Autonomous Regulations R2019; is followed for the students admitted in the Academic Year 2020-21. SMVEC Autonomous Regulations R2020, is followed for the students admitted from the Academic Year 2021-22.

HIGHLIGHTS OF SMVEC AUTONOMOUS REGULATIONS 2020

- Tamil, Hindi and French as options in Modern Indian Language
- Industry 4.0 ready curriculum
- Skill enhancement courses in every semester
- Multidisciplinary curriculum with open elective courses
- Courses on entrepreneurial development
- ◆ TELTS /IAS / entrance exams coaching
- Declaration of results within a month after completion of examinations
- ♦ Employability Enhancement Course

The Institute has Established 17 Centers of Excellence to provide 91 International Certification courses from IBM. Geogle, Cisco, E Plan, Microsoft, Antodesk, Texas instruments, Festo, Bentley, Schneider Electric, Annazon web services, Siemens, Tally, DELL EMG³, Harita Techsery, PTC, LN an Excellence in Technology & Didactic solutions, All the students should enroll in the certification course in every semester

Industrial Training / Internship

Students may undergo training or internship during summer/winter vacation at Industry? Research organization, students are also permitted to undergo internships during their sixth semester after the theory classes are over

Date	Day	Schedule	Working day Holiday
1	Fri		4 ===
2	Sat		
3	Sun		Holiday
4	Man		
5	Tue		
6	Wed		
7	Thu		
В	Fri		
9	Sat		
10	Sun		Holiday
11	Mon		
12	Tue		
13	Wed		
14	Thu	Tamil New Year / Dr. Ambedkar Birthday	Holiday
15	Fri	Good Friday	Holiday
16	Sat	***************************************	
17	Sun		Holiday
18	Mon		
19	Tue		
20	Wed		
21	Thu		
22	Fri	· · · · · · · · · · · · · · · · · · ·	
23	Sat		
24	Sun		Holiday
25	Mon		
26	Tue		
27	Wed		
28	Thu		
29	Fri	·	
30	Sat		*****
31	Sun		Holiday
	k	Total number of working days : Total number of holiday ;	

Date	Day	Schedule	Working day Holkfay
1	Tue		76
2	Wed	······	77
3	Thu		78
4	Fri		79
5	Sat		80
6	Sun		Holiday
7	Mon	Model exam starts (5 units)	81
8	Tue		82
9	Wed		83
10	Thu		84
11	Fri		85
12	Sat	Model exam result analaysis and report submission	86
13	Sun		Holiday
14	Mon		87
15	Tue	Model practical starts	88
16	Wed		89
17	Thu	Model practical ends	90
18	Fri		***************************************
19	Sat		
20	Sun	2000	Holiday
21	Mon	Tentative End Semester Practical Examination Start	
22	Tue		
23	Wed		
24	Thu	Tentative End Semester Practical Examination Ends	
25	Fri	Study Holiday	***************************************
26	Sat		
27	Sun		Holiday
28	Mon		
29	Tue		
30	Wed	Tentative End Semester Theory Examination Starts	***********
31	Thu		***************************************

வெற்றி என்று, வட்சியத்தைப் பழம்முயாகப் பூர்த்து கொள்வது – கூறப்றங்கேல்

Supplementary Examinations

Supplementary examination is an additional examination conducted within a month of nine after declaring the results. In order to complete the program within 3 years, only the student with maximum of two arrears will be permitted to appear for supplementary examination.

- More number of students will receive the degree within the stipulated time
- The industries prefer to recruit students having nil arears. If the supplementary examinations are conducted, then more number of students will be eligible for the recruitment.

Photo copy of answer book

After the publication of the result, photocopy of the answer books shall be provided to the student on request with stipulated fee fixed by the College from time to time

Punctuality in Attendance

The students are requested to keep up punctuality in attending the college. The late course will be losing their attendance and the internal marks. Hence all the students are requested to attend the college on time. A student shall be permitted to appear for the End Semester Examination at the end of the semester only if the she secures 75% of overall attendance.

Repeating the Course
A student who secures overall attendance which is less than 60% has to repeat the course with the approval from the Academic section of the institution.

Mentor Mentee System

In this system a team of students are allotted to a mentor and he/she will be mentoring these students.

				cueme in	Continuou	3 VF 23 E	Symi	Left I	6.34				
			14	Co	ntinuous As	sessin	ent (count	omponents				
S. No	Cour		Test Marks	Average of maths for proposit text vive for each experiment	Average of marks for caparing a caparing a caparing a caparing a caparing caparing a cap	Model Exam? Report	Assignment	Review - 1	Review-2	Review-3	Attendance	Total Marks	
1.	Theory		15			-	5	-	-		5	25	
2.	Practica		٠	10	1.5	15	٠	•	ŀ	•	10	50	
3.	Project v	vork	-	-			-	10	10	20	-	40	
C	XT I to 2			5	4		2	Lain		1	50		
Test Type		1	1.50	larks	s 5 Marks		10 Marks			Total Marks			
		du			(open c								
M	odel exam	201	11-11			Question Pattern				75			
	Table (b)	Que	stion	paperpat	terns for En	d sens	ester	Exai	ulnat	lon (ESE)		
1	Marks	T		5 Mark	\$		0 Ma			Tota	Ma	rks	
	10	(0	nie qi	5 restina from	each unit)	3 (out of 5) (open choice) 75			75				
E	ad Seme	ster l	Exan	unation Q	uestion Pap	er Pat	tern	for S	ix U	iits C	ours	es	
	urse	2M		52	Mark		8/9	Ma	k	1	istal M	lak	
Part A				(out of 3 questions, one from each unit)		(out	of 2		tion stions nit II)		37	1	
				-1.4		(com	puls	que ory q	nesti	on			
Pau	TB	5	7		2 questions, (each unit)	(out	of 3		stion stion		38		

The internal marks will be provided fully based on the continuous assessment

Date	Day	Schedule	Working day Holiday	
1	Tue	CAT - II Result Analysis and Report submission	52	
2	Wed	5-18-18-18-18-18-18-18-18-18-18-18-18-18-	53	
3	Thu	1 2	54	
4	Frí		55	
5	Sat	Special coaching class I GP I Seminar I GL I Placement I Academic Activities	56	
6	Sun		Holiday	
7	Mon		57	
8	Tue		58	
9	Wed		59	
10	Thu		60	
11	Fri	Field trip / Industrial Visit	61	
12	Sat	Special coaching class / GP / Seminar / GL / Placement / Academic Activities	62	
13	Sun		Holiday	
14	Mon		63	
15	Tue		64	
16	Wed		65	
17	Thu		66	
18	Fri		67	
19	Sat	Assignment - II	68	
20	Sun		Holiday	
21	Mon	Feedback from the students & Analysis	69	
22	Tue	QCM meeting 3 / Report Submission	70	
23	Wed		71	
24	Thu		72	
25	Fri	***************************************	73	
26	Sat		74	
27	Sun		Holiday	
28	Mon	Submission of attendance and Assessment Term - 3	75	
**********	***************************************	Total number of working days : 24 Total number of holiday : 04	***************************************	

Larrenant	2022

Date	Day	Day Schedule Y	
1	Sat	New Year	Holiday Holiday
2	Sun		Holiday
3	Mon		31
4	Tue	CAT - I Result Analysis and Report submission	32
5	Wed		33
6	Thu		34
7	Fri		35
8	Sat	Special coaching class / GP / Seminar / GL / Placement / Academic Activities	36
9	Sun		Holiday
10	Mon		37
11	Tue		38
12	Wed		39
13	Thu	Bhogi	Holiday
14	Fri	Pongal	Holiday
15	Sat	Thiruvalluvar Day	Holiday
16	Sun	Kaanum Pongal	Holiday
17	Mon		40
18	Tue		41
19	Wed		42
20	Thu	Assignment - II	43
21	Fri	Feedback from the students & Analysis	44
22	Sat	QCM meeting 2 / Report Submission	45
23	Sun		Holiday
24	Mon	CAT - II starts (1 % units)	46
25	Tue		47
26	Wed	Republic Day	Holiday
27	Thu		48
28	Fri	A company of the comp	49
29	Sat		50
30	Sun		Holiday
31	Mon	Submission of attendance and Assessment Term - 2	51
		Total number of working days: 21 Total number of holiday: 10	

Canbaygie, Lexan Galligy Chardwy spidiuddor. Librara Ighiga Cardinggania gaidhri

		eightage of Av	sessment fo	or Theory Course	\$		
S.No.	Test	Portion for Test	Total Marks	Duration of Test	Weightage for Internal Marks		
1	CAT-1	1 ½ unit	50	1 % hours			
2	CAT-2	1 1/2 unit	50	1 ½ hours	10		
3	Model	All 5 units	75	3 hours	5		
_	Continuous Assessment for Theory courses						

Distribution of Attendance marks for theory : 5 marks

The distribution of 5 marks for theory class attendance is as follows: 5 marks for 95% attendance and above

4 marks for 90% attendance and above but below 95%

3 marks for 85% attendance and above but below 90% 2 marks for 85% attendance and above but below 85% 1 mark for 75% attendance and above but below 80%

Distribution of Attendance marks for practical: 10 marks

The distribution of 10 marks for practical class attendance is as follows: 10 marks for 95% attendance and above

8 marks for 90% attendance and above but below 95%

5 marks for 35% attendance and above but below 90% 4 marks for 35% attendance and above but below 85% 2 marks for 75% attendance and above but below 80%

Students should not be absent for the classes. Attendance for the classes are monitored regularly and recorded. Continuous assessment mark will be based on the performance of the students in the continuous assessment test, Assignments and attendance percentage.

Assignments

Out of 25 (commuous assessment marks), 5 Marks will be awarded for the assignment. The students have to submit 3 assignments in each subject. The assignment questions will be different for each and every student.

Importance of Continuous Assessment marks

The continuous assessment marks once earned are carried over to the subsequent exams also. Hence the students are requested to work hard to get the maximum of the continous assessment marks are lower, it will pull down chances of getting the first class, distinction, gold metals and runks.

Importance of CAT-I/CAT-II/Model exam

Continous assessment marks are awarded for the performance in the CAT-I, CAT-II, Model exam. Hence all the students are requested to prepare well for each test/examination to earn the maximum continuous assessment marks.

Undertaking Minor / Major Projects

Each student is advised to take atleast one minor project. Getting involved in the projects will help to understand the basics of the course. Moreover, the Management awards eash prizes for the best projects in each department.

Participation in the Curricular / Co-curricular / Extra curricular Activities

All the students are encouraged to participate in the curricular / co-curricular / extra curricular activities. Involvement in these activities will improve their knowledge level in the subject. If a student or a team gets cash prize / award at the technical event organised by the recognised institutions, then the management of this institution will also sanction an amount equivalent to the award / cash prize as a token of appreciation.

Leave Account Record

For each student, leave account record is provided. The students are instructed to show the leave record to their parents and strictly adhere to the instructions given for availing the leave. The leave account record with price approval from the IROD must be maintained properly. In exceptional cases, the students are permitted to get the approval after availing the leave.

Transport Facility

56 buses are arranged for the students to reach the college from Puducherry, Kanayashetdadan, Vilhpuman, Noyveli, Pannai, Cuddalore, Vellihuppum, Mashkarai and Tindivanam covering almost all the areas. Separate transport facility is arranged for the students who remain in the college after 5 pm. for withing computed tab, library and sports facilities. The students are requested to utilise the transport facility.

All the students are requested to avoid mobile phones. Students who come by two wheelers must wear belinet.

	r 2021

Date	Day	Schedule	Working day: Holiday
1	Wed		5
2	Thu		6
3	Fri		7
4	Sat	Induction programs ends	8
5	Sun		Holiday
6	Mon	Regular classes starts	9
7	Tue		10
8	Wed		11
9	Thu		12
10	Fri		13
11	Sat		14
12	Sun		Holiday
13	Mon		15
14	Tue		16
15	Wed		17
16	Thu		18
17	Fri	Field trip / Industrial Visit	19
18	Sat	Special coaching class / CP / Seminar / GL / Placement / Academic Activities	20
19	Sun	0.0000000000000000000000000000000000000	Holiday
20	Mon		21
21	Tue		22
22	Wed	Assignment - I	23
23	Thu	Feedback from the students & Analysis	24
24	Fri	QCM meeting 1 / Report Submission	25
25	Sat	Chirtmas	Holiday
26	Sun		Holiday
27	Mon	CAT - I starts (1 % units)	26
28	Tue		27
29	Wed		28
30	Thu		29
31	Fri	Submission of attendance and Assessment Term - 1	30

Total number of working days: 26 Total number of holiday: 05

Date	Day	Schedule	Working day Holiday
1	Mon	•	
2	Tue		
3	Wed		
4	Thu	Deepavali	Holiday
5	Fri		Holiday
6	Sat		Holiday
7	Sun		Holiday
8	Mon	1	
9	Tue		
10	Wed		
11	Thu		
12	Fri		
13	Sat		
14	Sun		Holiday
15	Mon		
16	Tue		
17	Wed		
18	Thu		
19	Fri	1	
20	Sat		
21	Sun		Holiday
22	Mon		****
23	Tue		
24	Wed		
25	Thu	I year Induction Day	
26	Fri	AICTE Induction programs starts	1
27	Sat		2
28	Sun		Holiday
29	Mon		3
30	Tue		1 4
	L	Total number of working days: 04 Total number of holiday: 01	

		Place	ment and	Training	Division		
ontact wi	ith reputes ating vari	d andtinat	ional con portunitie	panies, in	dustries a	t the year and plays number of	an impor
		Activ	tles of the	Training	Division	ì	
Assist Arrang Create	s the stud- ges indust s awaren	ents to get rial visits ess on the og classes	in-plant opportuni	training ties open , GRE, TO	for higher	ll develope r studies. TS, LAS, II	
Academic	2014-15	2015-16	2016-17	2017-18	2018-19	2019-20	2020-21
Students Placed	95%	95%	93%	95%	95%	95%	96%
	[***************************************			2021 - 22]	
	Virtusa	echnolog	ies 27	Ami Onla	Technology	es 3	
	ZOHO			Tata Mo		1	
	TCS			Exelacon			
	Unisys		147		nstruneni		
	CTS			Fleation			
	Zucpi			UniSteel		5	
	Embedia		1	Mailam I	ndia	20	
	Econ		1	CSS		3	
	Wipto		124	Adroit S	oft	1	
	Musigm	3	46	Shiash I	afotech	6	
	DataMat	rix	2	Accentu	re	1	
	MeroChi	Technolog		MST Rel	3ac	18	
	Орро		7	Skolar A	cademy	13	
	Secure C	loud	6	ANZ Bak		1	
	Support	Studio	4	IIIS mark	æt	1	
	Capgeni		6	Kaar Tec		2	
	Eaton		1				
	Harman		3				
	Mindire		3			1	

Com Steams and tob ten teams	
Your seniors were sincere, hard working and got the Gold medals of	the Pondicherr
University and the top ten ranks in all the branches. The details of	f the Universit
Goldmedals and Top Ten Ranks won by the students are	given below.

A Indicates the Gold medal and University First Rank.

The Management awards 3 sovereigns of gold to the 1* rank holder, 2 sovereigns to the 2** rank holder, 1 sovereign to the 3** rank holder, 4** and 5** rank holders shall receive a cash award of Rs. 10,000 - (Rupees ten thousand) exchand 6** to 10** rank holders shall receive a cash award of Rs. 5,000 - (Rupees free thousand) exchand 6** to 10** rank holders, ball receive a cash award of Rs. 5,000 - (Rupees free thousand) each

Name of the	Year					
Course	2017	2018	2019			
Blich EEE	2,4,6,7	9	£23,467,89,10			
Black ECE	23,45,6,7,8,9,10		% 3.45,67.9.10			
Blich CSE	9 .2.3, 4, 10		8,2,4,6,7,8,10			
Black F	9 ,2,3,4,5,6,7,8,9,10	9	A 2,3,5,6,8			
BTach KE	,2,3,4,5,6,7,8,9,10	9	R 2,3,4,5,6,7,8,9,10			
Blich Medi	% A.5, 7, 9, 10		3,7,8,10			
BRds Civil	2, 3, 10		2.3,4,6,7,10			
MCA	3,4,7,9,10	*	£267.89.10.11			
MBA	\$ 3,4,6.7,8		\$23.457.810			
MToth CSE	9 2, 3, 4, 5, 7, 8, 9		W .7			
Mish ece	2, 3, 6, 7, 8, 9		2.3.4,5			
Mich PED	8		₩23			
Milech NW	R 2, 3, 4, 5, 7, 8, 9		% 23			
MicdelSD	Я		% ,2,3,4			
MTech(MF)	€ ,2		99			

Important points for the kind attention of the Parents

The I semester classes commenced on 26° November 2021. The students have to complete a let of work within a short period. Hence the parents are kindly requested not to permit their wards to avail frequent leave during this semester period for the following reasons.

It is compulsory for all the students to complete Certificate Courses, Skill Development Courses and Mandatory Courses along with their Academic Courses. These courses will enhance the students to upgrade their required skills to cope up with the Industry.

Marks in the continuous assessment test decide the major part of the continuous assessment marks. So, availing leave for the continuous assessment test must be avoided at any cost as this would seriously affect the assessment marks.

Practicals are very important not only to score more marks but also it will help to understand the theory part of the subject, hence advice your ward not to cut the practical classes.

Please spare your valuable time to talk to your son/daughter every day and try to understand what he/she is doing in respect of his/her studies. Kindly extend all your support to your son/daughter which will help them to come out successfully. For any assistance from our side you may always feel free to contact the respective DEAN at 0413-2642000 (extr) 2308.

WFFCampus

Our campus has been enabled by high speed uninterrupted Wi-Fi connectivity. The Computer Centre is open till 8,00 p.m. on all the working days except on the dates of University examinations.

Library Working Hours

8.30 a.m. to 8.30 p.m. (On all the working days)
8.30 a.m. to 10.00 p.m. (During the exemination days)

Women Cell
For the benefit of the girl students, a Women Cell has been constituted in the college. The girl students may approach the Chairperson / members for assistance.

Grievance Redressal Cell

There is a Grievance Redressal Cell under the Chairmanship of the Director of the institution. Students are requested to approach the Chairman / members to redress their grievances. Mail ID; grievance@smycc.ac.in

Annexure - IV





SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE
(An Autonomous Institution)
(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)
(Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution & Accredited by NAAC with "A" Grade) Madagadipet, Puducherry - 605 107



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF STUDENTS AND FACULTIES ENROLLED FOR NPTEL COURSE DURING THE ACADEMIC YEAR 2021 - 2022

PERIOD	DEPARTMENT	FACULTIES	STUDENTS
JAN-APR 2022	EEE	16	111
	Total Enrollment	12'	7

SRI MANAKULA VINAYAGAR ENGINEERING COLLEGE (An Autonomous Institution) (Approved by AICTE, New Delhi & Affiliated to Pondicherry University) (Accredited by NBA-AICTE, New Delhi, ISO 9001:2000 Certified Institution & Accredited by NAAC with "A" Grade)



Madagadipet, Puducherry - 605 107





DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

List of faculty enrolled for NPTEL during the academic year 2021-2022

S. No	Name	Designation	Name of the course registered
1	Dr. S.Anbumalar	Professor & Head	
2	Dr.P.Jamuna	Professor	
3	Dr.D.Raja	Professor	
4	Dr.K.Gowrishankar	Professor	
5	Dr.G.GaneshKumaran	Associate Professor	
6	Dr.M.Jayachandran	Assistant Professor	
7	Mr.A.Janagiraman	Assistant Professor	NBA Accreditation & Teaching And Learning in
8	Mr.K.Thangaraj	Assistant Professor	Engineering
9	Mr.J.Muruganandam	Assistant Professor	[12 Weeks]
10	Mr.C.Adrien Perianayagam	Assistant Professor	
11	Mr.R.Ragupathy	Assistant Professor	10 2 AH 32
12	Ms.N.Swarnalakshmi	Assistant Professor	To your stress as
13	Mr.R.Nakkeeran	Assistant Professor	
14	Mr.D.Sivaraj	Assistant Professor	
15	Ms.R.Aarthi	Assistant Professor	
16	Mr.I.Shivashankar	Assistant Professor	Effective Engineering Teaching in Practice [4 Weeks]

List of students enrolled for NPTEL during the academic year 2021-2022

S. No.	Name	Sem	Sec	Name of the course	
1	ABDUL RAZAAK. A	V	Α	Microprocessor and Microcontroller	
2	ABIRAAMI. V	V	Α	Microprocessor and Microcontroller	
3	AKASH. S	V	В	Microprocessor and Microcontroller	Direction in
4	AKSHAYA S	V	В	Ethical Hacking	- December 2
5	ALASHFAK M	V	В	Microprocessor and Microcontroller	Managara I
6	ANAND M V	V	В	Microprocessor and Microcontroller	Alaska da da
7	ANJANA. V	V	Α	Microprocessor and Microcontroller	Was and
8	ANNAMALAI. E	V	Α	Microprocessor and Microcontroller	Analog Circuits
9	ARAVIND. G	V	Α	Microprocessor and Microcontroller	
10	ARAVINDA. C	V	Α	Microprocessor and Microcontroller	Padara Idal
11	ARAVINDHAN. A	V	В	Microprocessor and Microcontroller	dental de la constant
12	ARIPRASATH. N	V	Α	Microprocessor and Microcontroller	
13	ARTHI. A	V	Α	Microprocessor and Microcontroller	NAC
14	ARULEESWARAN.P	V	В	Microprocessor and Microcontroller	Culti-
15	ARUNACHALAM.T	V	Α	Microprocessor and Microcontroller	
16	ARVIND.D	V	В	Microprocessor and Microcontroller	

17	ASMA BEGAM. M	V	В	Microprocessor and Microcontroller	
18	BHUVANESH I	V	Α	Microprocessor and Microcontroller	1.
19	BOMIDI PUJITHA	V	В	Microprocessor and Microcontroller	
20	DEEPA PRAGASAN. V	V	Α	Microprocessor and Microcontroller	
21	DEEPIKA. V	V	Α	Microprocessor and Microcontroller	
22	DEIVAPRASATH.A	V	В	Microprocessor and Microcontroller	
23	DELLI BABU. S	V	В	Microprocessor and Microcontroller	
24	DEVANATHAN. J	V	Α	Microprocessor and Microcontroller	
25	DHILIPKUMAR. S	V	Α	Microprocessor and Microcontroller	. 1
26	DHINESHE. S	V	Α	Microprocessor and Microcontroller	
27	DHIVYADARSHNI. V	V a	Α	Microprocessor and Microcontroller	
28	DILEEP PRASATH K	V	В	Microprocessor and Microcontroller	1,00
29	DINESH BABU.A	V	Α	Microprocessor and Microcontroller	
30	DINESH KUMAR. M	V	В	Microprocessor and Microcontroller	
31	GOGULARAJ.V	V	Α	Microprocessor and Microcontroller	
32	GOKULRAJ N	V .	В	Microprocessor and Microcontroller	
33	GOKUL S	V	Α	Microprocessor and Microcontroller	4 7
34	GUGAN. M	V	Α	Fundamental of Power Electronics	
35	GUNAVATHI S	V	В	Microprocessor and Microcontroller	
36	HARIHARAN S	V	В	Microprocessor and Microcontroller	v.a. 🔻
37	HARI PRASAD K	V	Α	Microprocessor and Microcontroller	
38	HARISH. A	V	Α	Microprocessor and Microcontroller	
39	HEMALATHA A	V	Α	Microprocessor and Microcontroller	
40	HEMALATHA. V	V	В	Microprocessor and Microcontroller	Ethical hacking
41	HEMAMAALAN. C	V a	В	Microprocessor and Microcontroller	sters thou 1779
42	ISRAK HUSSAIN. S	V	. В	Microprocessor and Microcontroller	ball of the
43	IYYAPPAN. M	V	Α	Microprocessor and Microcontroller	
44	JAGADHEESAN. P	V	В	Microprocessor and Microcontroller	
45	JAGAN.P	V	Α	Microprocessor and Microcontroller	manufacture of the second
46	JAI GANESH. J	V	Α	Microprocessor and Microcontroller	
47	JAWAHAR. G	V	В	Microprocessor and Microcontroller	111
48	JEEVAJOTHI. K	V	В	Microprocessor and Microcontroller	
49	JENSY ALBIYA. A.J.	V	В	Microprocessor and Microcontroller	
50	KARTHIK. R	V	В	. Microprocessor and Microcontroller	
51	KHAJA MOIDEEN S	V	В	Microprocessor and Microcontroller	1,
52	KIRUTHIGA. C	V	Α	Microprocessor and Microcontroller	
53	KISHOR. G	V	Α	Microprocessor and Microcontroller	
54	KISHORE.D	V	В	Microprocessor and Microcontroller	
55	KUMARAN. S	V	Α	Microprocessor and Microcontroller	
56	LOGANATHAN S	V	В	Microprocessor and Microcontroller	
57	LOGESHWARAN. V	V	Α	Microprocessor and Microcontroller	e, 12 - MEVI
58	LOKESWARI. G	V	В	Microprocessor and Microcontroller	* * * * * * * * * * * * * * * * * * *
59	MESHACH. E	V	В	Ethical Hacking	Berthall -
60	MOHAMED FAWAZ.Y	V	Α	Microprocessor and Microcontroller	
61	NANDHINI.C	V	- A	Microprocessor and Microcontroller	-14-1257
62	NARENDIRAN.A	V	В	Microprocessor and Microcontroller	
63	NIVETHITHA. R	V	Α	Microprocessor and Microcontroller	
64	NIVETHITHAASRI. P.R.	V	В	Microprocessor and Microcontroller	Jan Bay Congress of The
65	OUMAR GOURU. O	V	A	Microprocessor and Microcontroller	
66	PALEPU SHIVA	V	В	Microprocessor and Microcontroller	

PRIYADHARSHAN. S PRIYENGA.E RAJARAJAN. D RAJMUGILAN. R RASIN A RATHINASABAPATHY A SAKTHI ESWARAN.S SANCHUNA .S SANJAY. M SANJAY. M SARAVANAKRISHNAN. V	V V V V V V V V V V V V V V V V V V V	B A A B B B B B	Microprocessor and Microcontroller	
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SATHYANARAYANAN.V	V	Α	Microprocessor and Microcontroller	1100 100
SHARAN. S	V	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
SINDHUJA. K	V	Α	Microprocessor and Microcontroller	
SIVABALAN S	V	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
SIVARAMAN P	V	Α	Microprocessor and Microcontroller	-
SOWMYA S	٧	В	Fuzzy Sets, Logic and Systems & Applications	Microprocessor and Microcontroller
SUNIL KUMAR. M		В,	Microprocessor and Microcontroller	
SURENDAR.G	V	Α	Microprocessor and Microcontroller	
SURYAPRAKASH. P	V	В	Microprocessor and Microcontroller	- i k
THAMARAISELVAN. S	V	В	Renewable Energy Engineering: Solar, Wind and Biomass Energy Systems	Microprocessor and Microcontroller
THAMIZHSELVAN. G	V	В	Microprocessor and Microcontroller	
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THANYASRI.S.K	V	Α	Microprocessor and Microcontroller	
THIRUMANIRAJ. P	V	Α	Microprocessor and Microcontroller	
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VASANTHAKUMAR. R	V	В	Microprocessor and Microcontroller	2 - 1
VETRIVEL.V	V	Α	Microprocessor and Microcontroller	
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VIGNESH. K	V	В	Microprocessor and Microcontroller	PS 10-16 1 256 4- 2 1 16-
VIGNESHWARAN.V	V	- A	Microprocessor and Microcontroller	
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YOGESH. R	V	Α	Microprocessor and Microcontroller	
YOGESHWAR S	V	Α	Microprocessor and Microcontroller	
ATHMAJAN.S	V	В	Microprocessor and Microcontroller	
DHANUSH.N	٧	В	Microprocessor and Microcontroller	Power System Engineering
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Annexure - V

List of Examiners





(An Autonomous Institution)
(Approved by AICTE, New Delhi & Affiliated to Pondicherry University)
(Accredited by NBA-AICTE, New Delhi & Accredited by NAAC with *A* Grade)
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